

GSTMMBTA06JZF

NPN Transistor

Product Description

This device is designed for a general-purpose application.

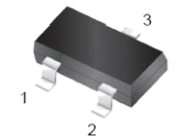
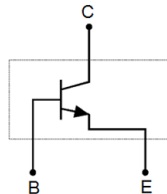
Features

- BV_{CE0} 80V

Mechanical Data

- SOT-23. Package
- RoHS Compliant and Halogen Free

Package and Pin Assignment

SOT-23		Equivalent Circuit	
			
Pin	Description	Pin	Description
1	Base	3	Collector
2	Emitter		

Ordering and Marking Information

GS P/N	Package	Marking	Quantity / Reel
GSTMMBTA06JZF	SOT-23	1GM	3,000PCS
GSTMMBTA06JZF			
- Product Code: GSTMMBTA06	- Package Code: JZ for SOT-23	- Green Level: F for RoHS Compliant and Halogen Free	
Marking Information			
- Product Code: 1GM			

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
V _{CEO}	Collector-Emitter Voltage	80	V
V _{CBO}	Collector-Base Voltage	80	V
V _{EBO}	Emitter-Base Voltage	4	V
I _C	Peak Collector Current	500	mA
P _D	Power Dissipation T _A =25°C*	300	mW
R _{θJA}	Thermal Resistance, Junction to Ambient*	416	°C/W
T _J	Junction Temperature Range	-55 to +150	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C

* Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Electrical Characteristics (T_A=25°C unless otherwise specified)

Symbol	Description	Conditions	Min	Max	Unit
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C =1mA, I _B =0mA	80	-	V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C =0.1mA, I _E =0mA	80	-	V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E =0.1mA, I _C =0mA	4	-	V
I _{CBO}	Collector Cutoff Current	V _{CB} =80V, I _E =0mA	-	100	nA
I _{CEO}	Collector Cutoff Current	V _{CE} =60V, I _B =0mA	-	1	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} =3V, I _C =0mA	-	100	nA
h _{FE}	DC Current Gain	I _C =10mA, V _{CE} =1V	100	-	-
		I _C =100mA, V _{CE} =1V	100	-	-
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C =100mA, I _B =10mA	-	0.25	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C =100mA, I _B =10mA	-	1.2	V
f _T	Transition Frequency	V _{CE} =2V, I _C =10mA, f=100MHz	100	-	MHz

Typical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

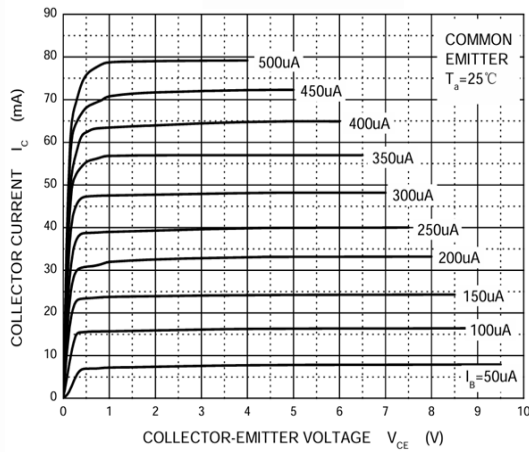


FIG 1. Static Characteristic

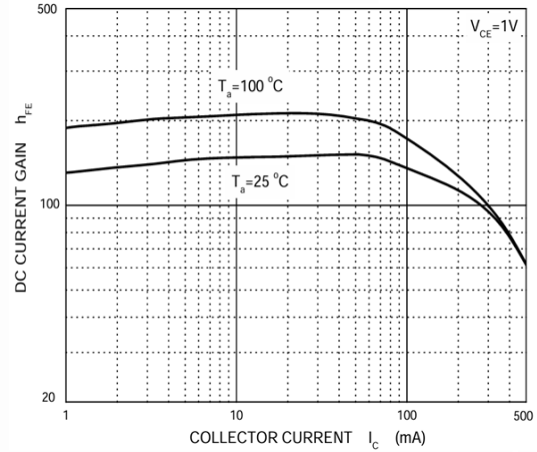


FIG 2. h_{FE} vs. I_c

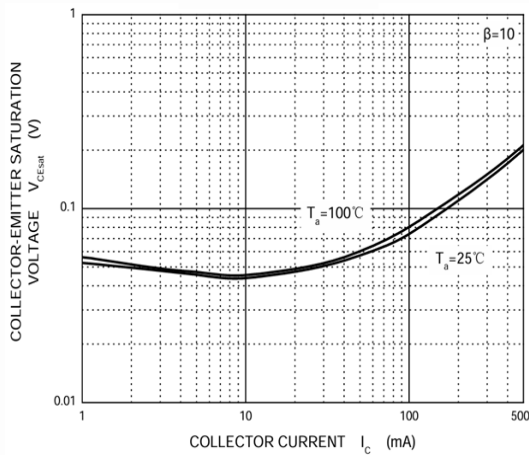


FIG 3. $V_{CE(SAT)}$ vs. I_c

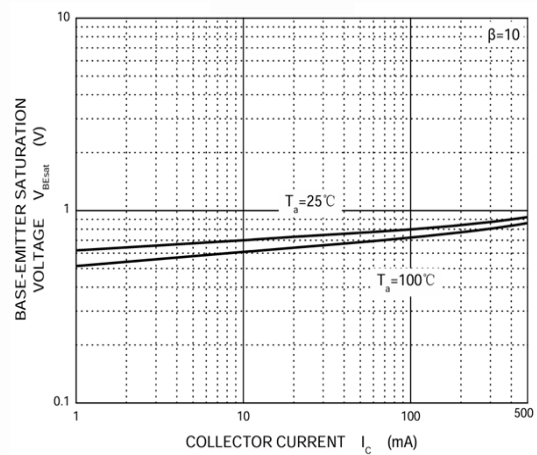


FIG 4. $V_{BE(SAT)}$ vs. I_c

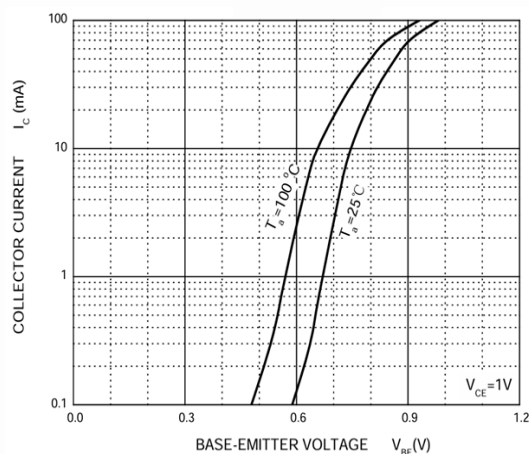


FIG 5. V_{BE} vs. I_c

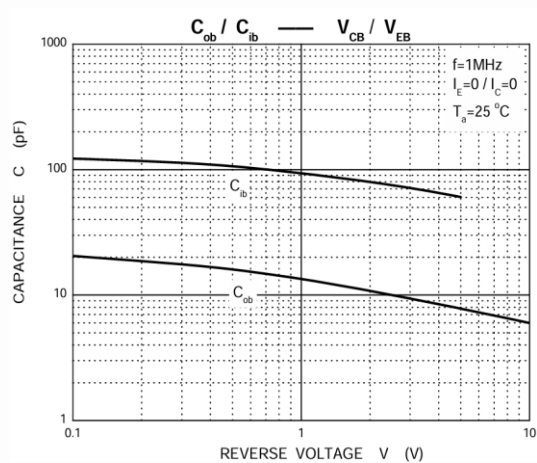


FIG 6. Capacitance Characteristic

Typical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

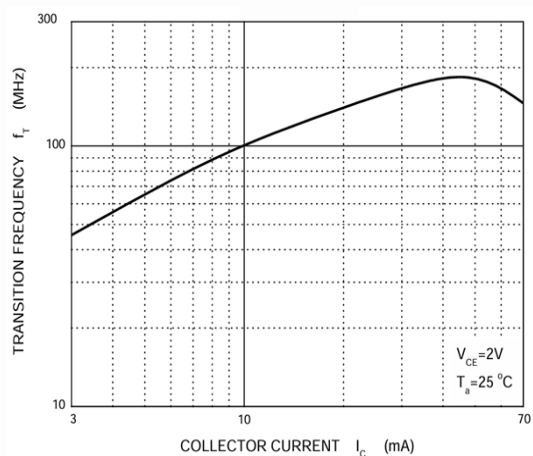


FIG 7. f_T vs. I_C

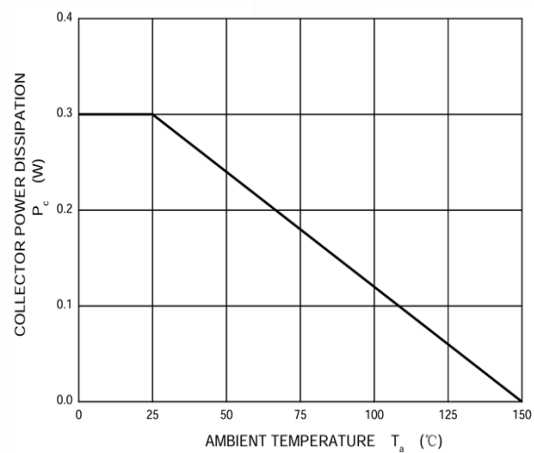
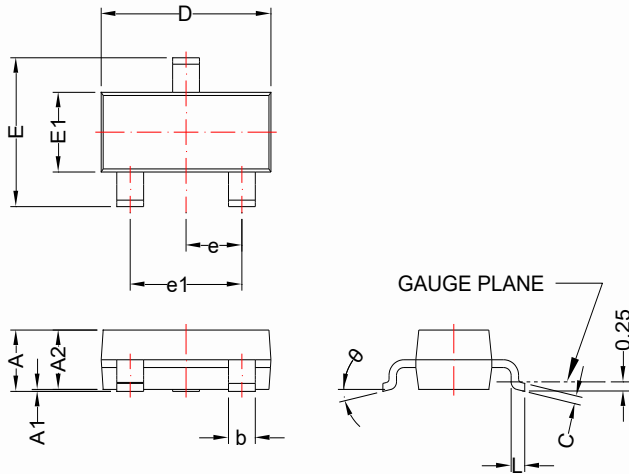


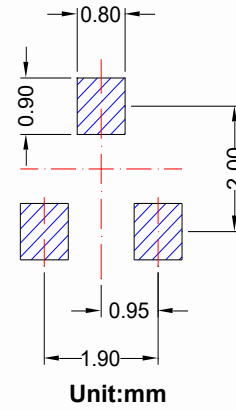
FIG 8. Power Derating Curve

SOT-23

Package Dimension



Recommended Land Pattern



Dimensions				
SYMBOL	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.75	1.20	0.030	0.047
A1	0.00	0.15	0.000	0.006
A2	0.70	1.10	0.028	0.043
b	0.30	0.60	0.012	0.024
c	0.08	0.20	0.003	0.008
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E1	1.20	1.40	0.047	0.055
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.2	0.6	0.008	0.024
θ	0°	8°	0°	8°

NOTE:

Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.

NOTICE

- Globaltech Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Globaltech Semiconductor products described or contained herein. Globaltech Semiconductor products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Globaltech Semiconductor makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- Information furnished is believed to be accurate and reliable. However Globaltech Semiconductor assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Globaltech Semiconductor. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information without express written approval of Globaltech Semiconductor.

CONTACT US

GS Headquarter	
	4F, NO.43-1, Lane 11, Sec. 6, Minquan E. Rd Neihs District, Taipei City 114761, Taiwan (R.O.C).
	886-2-2657-9980
	886-2-2657-3630
	sales_twn@gs-power.com

RD Division	
	824 Bolton Drive Milpitas. CA. 95035
	1-408-457-0587