

GSMDC4906Z

40V N-Channel MOSFETs

Product Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.


Features

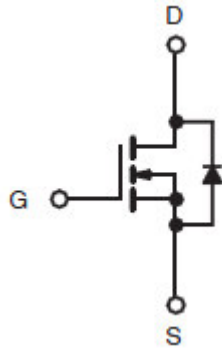
- 40V, 35 A, $R_{DS(ON)}=9\text{ m}\Omega@V_{GS}=10\text{V}$
- Improved dv/dt capability
- Fast switching
- 100% EAS guaranteed
- Green Device Available
- DFN3X3-8L package design

Applications

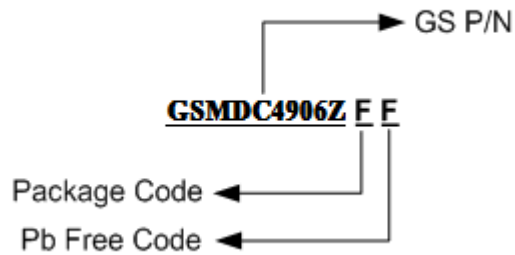
- Notebook
- Load Switch
- LED applications
- Hand-Held Device

Packages & Pin Assignments

GSMDC4906ZFF (DFN3X3-8L)	
 <p>Bottom View</p>	
Pin	Description
1	Source
2	Source
3	Source
4	Gate
5	Drain
6	Drain
7	Drain
8	Drain

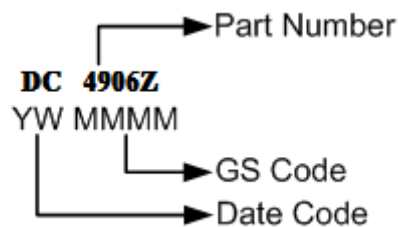


Ordering Information



Part Number	Package	Quantity
GSMDC4906ZFF	DFN3X3-8L	3000 PCS

Marking Information



Absolute Maximum Ratings

$T_C=25^\circ\text{C}$ Unless otherwise noted

Symbol	Parameter	Typical	Unit
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate –Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	35
		$T_C=100^\circ\text{C}$	22.1
I_{DM}	Pulsed Drain Current (Note 1)	140	A
P_D	Power Dissipation ($T_C=25^\circ\text{C}$)	44	W
	Power Dissipation (Derate above 25°C)	0.36	W/ $^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to +150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	2.8	$^\circ\text{C/W}$

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.

Electrical Characteristics

T_J=25°C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	40			V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA		0.03		V/°C
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	1.2	1.8	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient			-5		mV/°C
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V			1	uA
		V _{DS} =32V, V _{GS} =0V, T _J =125°C			10	
I _S	Continuous Source Current	V _G =V _D =0V, Force Current			35	A
I _{SM}	Pulsed Source Current				70	
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =10V, I _D =25A		7.4	9	mΩ
		V _{GS} =4.5V, I _D =12A		10.6	13.5	
g _{FS}	Forward Transconductance	V _{DS} =10V, I _D =2A		13		S
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =1A			1	V
Dynamic						
Q _g	Total Gate Charge (Note 2,3)	V _{DS} =20V, V _{GS} =10V, I _D =8A		19.7	30	nC
Q _{gs}	Gate-Source Charge (Note 2,3)			2.8	4.2	
Q _{gd}	Gate-Drain Charge (Note 2,3)			5.1	7.6	
C _{iSS}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz		1220	2200	pF
C _{oss}	Output Capacitance			130	250	
C _{rss}	Reverse Transfer Capacitance			55	110	
t _{d(on)}	Turn-On Time (Note 2,3)	V _{DD} =15V, I _D =1A, V _{GS} =10V, R _G =3.3Ω		13.2	25	ns
t _r				2.2	5	
t _{d(off)}	Turn-Off Time (Note 2,3)			72	130	
t _f				4.5	10	
R _g	Gate Resistance		V _{DS} =0V, V _{GS} =0V, f=1MHz		2.2	

Note 2: The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.

Note 3: Essentially independent of operating temperature.

Typical Performance Characteristics

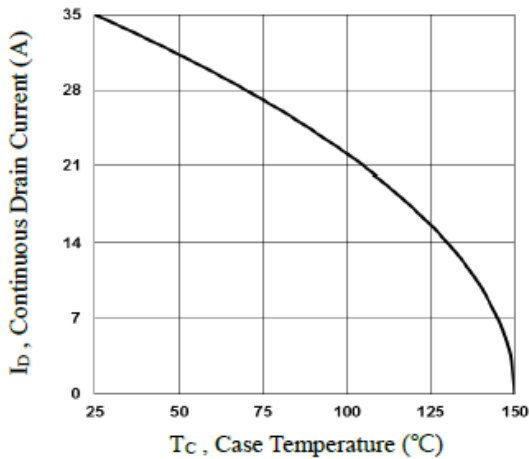


Fig.1 Continuous Drain Current vs. T_c

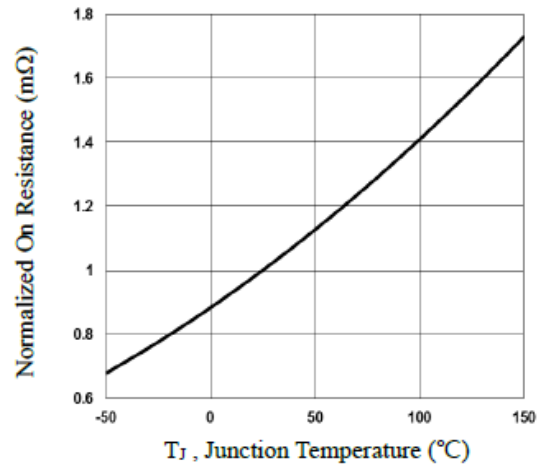


Fig.2 Normalized R_{DS(on)} vs. T_j

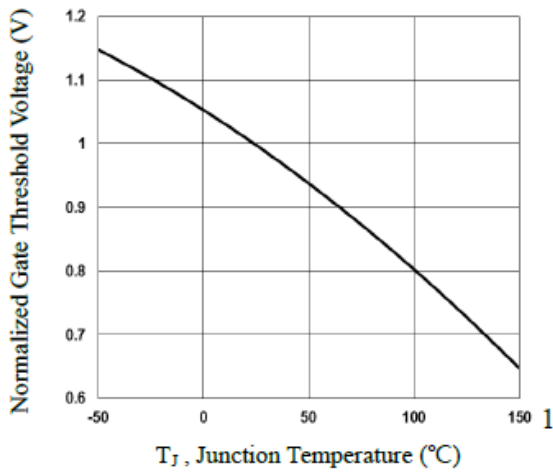


Fig.3 Normalized V_{th} vs. T_j

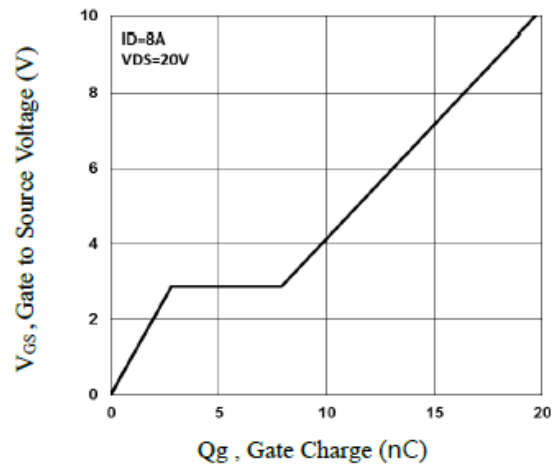


Fig.4 Gate Charge Waveform

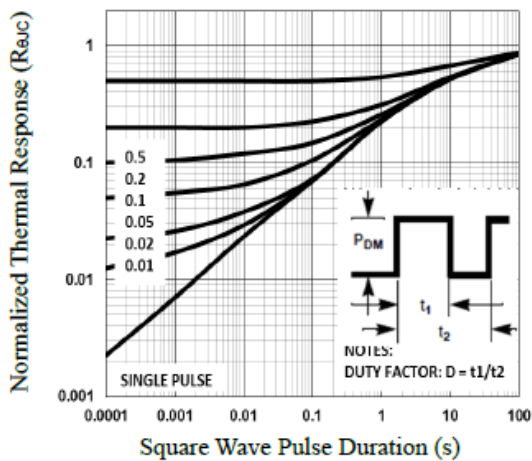


Fig.5 Normalized Transient Impedance

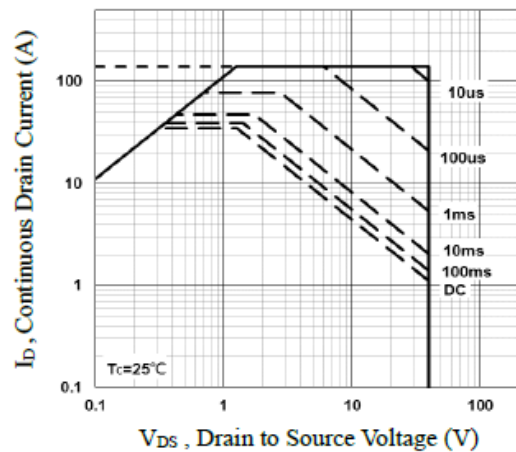


Fig.6 Maximum Safe Operation Area

Typical Performance Characteristics

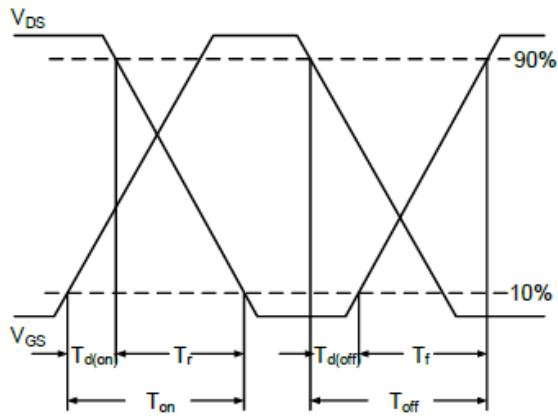


Fig.7 Switching Time Waveform

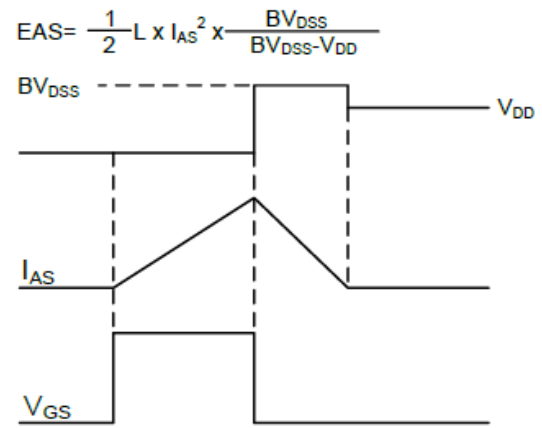
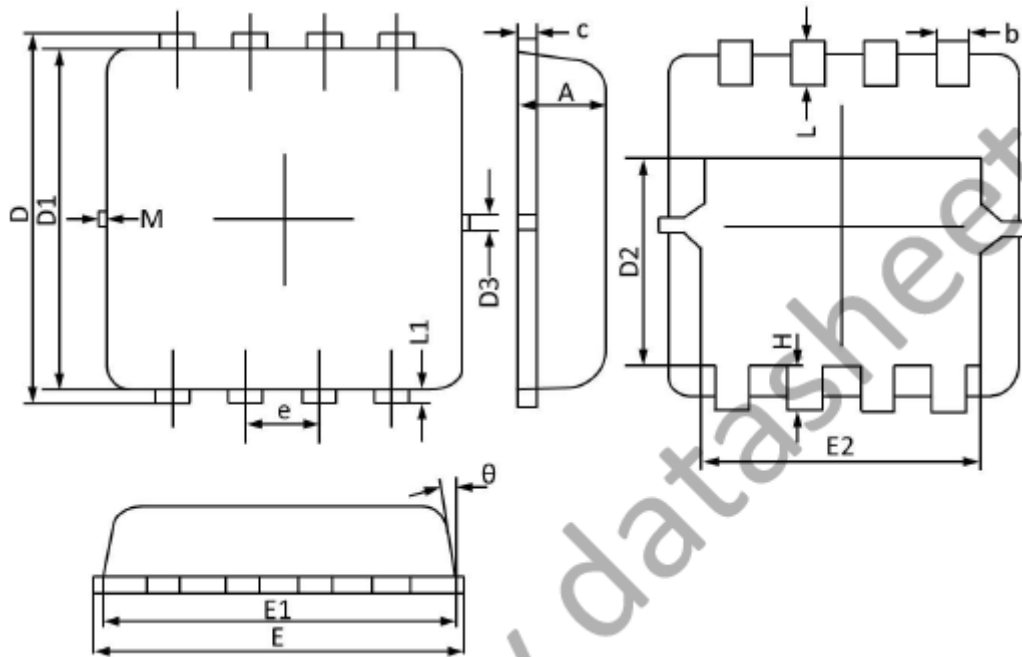


Fig.8 EAS Waveform

Package Dimension

DFN3X3-8L









Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
b	0.250	0.350	0.010	0.013
c	0.100	0.250	0.004	0.009
D	3.250	3.450	0.128	0.135
D1	3.000	3.200	0.119	0.125
D2	1.780	1.980	0.070	0.077
D3	0.130 REF		0.005 REF	
E	3.200	3.400	0.126	0.133
E1	3.000	3.200	0.119	0.125
E2	2.390	2.590	0.094	0.102
e	0.650 BSC		0.026 BSC	
H	0.300	0.500	0.011	0.019
L	0.300	0.500	0.011	0.019
L1	0.130 REF		0.005 REF	
θ	0°	12°	0°	12°
M	0.150 REF		0.006 REF	

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