

GSMDC4906X

40V N-Channel MOSFETs

Product Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

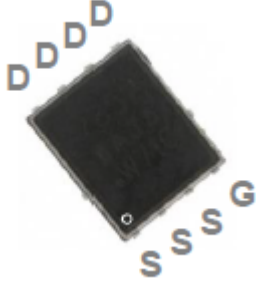
Features

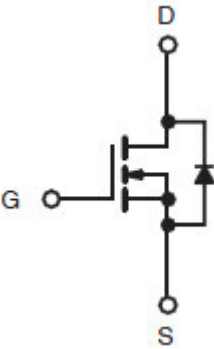
- 40V, 70 A, $R_{DS(ON)}=8.5\text{ m}\Omega@V_{GS}=10\text{V}$
- Improved dv/dt capability
- Fast switching
- 100% EAS guaranteed
- Green Device Available
- DFN3X3-8L package design

Applications

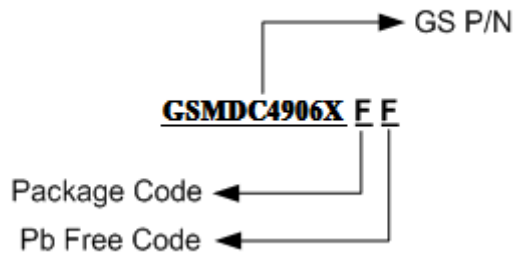
- Notebook
- Load Switch
- LED applications
- Hand-Held Device

Packages & Pin Assignments

GSMDC4906ZFF (DFN5X6-8L)	
 <p>Bottom View</p>	
Pin	Description
1	Source
2	Source
3	Source
4	Gate
5	Drain
6	Drain
7	Drain
8	Drain

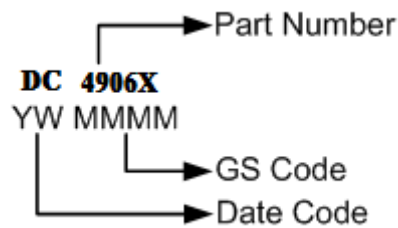


Ordering Information



Part Number	Package	Quantity
GSMDC4906XFF	DFN5X6-8L	3000 PCS

Marking Information



Absolute Maximum Ratings

$T_C=25^{\circ}\text{C}$ Unless otherwise noted

Symbol	Parameter	Typical	Unit
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate –Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_C=25^{\circ}\text{C}$	70
		$T_C=100^{\circ}\text{C}$	44
I_{DM}	Pulsed Drain Current (Note 1)	280	A
EAS	Single Pulse Avalanche Energy(Note 2)	76	mJ
IAS	Single Pulse Avalanche Current(Note 2)	39	A
P_D	Power Dissipation ($T_C=25^{\circ}\text{C}$)	72.3	W
	Power Dissipation (Derate above 25°C)	0.58	W/ $^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to +150	$^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to +150	$^{\circ}\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	1.73	$^{\circ}\text{C}/\text{W}$

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25\text{V}, V_{GS}=10\text{V}, L=0.1\text{mH}, I_{AS}=39\text{A}, R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$

Electrical Characteristics

$T_J=25^{\circ}\text{C}$ Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	40			V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$		0.03		$V/^{\circ}\text{C}$
$V_{GS(th)}$	Gate Threshold Voltage		1.2	1.6	2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient	$V_{DS}=V_{GS}, I_D=250\mu A$		-5		$\text{mV}/^{\circ}\text{C}$
I_{GSS}	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40V, V_{GS}=0V$			1	uA
		$V_{DS}=32V, V_{GS}=0V$, $T_J=85^{\circ}\text{C}$			10	
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current			70	A
I_{SM}	Pulsed Source Current				140	
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V, I_D=15A$		6.5	8.5	m Ω
		$V_{GS}=4.5V, I_D=8A$		9	12	
g_{FS}	Forward Transconductance	$V_{DS}=10V, I_D=2A$		13		S
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=1A$			1	V
Dynamic						
Q_g	Total Gate Charge (Note 3,4)			19.7	30	nC
Q_{gs}	Gate-Source Charge (Note 3,4)	$V_{DS}=20V, V_{GS}=10V$, $I_D=10A$		2.8	4.2	
Q_{gd}	Gate-Drain Charge (Note 3,4)			5.1	7.6	
C_{iss}	Input Capacitance			1278	2200	pF
C_{oss}	Output Capacitance	$V_{DS}=25V, V_{GS}=0V$, $f=1\text{MHz}$		135	250	
C_{rss}	Reverse Transfer Capacitance			87	170	
$t_{d(on)}$	Turn-On Time (Note 3,4)	$V_{DD}=15V, I_D=1A$, $V_{GS}=10V, R_G=3.3\Omega$		13.2	25	ns
t_r				2.2	5	
$t_{d(off)}$	Turn-Off Time (Note 3,4)			72	130	
t_f				4.5	10	
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V$, $f=1\text{MHz}$		2.2		Ω

Note 3: The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

Note 4: Essentially independent of operating temperature.

Typical Performance Characteristics

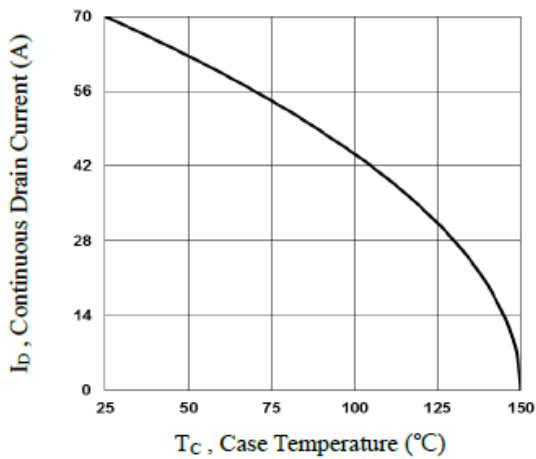


Fig.1 Continuous Drain Current vs. T_c

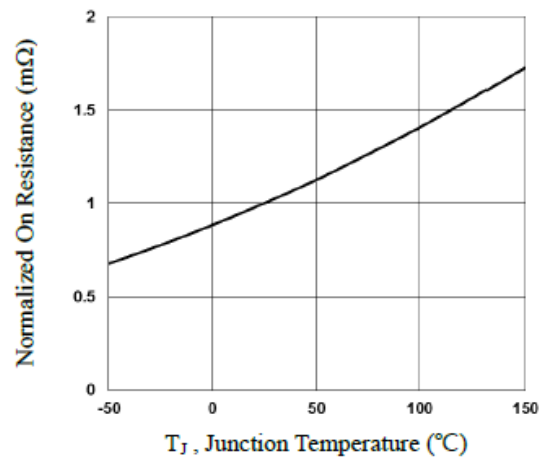


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

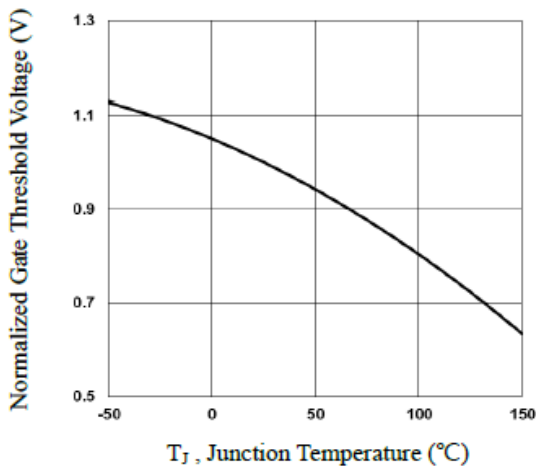


Fig.3 Normalized V_{th} vs. T_j

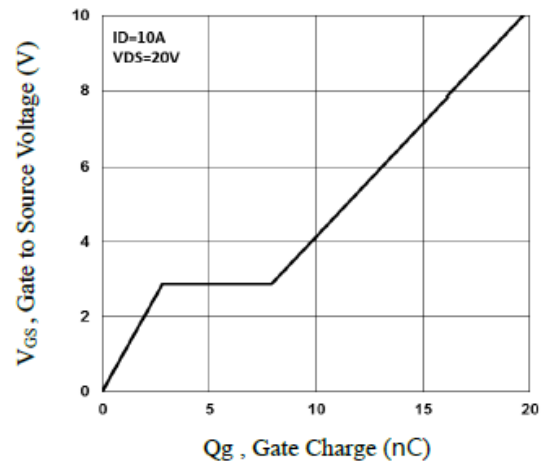


Fig.4 Gate Charge Waveform

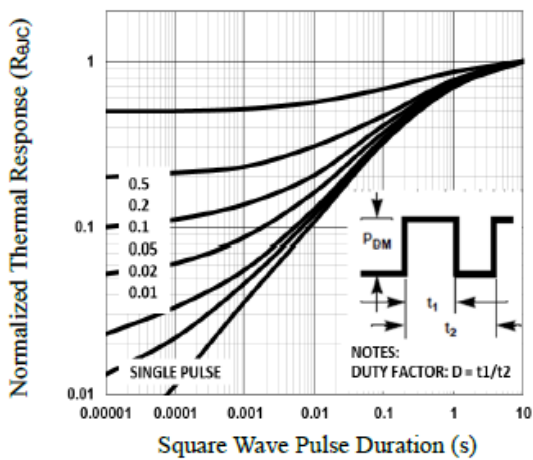


Fig.5 Normalized Transient Impedance

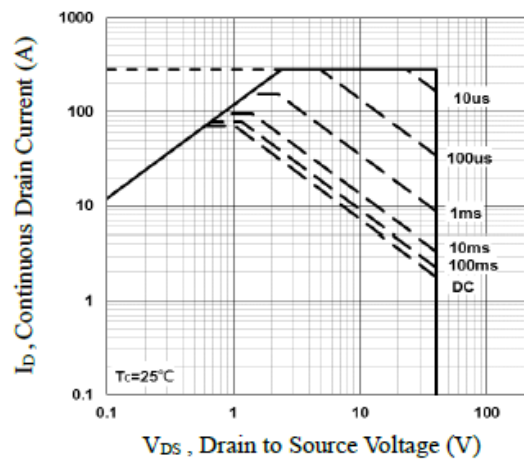


Fig.6 Maximum Safe Operation Area

Typical Performance Characteristics

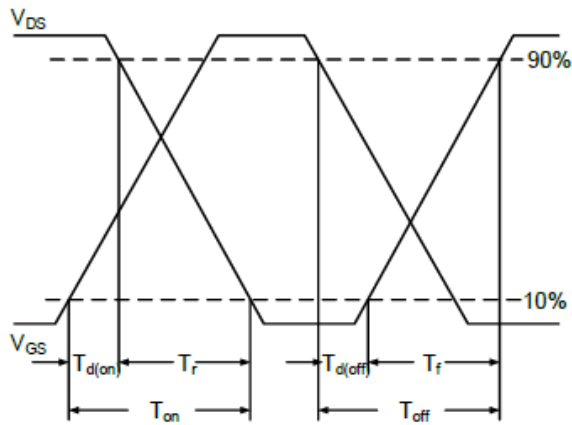


Fig.7 Switching Time Waveform

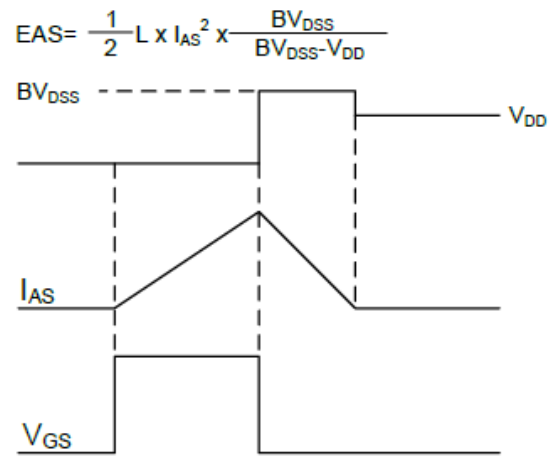
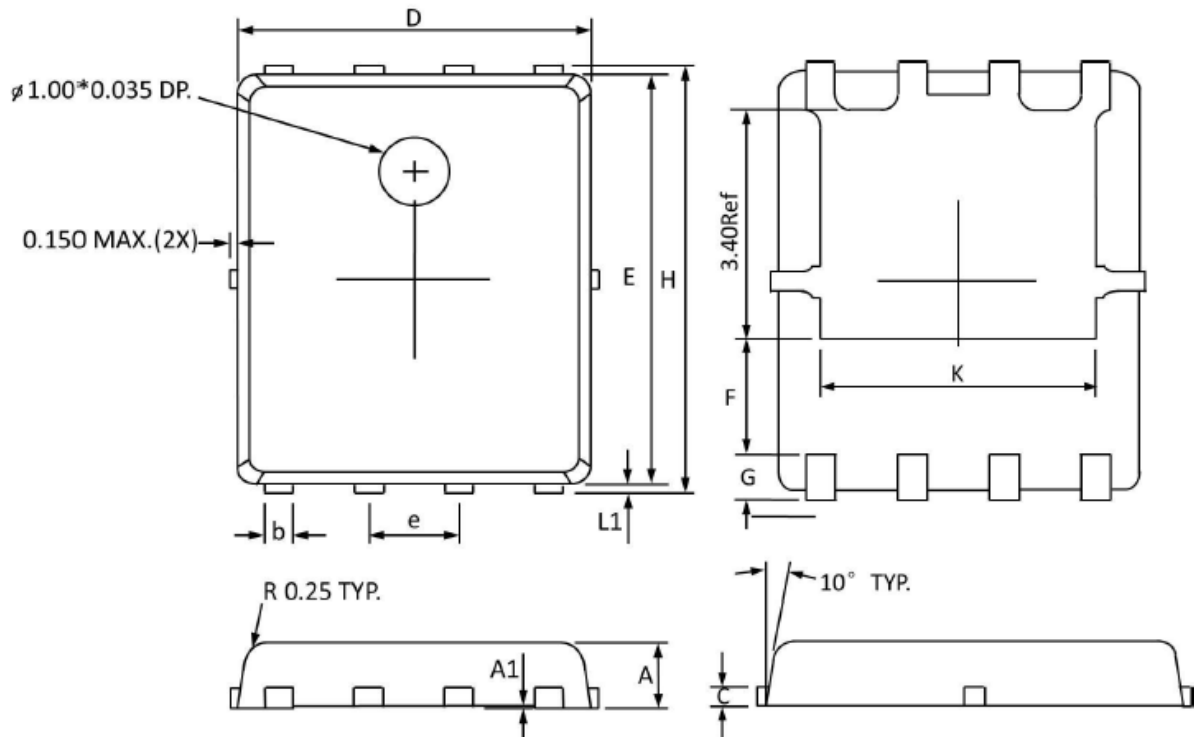


Fig.8 EAS Waveform

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

Package Dimension

DFN5X6-8L









Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.000	0.032	0.039
A1	0.000	0.005	0.000	0.000
b	0.350	0.490	0.014	0.019
C	0.254 Ref		0.254 Ref	
D	4.900	5.100	0.193	0.200
E	5.700	5.900	0.225	0.232
e	1.27 BSC		1.27 BSC	
F	1.400 Ref		1.400 Ref	
G	0.600 Ref		0.600 Ref	
H	5.950	6.200	0.235	0.244
L1	0.100	0.180	0.004	0.007
K	4.000 Ref		4.000 Ref	

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CONTACT US

GS Headquarter	
	4F.,No.43-1,Lane11,Sec.6,Minquan E.Rd Neihu District Taipei City 114, Taiwan (R.O.C)
	886-2-2657-9980
	886-2-2657-3630
	sales_twn@gs-power.com

RD Division	
	824 Bolton Drive Milpitas. CA. 95035
	1-408-457-0587