

G SMBSS139TFF

60V N-Channel MOSFETs

Product Description

These N-Channel Enhancement Mode Power Field Effect Transistors are Using Trench DMOS Technology. This Advanced Technology has been Especially Tailored to Minimize on-state Resistance, Provide Superior Switching Performance, and Withstand high Energy Pulse in the Avalanche and Commutation mode.

These Devices are well Suited for High Efficiency Fast Switching Applications.

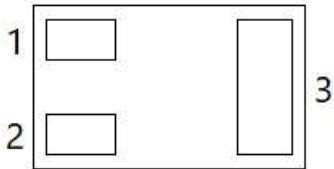
Features

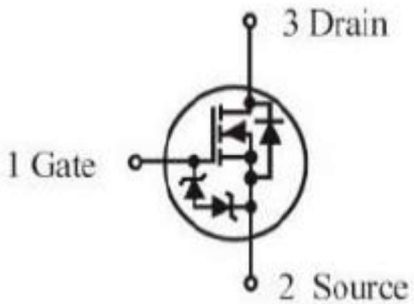
- 60V, 0.2A, $R_{DS(ON)}=2.5\Omega@V_{GS}=4.5V$
- Improved dv/dt Capability
- Fast Switching
- 100% EAS Guaranteed
- Green Device Available
- ESD Protected : 1500V

Applications

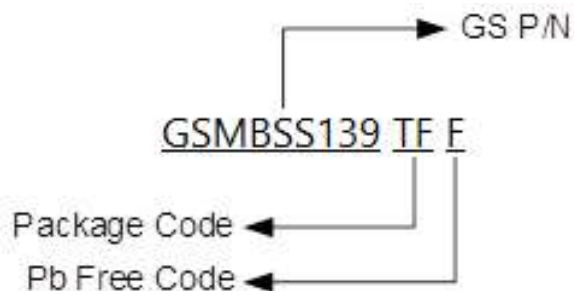
- Notebook
- Load Switch
- LED Applications

Packages & Pin Assignments

G SMBSS139TFF (DFN1006-3L)	
 <p>Transparent top view</p>	
Pin	Description
1	Gate
2	Source
3	Drain

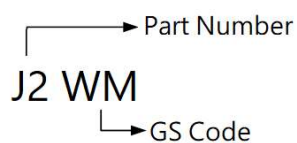


Ordering Information



Part Number	Package	Quantity Reel
GSMBSS139TFF	DFN1006-3L	10000 PCS

Marking Information



Absolute Maximum Ratings

T_A=25°C Unless otherwise noted

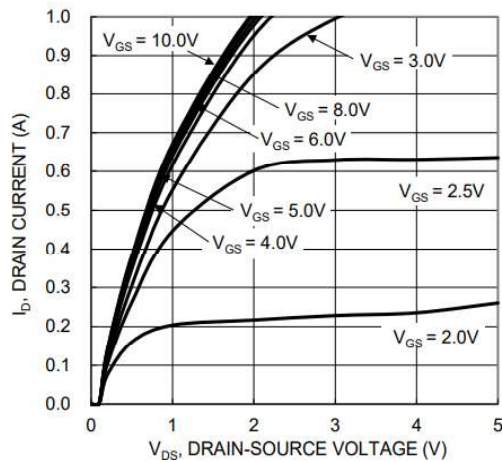
Symbol	Parameter	Limits	Unit
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Continuous Drain Current (T _A =25°C)	0.33	A
I _{DM}	Pulsed Drain Current	0.8	A
P _D	Power Dissipation (T _A =25°C)	0.36	W
T _J	Operating Junction Temperature Range	-55 to +150	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C
R _{θJA}	Thermal Resistance-Junction to Ambient	350	°C/W

Electrical Characteristics

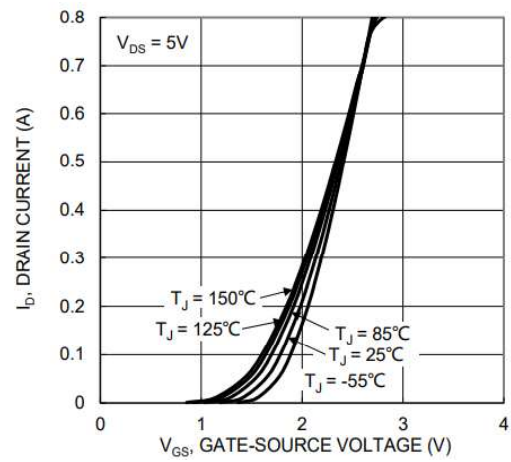
T_A=25°C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	60	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.8	-	1.5	V
I _{GSSF}	Gate Leakage Current , Forward	V _{DS} =0V, V _{GS} =20V			10	μA
I _{GSSR}	Gate Leakage Current , Reverse	V _{DS} =0V, V _{GS} =-20V			-10	μA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =250V, V _{GS} =0V			0.1	uA
		V _{DS} =50V, V _{GS} =0V,			0.5	
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =10V, I _D =0.5A		-	1.5	Ω
		V _{GS} =4.5V, I _D =0.2A		-	2.5	
		V _{GS} =2.5V, I _D =0.1A	-	-	4.1	
g _{FS}	Forward Transconductance	V _{DS} =25V, I _D =0.2A	100	-	-	mS
Dynamic						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz		22.8		pF
C _{oss}	Output Capacitance			3.5		
C _{rss}	Reverse Transfer Capacitance			2.9		
t _{d(on)}	Turn-On Time	V _{DD} =30V, I _D =0.5A, V _{GS} =10V, R _G =25Ω		3.8		ns
t _{d(off)}	Turn-Off Time			19		

Typical Performance Characteristics



Typical Output Characteristic



Typical Transfer Characteristic

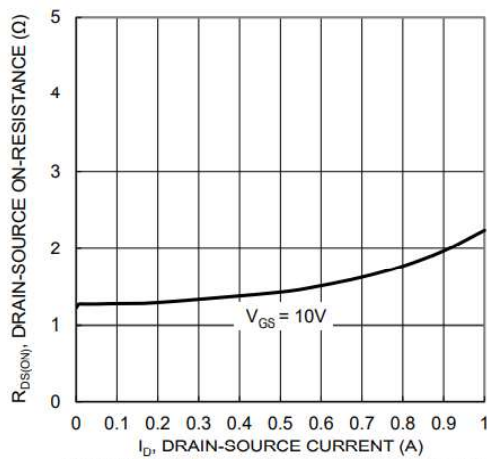


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage
Typical On-Resistance vs. Drain Current and Gate Voltage

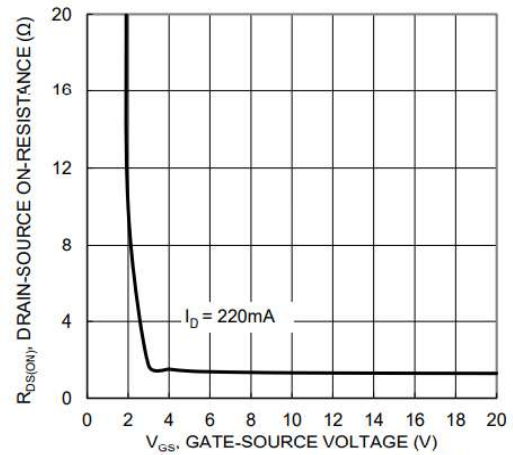
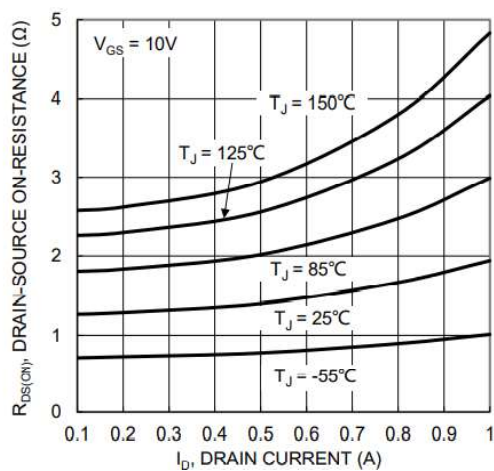
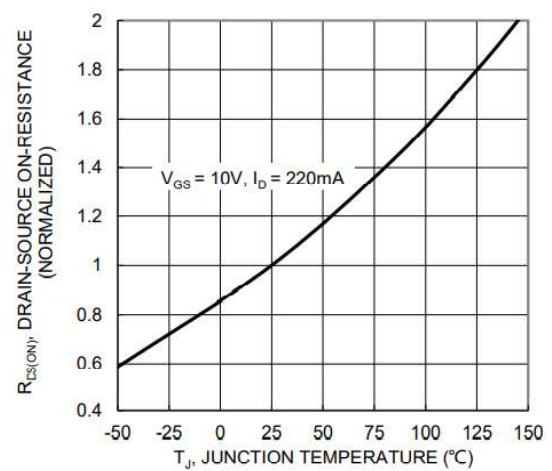


Figure 4. Typical Transfer Characteristic
Typical Transfer Characteristic

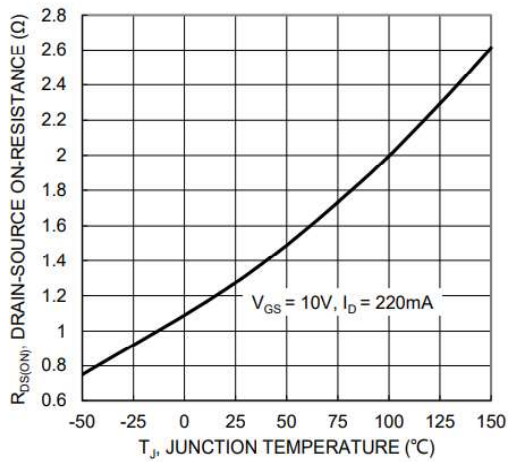


Typical On-Resistance vs. Drain and Junction Temperature

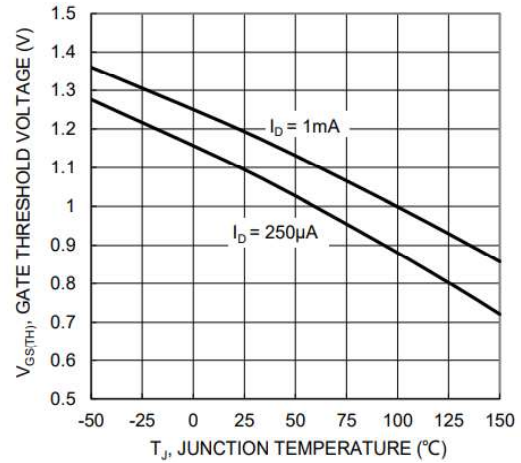


On-Resistance Variation with Junction Temperature

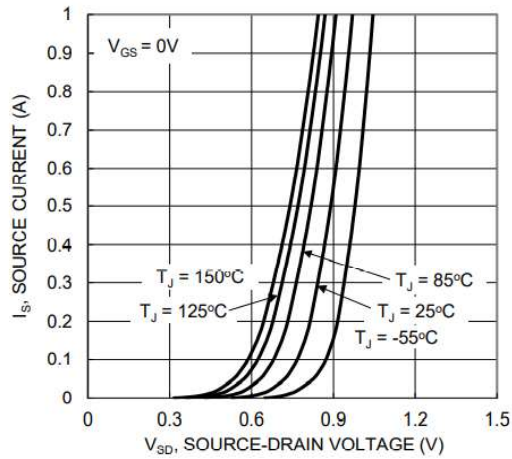
Typical Performance Characteristics (Continue)



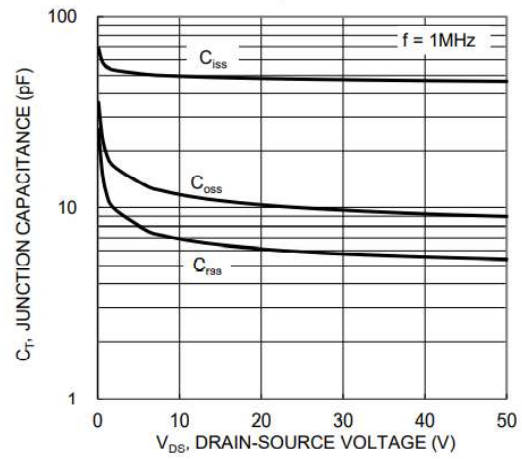
On-Resistance Variation with Junction Temperature



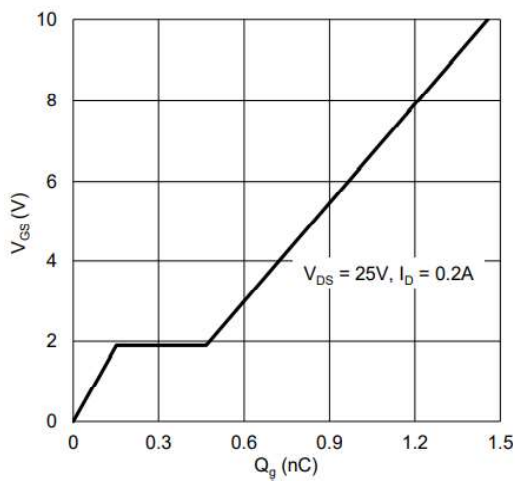
Gate Threshold Variation vs. Junction Temperature



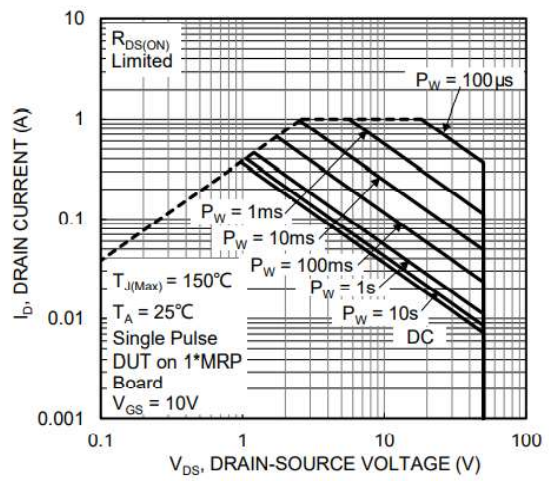
Diode Forward Voltage vs. Current



Typical Junction Capacitance



Gate Charge



Safe Operation Area

Typical Performance Characteristics (Continue)

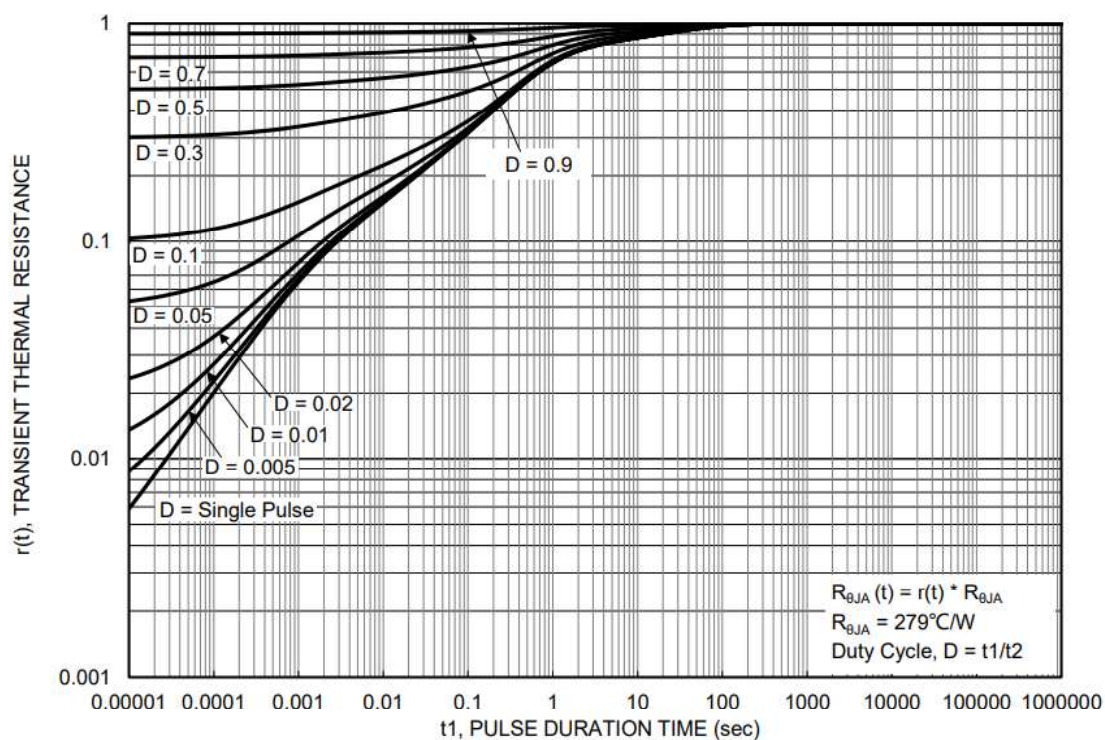
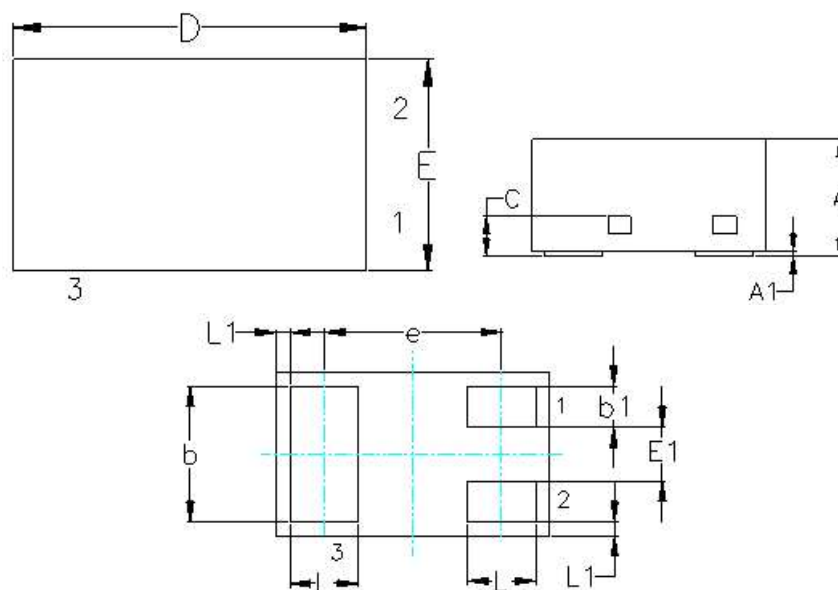


Figure13 Transient Thermal Resistance

Package Dimension

DFN1006-3L







Dimensions



SYMBOL	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.450	0.550	0.018	0.022
A1	0.000	0.050	0.000	0.002
b	0.450	0.550	0.018	0.022
b1	0.100	0.200	0.004	0.008
C	0.120	0.180	0.005	0.007
D	0.950	1.050	0.037	0.041
E	0.550	0.650	0.022	0.026
E1	0.150	0.250	0.006	0.010
e	0.650 BSC		0.026 BSC	
L	0.200	0.300	0.008	0.012
L1	0.050 REF		0.002 REF	

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CONTACT US

GS Headquarter	
	4F.,No.43-1,Lane11,Sec.6,Minquan E.Rd NeiHu District Taipei City 114, Taiwan (R.O.C)
	886-2-2657-9980
	886-2-2657-3630
	sales_twn@gs-power.com

RD Division	
	824 Bolton Drive Milpitas. CA. 95035
	1-408-457-0587