

# GSM7002KY

## Dual N-Channel Enhancement Mode MOSFET

### Product Description

The GSM7002KY is the Dual N-Channel enhancement mode field effect transistors are produced using high cell density DMOS technology.

These products have been designed to minimize on-state resistance while provide rugged, reliable, and fast switching performance.

They can be used in most applications requiring up to 640mA DC and can deliver pulsed currents up to 950mA. These products are particularly suited for low voltage, low current applications such as small servo motor control, power MOSFET gate drivers, and other switching applications.

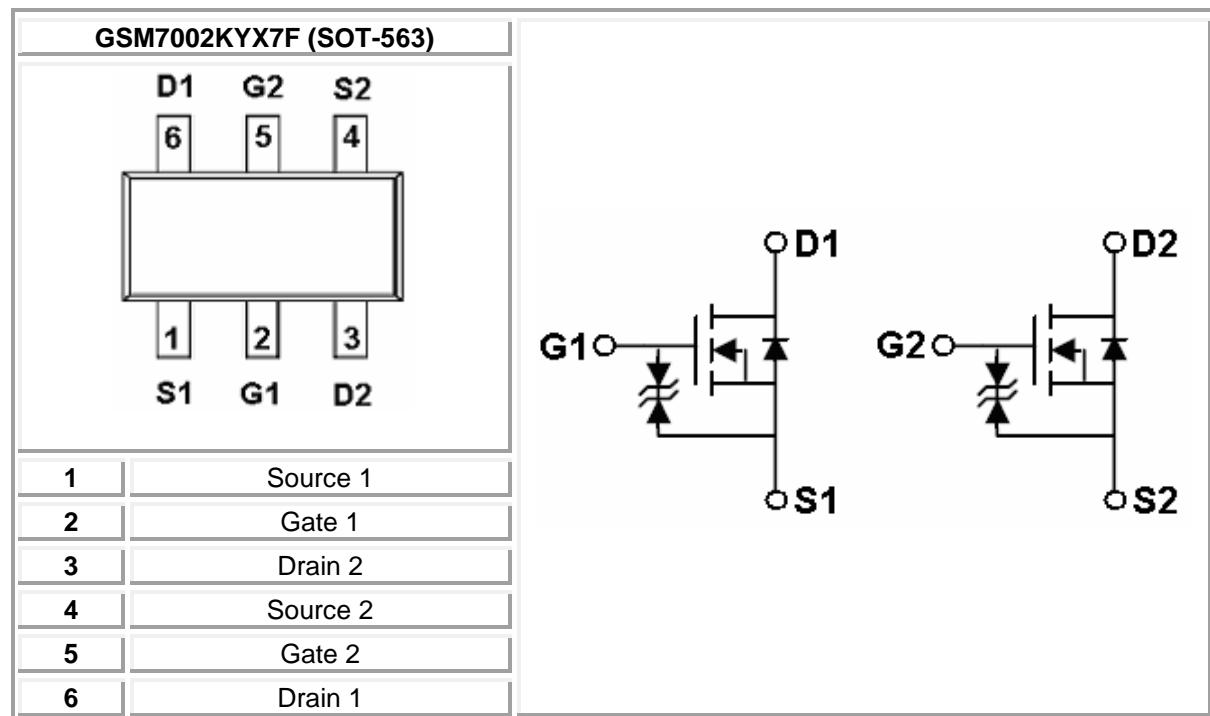
### Features

- 60V/0.5A,  $R_{DS(ON)}=3.0\Omega @ V_{GS}=10V$
- 60V/0.2A,  $R_{DS(ON)}=4.0\Omega @ V_{GS}=4.5V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- SOT-563 package design

### Applications

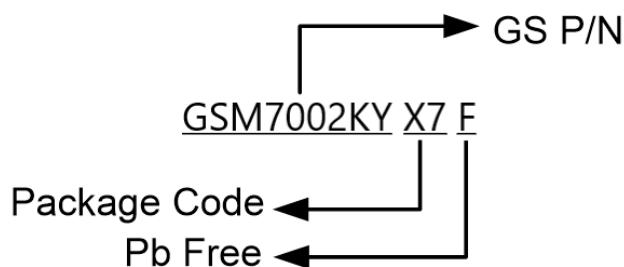
- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- High saturation current capability. Direct Logic-Level Interface: TTL/CMOS
- Battery Operated Systems
- Solid-State Relays

### Packages & Pin Assignments

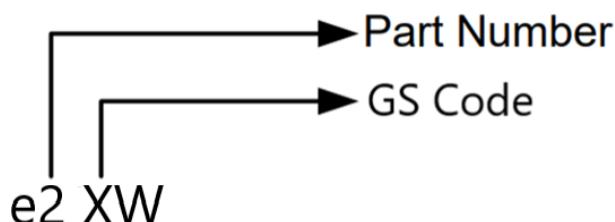


GSM7002KY

## Ordering Information



## Marking Information



Part Number	Package	Part Marking
GSM7002KYX7F	SOT-563	e2XW

## Absolute Maximum Ratings

TA=25°C Unless otherwise noted

Symbol	Parameter	Typical	Unit
V <sub>DS</sub>	Drain-Source Voltage	60	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current-Continuous	TA = 25°C TA = 70°C	mA mA
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	0.95	A
P <sub>D</sub>	Power Dissipation	TA = 25°C TA = 70°C	W W
R <sub>θJA</sub>	Thermal Resistance-Junction to Ambient	357	°C/W
T <sub>J</sub>	Junction Temperature Range	-55 to 150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C

(1) Pulse width limited by safe operating area.

## Electrical Characteristics

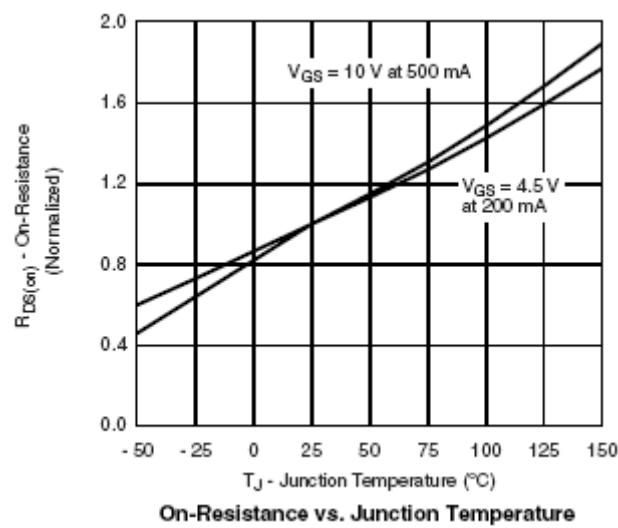
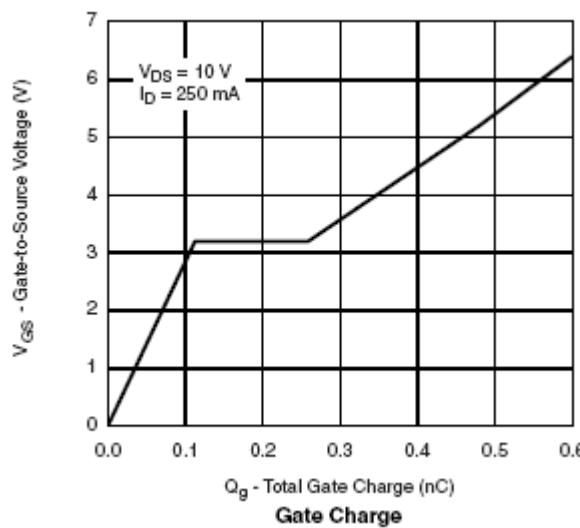
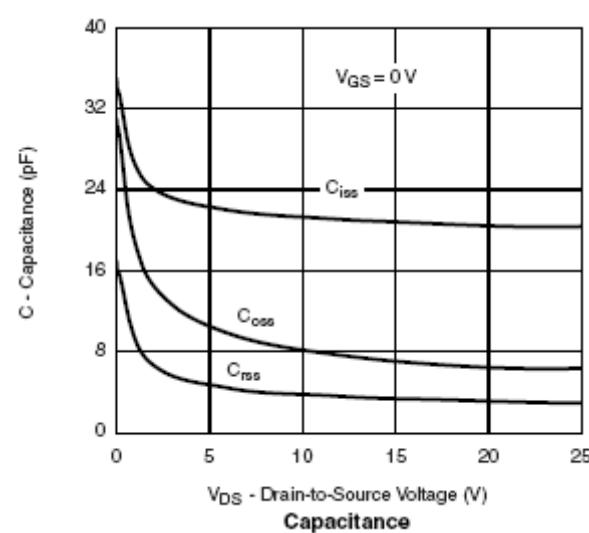
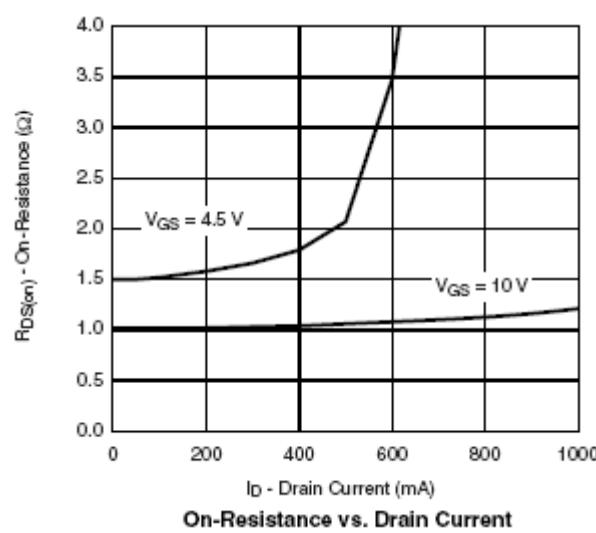
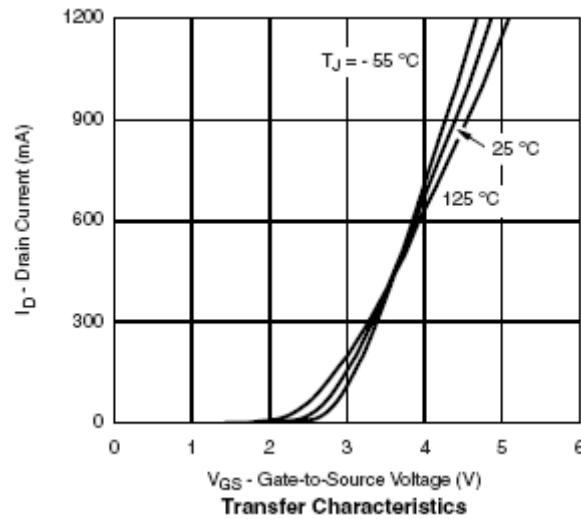
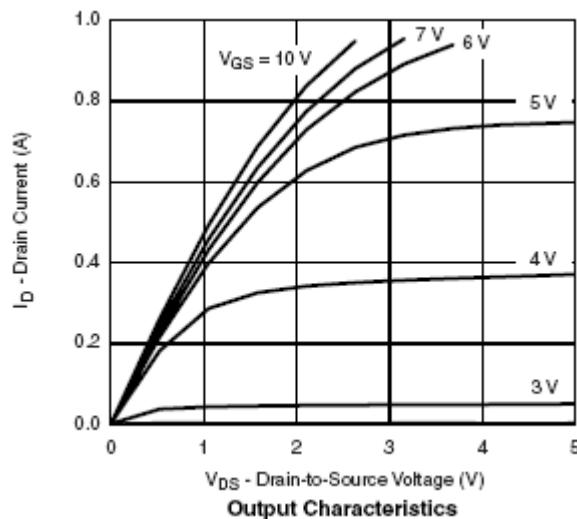
$T_J=25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	60	-	-	V
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	1		2.5	V
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V},$	-	-	$\pm 10$	$\mu\text{A}$
$I_{DSS}$	Drain-Source Leakage Current``	$V_{DS}=60\text{V}, V_{GS}=0\text{V}, T_J=25^\circ\text{C}$	-	-	1	$\mu\text{A}$
		$V_{DS}=60\text{V}, V_{GS}=0\text{V}, T_J=85^\circ\text{C}$	-	-	30	
$R_{DS(\text{on})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=500\text{mA}$	-	-	3	$\Omega$
		$V_{GS}=4.5\text{V}, I_D=200\text{mA}$	-	-	4	
<b>Dynamic</b>						
$C_{iss}$	Input Capacitance	$V_{DS}= 25\text{V}, f = 1\text{MHz}, V_{GS}=0\text{V}$	-	32		$\text{pF}$
$C_{oss}$	Output Capacitance		-	8		
$C_{rss}$	Reverse Transfer Capacitance		-	6		
$Q_g$	Total Gate Charge	$V_{DD}=10\text{V}, I_D=0.25\text{A}, V_{GS}=4.5\text{V}$		1000		$\text{pC}$
$Q_{gs}$	Gate-Source Charge			100		
$Q_{gd}$	Gate-Drain Charge			150		
$t_{d(on)}$	Turn-On Time	$V_{DD}=30\text{V}, I_D=0.2\text{A}, R_G=10\Omega, V_{GEN}=4.5\text{V}, R_L=150\Omega$		10	20	$\text{ns}$
$t_r$	Turn-On Time			35	50	
$t_{d(off)}$	Turn-Off Time			20	30	
$t_f$	Turn-Off Time			40	60	
$V_{SD}$	Diode Forward Voltage	$I_S=0.2\text{A}, V_{GS}=0\text{V}$	-	0.7	1.3	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}, I_D=0.2\text{A}$		0.2		S
$I_S$	Continuous Source Current	$V_G = V_D = 0\text{V}$ , Force Current	-	-	450	$\text{mA}$

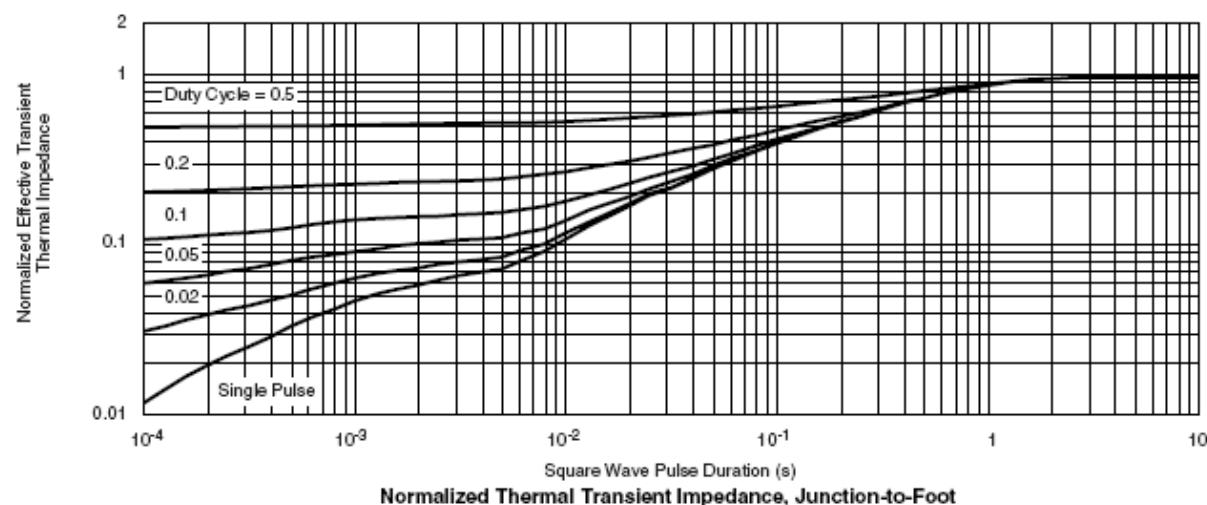
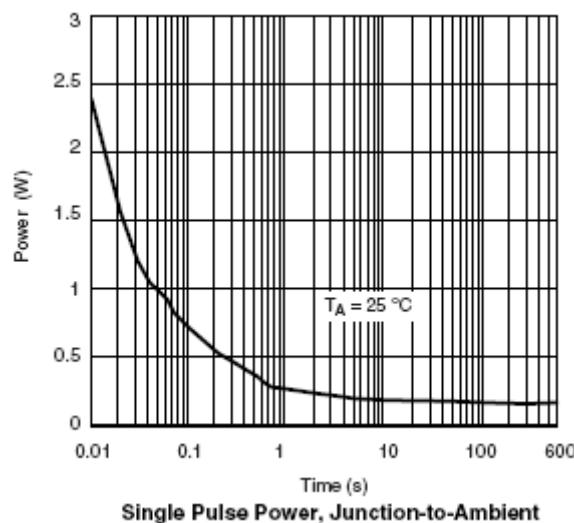
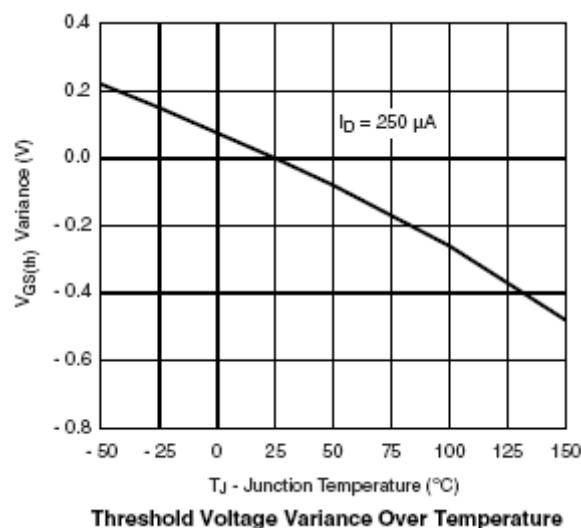
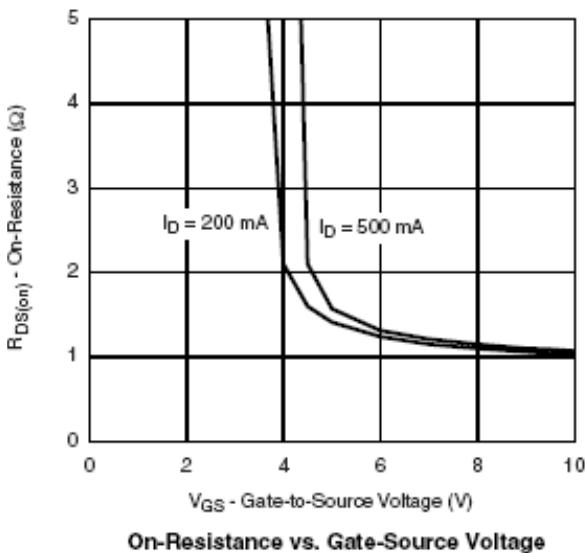
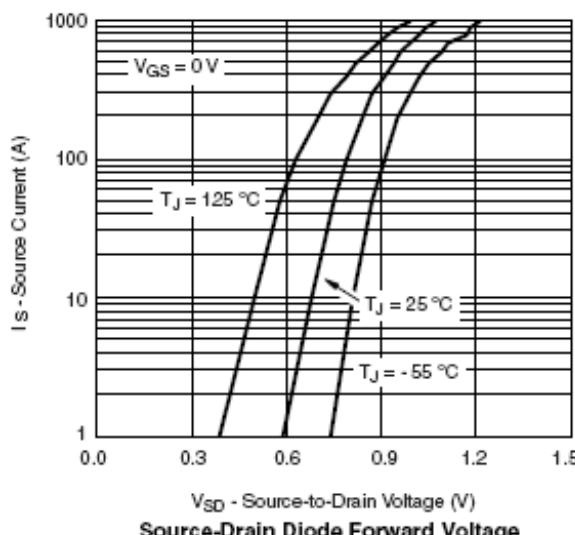
Note :

1. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
2. Essentially independent of operating temperature.

## Typical Performance Characteristics

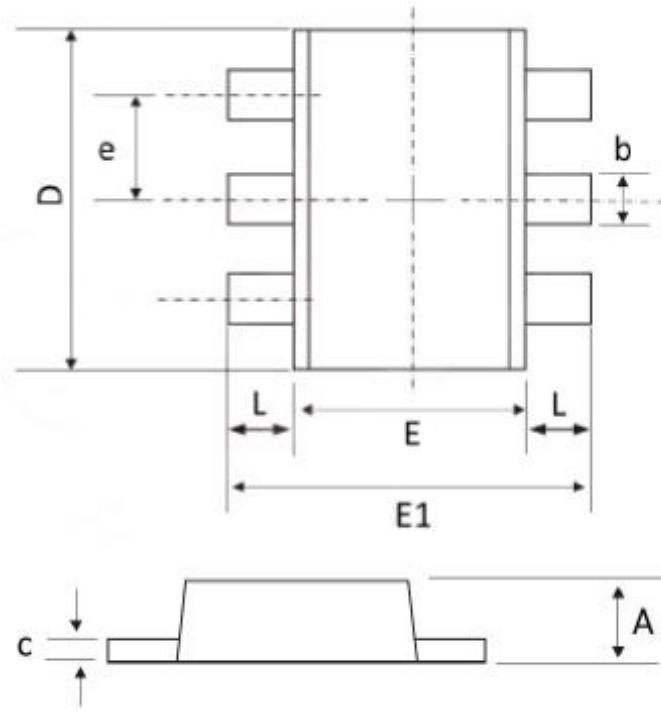


## Typical Performance Characteristics(Continue)



## Package Dimension

### SOT-563



#### Dimensions

Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.500	0.600	0.020	0.024
b	0.150	0.300	0.006	0.012
c	0.100	0.180	0.004	0.007
D	1.500	1.700	0.059	0.067
E	1.100	1.250	0.043	0.049
E1	1.550	1.700	0.061	0.067
e	0.5(BSC)		0.02(BSC)	
L	0.100	0.300	0.004	0.012

## NOTICE

Information furnished is believed to be accurate and reliable. However Globaltech Semiconductor assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Globaltech Semiconductor. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information without express written approval of Globaltech Semiconductor.

## CONTACT US

### GS Headquarter

	4F.,No.43-1,Lane11,Sec.6,Minquan E.Rd Neihu District Taipei City 114, Taiwan (R.O.C)
	886-2-2657-9980
	886-2-2657-3630
	sales_twn@gs-power.com

### RD Division

	824 Bolton Drive Milpitas. CA. 95035
	1-408-457-0587