

# GSM3901X

## 30V P-Channel MOSFET

### Product Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

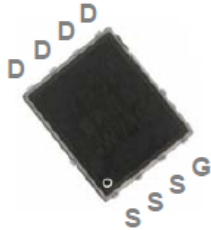
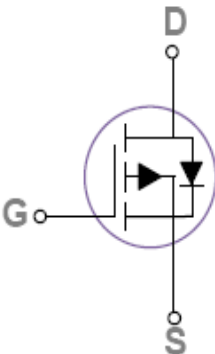
### Features

- -30V,-100A,RDS(ON) =3.3mΩ@VGS = -10V
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

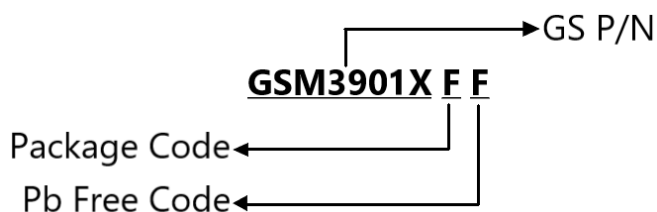
### Applications

- Motor Driver Application
- POL Applications
- Load Switch
- LED Application

### Packages & Pin Assignments

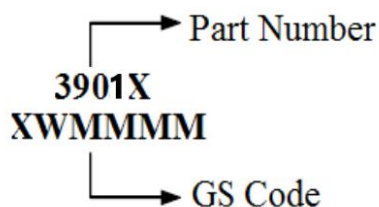
GSM3901XFF (DFN5X6-8L)		
 <p>Top View</p>		
		
Pin No	Symbol	Description
1,2,3	S	Source
4	G	Gate
5,6,7,8	D	Drain

## Ordering Information



Part Number	Package	Quantity
GSM3901XFF	DFN5X6-8L	3000pcs

## Marking Information



## Absolute Maximum Ratings

T<sub>c</sub>=25°C Unless otherwise noted

Symbol	Parameter	Typical	Unit
V <sub>DS</sub>	Drain-Source Voltage	-30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current	T <sub>c</sub> =25°C	-100
		T <sub>c</sub> =100°C	-63.2
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	-400	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	320	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	80	A
P <sub>D</sub>	Power Dissipation (T <sub>c</sub> =25°C)	138	W
	Power Dissipation-Derate above 25°C	1.11	W/°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C
R <sub>θJA</sub>	Thermal Resistance-Junction to Ambient	62	°C/W
R <sub>θJC</sub>	Thermal Resistance-Junction to Case	0.9	°C/W

## Electrical Characteristics

T<sub>J</sub>=25°C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1.2	-1.6	-2.2	V
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V			-1	uA
		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V T <sub>J</sub> =125°C			-10	
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current			-100	A
I <sub>SM</sub>	Pulsed Source Current				-200	A
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-30A		2.6	3.3	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-20A		3.8	5	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3A		20		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V			-1	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> =0V, I <sub>S</sub> =-20A ,		52		ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI/dt=100A/μs, T <sub>J</sub> =25°C		53		nC

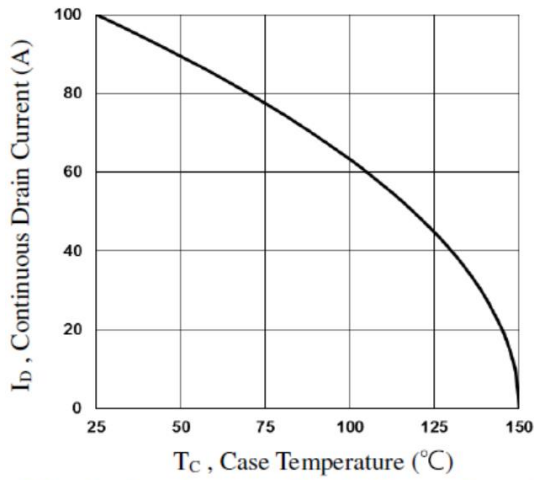
## Electrical Characteristics (Continue)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Dynamic</b>						
Q <sub>g</sub>	Total Gate Charge <sup>3,4</sup>	V <sub>DS</sub> =-24V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-10A		146	210	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3,4</sup>			22	44	
Q <sub>gd</sub>	Gate-Drain Charge <sup>3,4</sup>			32	64	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1MHz		7930	12000	pF
C <sub>oss</sub>	Output Capacitance			983	1300	
C <sub>rss</sub>	Reverse Transfer Capacitance			505	750	
t <sub>d(on)</sub>	Turn-On Time <sup>3,4</sup>	V <sub>DD</sub> =-15V, I <sub>D</sub> =-10A, V <sub>GS</sub> =-10V, R <sub>G</sub> =5Ω		17	34	ns
t <sub>r</sub>	Rise Time <sup>3,4</sup>			61	120	
t <sub>d(off)</sub>	Turn-Off Time <sup>3,4</sup>			200	400	
t <sub>f</sub>	Fall Time <sup>3,4</sup>			113	220	

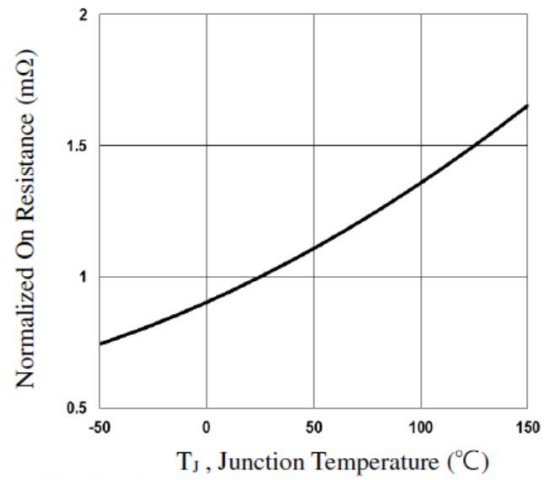
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=80A., R<sub>G</sub>=25W, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

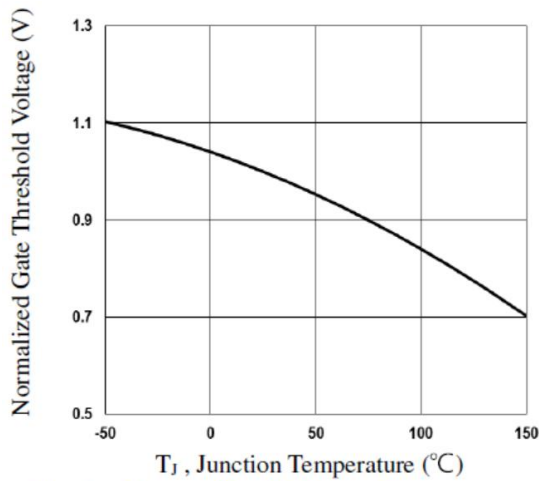
## Typical Performance Characteristics



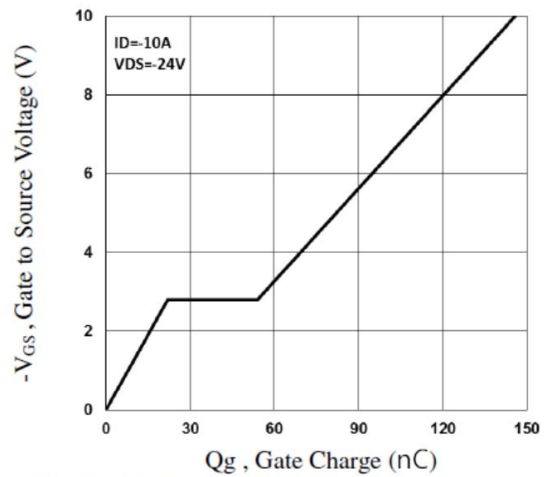
**Fig.1 Continuous Drain Current vs.  $T_C$**



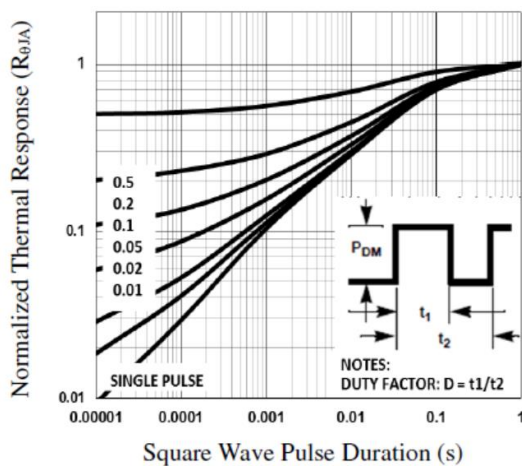
**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$**



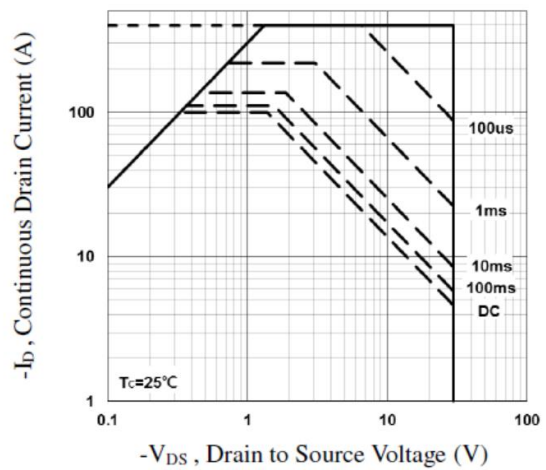
**Fig.3 Normalized  $V_{th}$  vs.  $T_J$**



**Fig.4 Gate Charge Waveform**



**Fig.5 Normalized Transient Impedance**



**Fig.6 Maximum Safe Operation Area**

Typical Performance Characteristics (Continue)

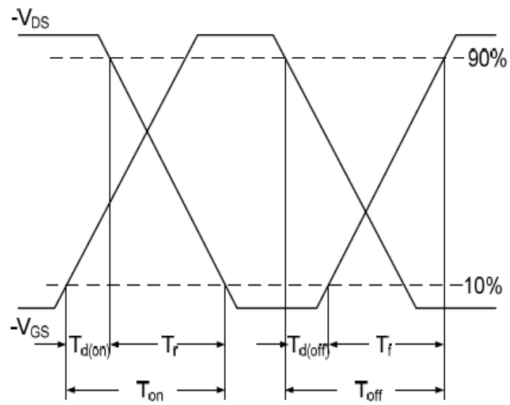


Fig.7 Switching Time Waveform

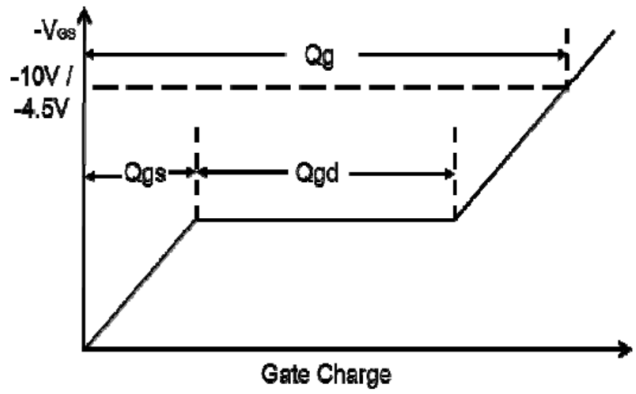
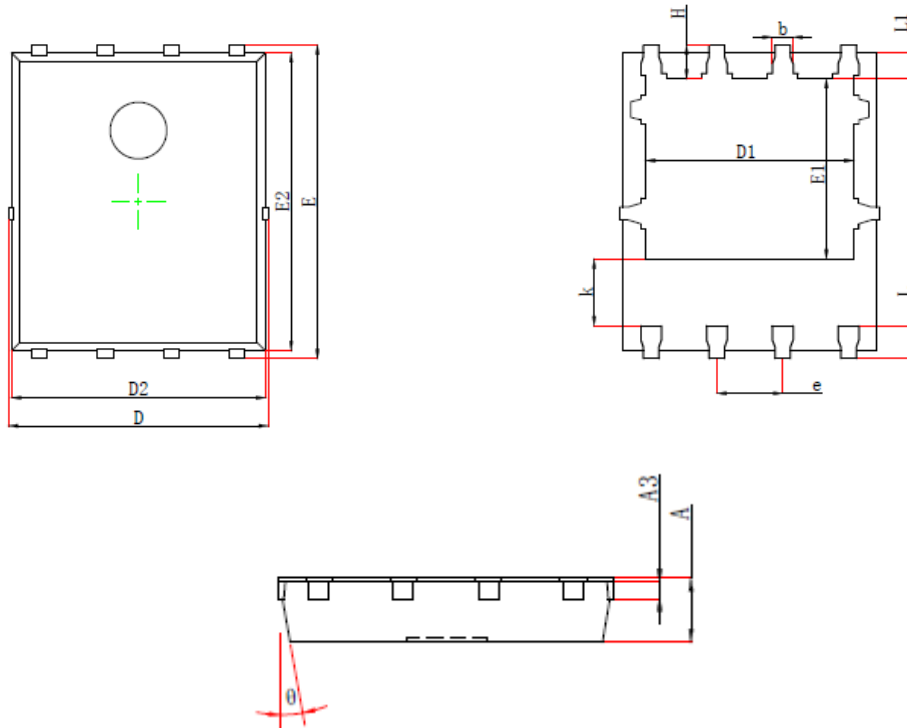


Fig.8 Gate Charge Waveform

## Package Dimension

### DFN5X6-8L









Dimensions				
SYMBOL	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
$\theta$	10°	12°	10°	12°

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## CONTACT US

GS Headquarter	
	4F.,No.43-1,Lane11,Sec.6,Minquan E.Rd NeiHu District Taipei City 114, Taiwan (R.O.C)
	886-2-2657-9980
	886-2-2657-3630
	sales_twn@gs-power.com

RD Division	
	824 Bolton Drive Milpitas. CA. 95035
	1-408-457-0587