

# GSM3385SF

## 30V P-Channel MOSFET

### Product Description

The P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

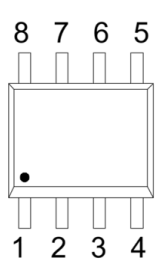
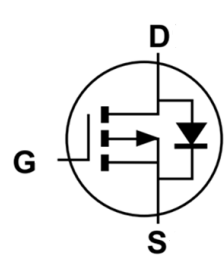
### Features

- $R_{DS(ON)} = 9.5m\Omega @ V_{GS} = -10V$
- $R_{DS(ON)} = 16m\Omega @ V_{GS} = -4.5V$
- SOP-8L Package
- RoHS Compliant and Halogen Free

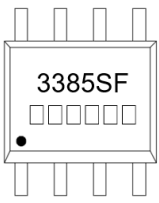
### Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

### Packages & Pin Assignments

SOP-8L			Equivalent Circuit		
					
Pin	Symbol	Description	Pin	Symbol	Description
1	S	Source	8	D	Drain
2	S	Source	7	D	Drain
3	S	Source	6	D	Drain
4	G	Gate	5	D	Drain

## Ordering and Marking Information

Ordering Information			
Part Number	Package	Part Marking	Quantity / Reel
GSM3385SF	SOP-8L	3385SF □□□□□□	4,000 PCS
<b>GSM3385</b> <span style="border: 1px solid black; padding: 0 2px;">1</span> <span style="border: 1px solid black; padding: 0 2px;">2</span>			
- <b>Product Code:</b> GSM3385		- <b>Package Code:</b> <span style="border: 1px solid black; padding: 0 2px;">1</span> is <b>S</b> for SOP-8L	- <b>Green Level:</b> <span style="border: 1px solid black; padding: 0 2px;">2</span> is <b>F</b> for RoHS Compliant and Halogen Free
Marking Information			
		- <b>Product Code:</b> 3385SF	
		- <b>GS Code:</b> □□□□□□ • The Dot denotes Pin 1	

## Absolute Maximum Ratings (T<sub>J</sub>=25°C Unless otherwise noted)

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-Source Voltage	-30	V
V <sub>GS</sub>	Gate-Source Voltage	±25	V
I <sub>D</sub>	Continuous Drain Current <sup>3</sup>	T <sub>A</sub> =25°C	-13
		T <sub>A</sub> =70°C	-10
I <sub>DM</sub>	Pulsed Drain Current <sup>1,3</sup>	-52	A
I <sub>AS</sub>	Single Pulse Avalanche Current, L = 0.1mH <sup>1</sup>	25	A
E <sub>AS</sub>	Single Pulse Avalanche Energy, L = 0.1mH <sup>1</sup>	93	mJ
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> =25°C	2.5
		T <sub>A</sub> =70°C	1.6
R <sub>θJA</sub>	Max Thermal Resistance-Junction to Ambient (Steady State) <sup>2</sup>	75	°C/W
	Max Thermal Resistance-Junction to Ambient (t ≤ 10s) <sup>2</sup>	50	
T <sub>J</sub>	Operating Junction Temperature Range	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

### NOTE:

- Pulsed width is limited by the maximum junction temperature.
- Surface mounted on 1in2 FR-4 board with 2oz. Copper.
- t ≤ 10s

## Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
B <sub>V</sub> DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-30	-	-	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	-	-	-1	μA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.2	-	-2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-10A	-	8.3	9.5	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-8A	-	12.4	16	
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz	-	2830	-	pF
C <sub>oss</sub>	Output Capacitance		-	430	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	365	-	
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-15V, I <sub>D</sub> =-10A V <sub>GS</sub> =-10V	-	50	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	9	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	12	-	
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-15V, I <sub>D</sub> =-10A V <sub>GS</sub> =-10V, R <sub>g</sub> =3.3Ω	-	12	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	12.5	-	
t <sub>d(off)</sub>	Turn-Off Delay Time		-	135	-	
t <sub>f</sub>	Turn-Off Fall Time		-	63	-	
<b>Diode Characteristics</b>						
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A	-	-	-1	V

## Typical Performance Characteristics

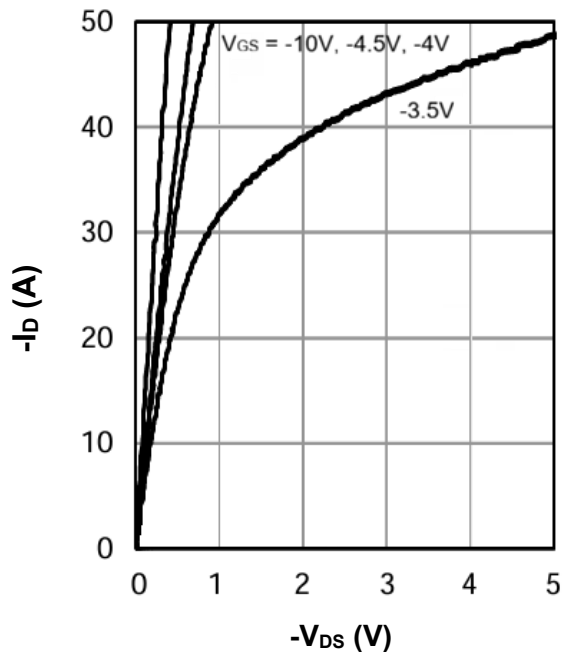


FIG.1 Output Characteristics

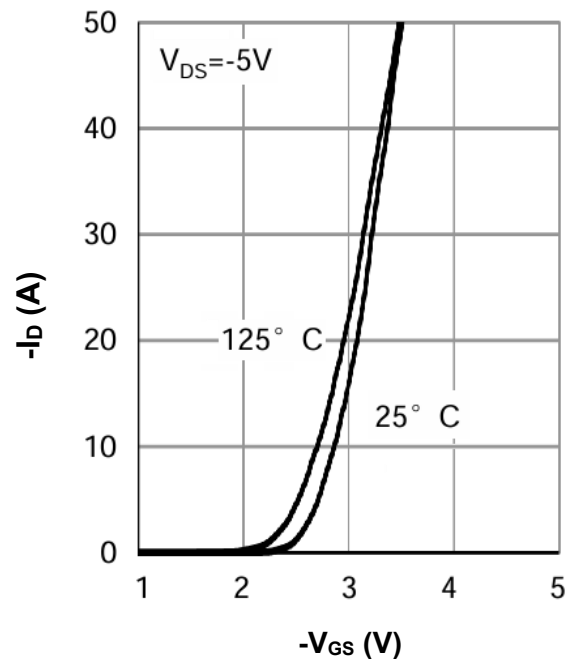


FIG.2 Transfer Characteristics

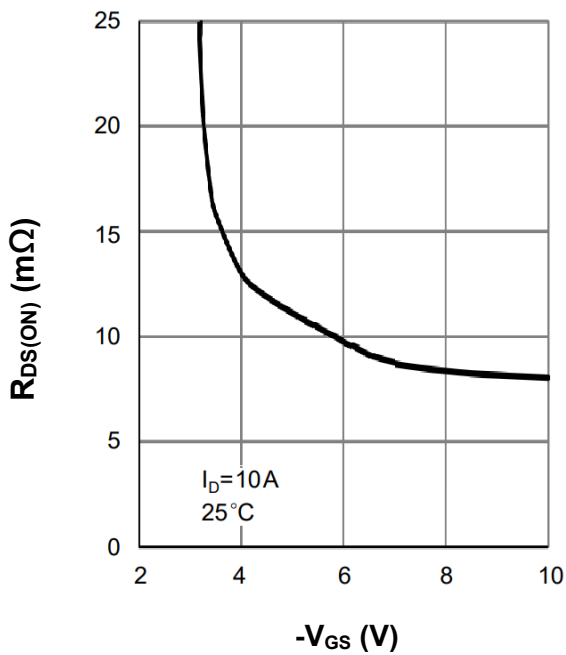


FIG.3 On-Resistance vs. Gate Voltage

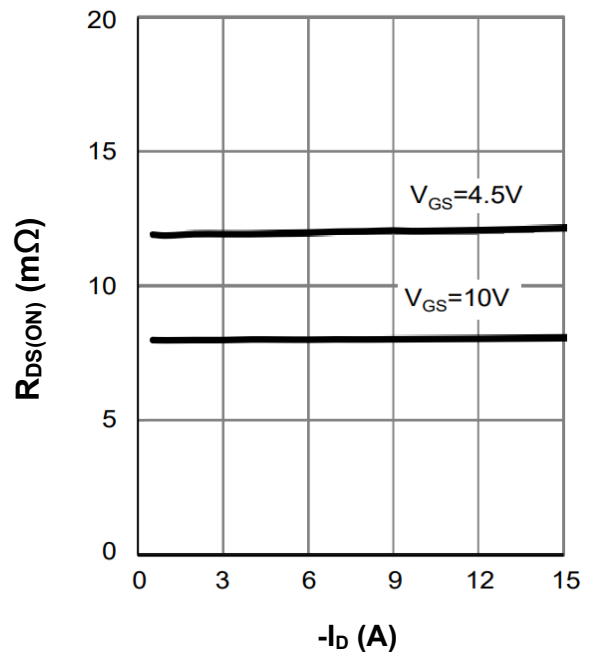


FIG.4 On-Resistance vs. Drain Current

## Typical Performance Characteristics

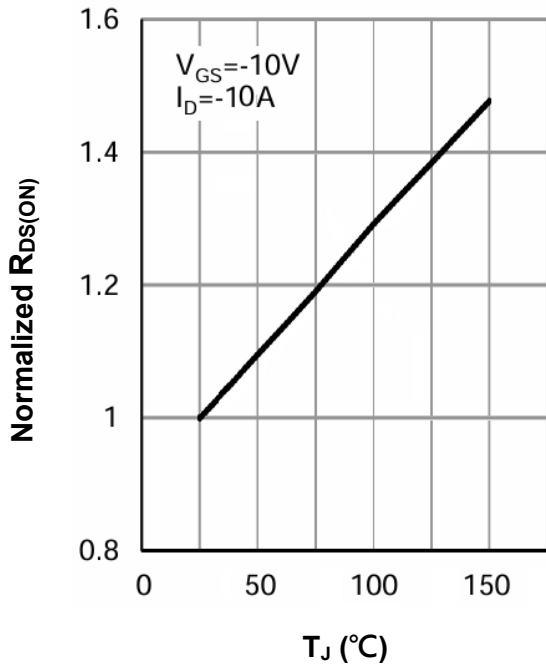


FIG.5 Normalized On-Resistance vs.  $T_J$

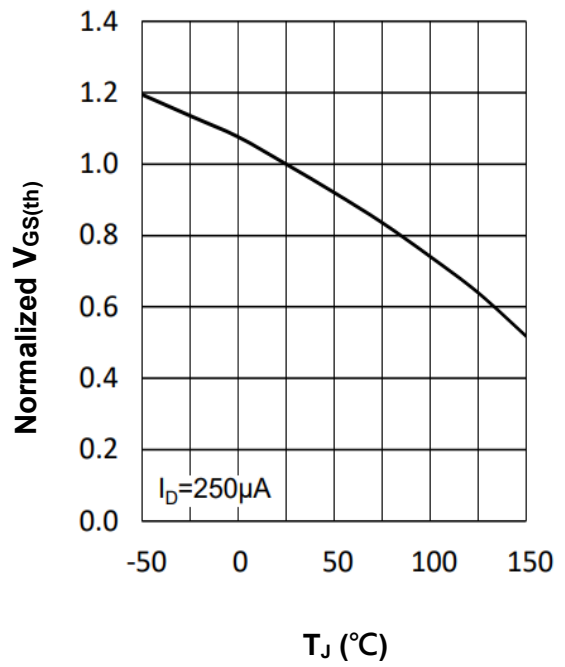


FIG.6 Normalized  $V_{GS(th)}$  vs.  $T_J$

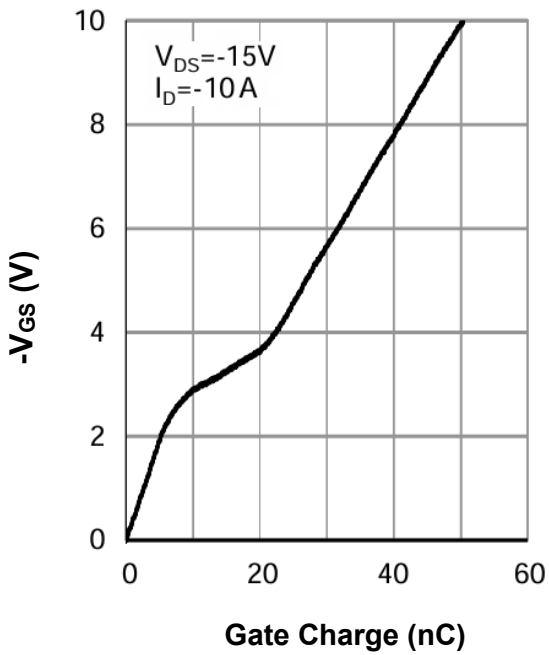


FIG.7 Gate Charge Characteristics

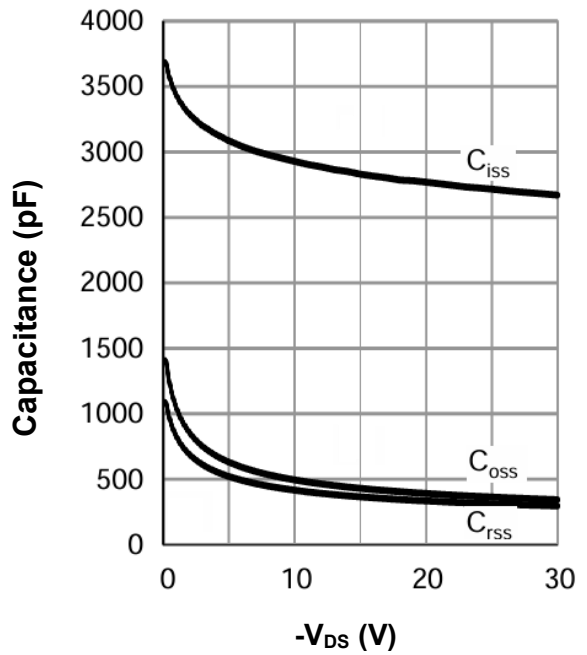


FIG.8 Capacitance Characteristics

## Typical Performance Characteristics

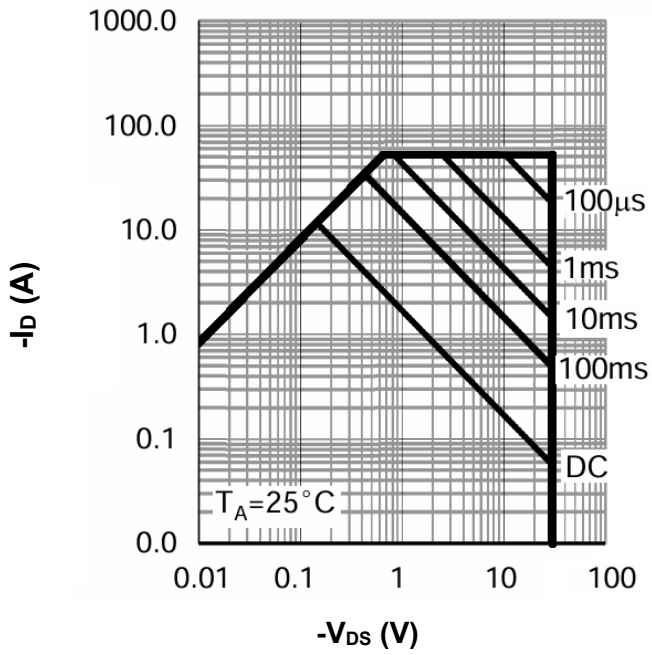


FIG.9 Maximum Safe Operation Area

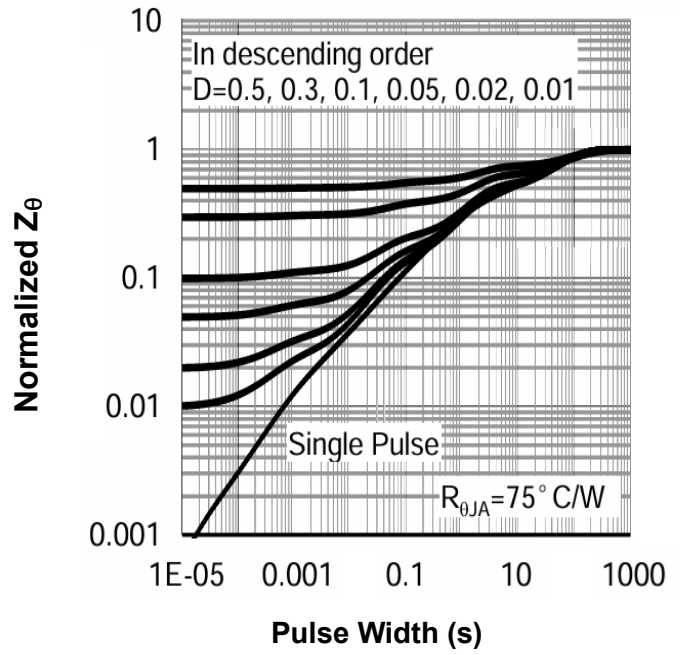
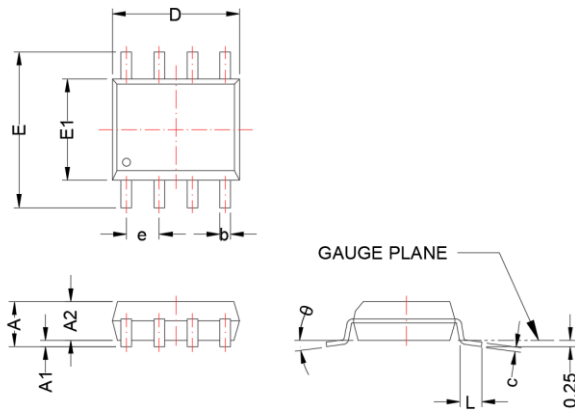


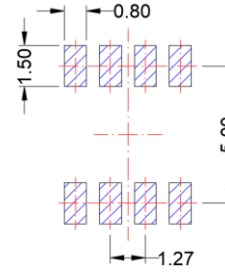
FIG.10 Normalized Transient Impedance

# SOP-8L

## Package Dimension



## Recommended Land Pattern



Unit:mm

Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
<b>A</b>	---	1.75	---	0.069
<b>A1</b>	0.10	0.25	0.004	0.010
<b>A2</b>	1.25	---	0.049	---
<b>b</b>	0.31	0.51	0.012	0.020
<b>c</b>	0.10	0.25	0.004	0.010
<b>D</b>	4.70	5.10	0.185	0.201
<b>E</b>	5.80	6.20	0.228	0.244
<b>E1</b>	3.80	4.00	0.150	0.157
<b>e</b>	1.27 BSC		0.050 BSC	
<b>L</b>	0.40	1.27	0.016	0.050
<b>θ</b>	0°	8°	0°	8°





**NOTE:**



Dimensions are exclusive of Burrs, Mold Flash and Tie Bar extrusions.

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