GSM3131SF

30V P-Channel MOSFET

Product Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

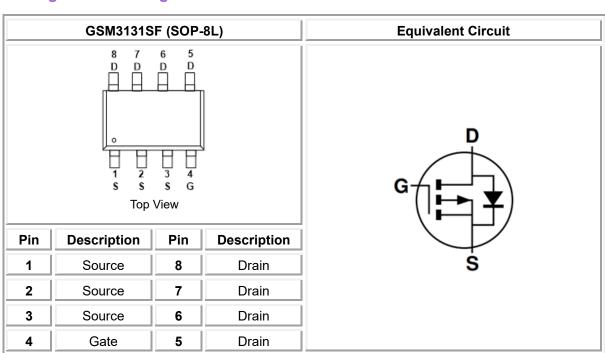
Features

- -30V, -6A, $R_{DS(ON)} = 32m\Omega$ @ $V_{GS} = -10V$
- Suit for -4.5V Gate Drive Applications
- SOP-8L Package
- RoHS Compliant and Halogen Free

Applications

- Notebook
- Load Switch
- **Battery Protection**
- Hand-held Instruments

Packages & Pin Assignments





Ordering and Marking Information

| Ordering Information | | | | |
|----------------------------|--|--------|-----------------|--|
| Part Number | Package Part Marking Qua | | Quantity / Reel | |
| GSM3131SF | SOP-8L | 3131S | 4,000 PCS | |
| GSM3131 1 2 | | | | |
| - Product Code: GSM3131 | - Package Code: 1 is S for SOP-8L 2 is F for RoHS Compliant and Halogen Free | | | |
| | Marking Infor | mation | | |
| 31318 | - Product Code: 3131S | | | |
| | - GS Code: | | | |

Absolute Maximum Ratings (T_J=25°C Unless otherwise noted)

| Symbol | Parameter | | Value | Unit | |
|------------------|---|----------------------|-------------|------------------------|--|
| V _{DS} | Drain-Source Voltage | | -30 | V | |
| V _{GS} | Gate-Source Voltage | | ±20 | V | |
| | 0 (0 1 | T _A =25°C | -6 | | |
| l _D | Continuous Drain Current | T _A =70°C | -4.7 | Α | |
| I _{DM} | Pulsed Drain Current ¹ | | -24 | Α | |
| I _{AS} | Single Pulse Avalanche Current ² | | -15 | Α | |
| Eas | Single Pulse Avalanche Energy ² | | 22.5 | mJ | |
| P _D | Power Dissipation (T _A =25°ℂ) | | 1.47 | W | |
| $R_{\theta JA}$ | Thermal Resistance-Junction to Ambient ³ | | 72 | °C/W | |
| TJ | Operating Junction Temperature Range | | -55 to +150 | $^{\circ}\!\mathbb{C}$ | |
| T _{STG} | Storage Temperature Range | | -55 to +150 | ${\mathbb C}$ | |

Note:

- Pulsed width is limited by the maximum junction temperature.
- V_{DD} =-15V, V_{GS} =-10V, L=0.1mH, I_{AS}=-15A. Surface mounted on 1in² FR-4 board with 2oz. Copper.



Electrical Characteristics (T_J=25°C Unless otherwise noted)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit | |
|------------------------|---|--|------|------|-------|------|--|
| | Static | Characteristics | | | | | |
| V _{(BR)DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250uA | -30 | | | V | |
| $V_{GS(th)}$ | Gate Threshold Voltage | V _{DS} =V _{GS} , I _D =250uA | -1.3 | -1.7 | -2.3 | V | |
| Igss | Gate-Source Leakage Current | V _{DS} =0V, V _{GS} =±20V | | | ±100 | nA | |
| I _{DSS} Drain | Paris Course Lockers Course | V _{DS} =-30V, V _{GS} =0V T _J =25°C | | | -1 | | |
| | Drain-Source Leakage Current | V _{DS} =-24V, V _{GS} =0V, T _J =125°C | | | -10 | uA | |
| R _{DS(on)} | | V _{GS} =10V, I _D =-4A | | 26 | 32 | | |
| | Drain-Source On-Resistance ⁴ | V _{GS} =4.5V, I _D =-3A, | | 42 | 46 | mΩ | |
| g FS | Forward Transconductance ⁴ | V _{DS} =-10V, I _D =-3A | | 8 | | S | |
| VsD | Diode Forward Voltage ⁴ | V _{GS} =0V, I _S =-1A | | | -1 | V | |
| ls | Continuous Source Current | V _G =V _D =0V, Force Current | | | -1.47 | Α | |
| | Dynam | ic Characteristics | | | | | |
| Q_g | Total Gate Charge | | | 8 | 15 | | |
| Q_{gs} | Gate-Source Charge | V _{DS} =-15V, V _{GS} =-4.5V, I _D =-5A | | 3.3 | 6 | nC | |
| Q_{gd} | Gate-Drain Charge | 100/1 | | 2.3 | 5 | | |
| C _{iss} | Input Capacitance | | | 757 | 1280 | | |
| Coss | Output Capacitance | V _{DS} =15V,V _{GS} =0V, f=1MHz | | 122 | 210 | pF | |
| C _{rss} | Reverse Transfer Capacitance | 1- 11411 12 | | 88 | 175 | | |
| t _{d(on)} | Turn-On Time | | | 4.6 | 9 | | |
| t _r | Rise Time | V _{DD} =15V, I _D =-1A, | | 14 | 26 | no | |
| $t_{\text{d(off)}}$ | Turn-Off Time | V_{GS} =-10V, R_G =6 Ω | | 34 | 58 | ns | |
| t f | Fall Time | | | 18 | 35 | | |

Note:

4. The data tested by pulsed , pulse width $\,\leq\,300\text{us}$, duty cycle $\,\leq\,2\%.$



Typical Performance Characteristics

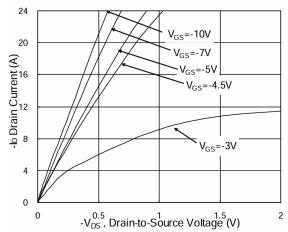


Fig.1 Output Characteristics

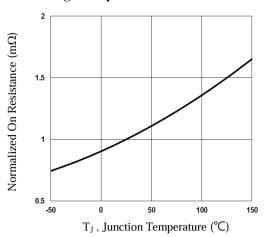


Fig.2 On-Resistance vs. Gate Voltage

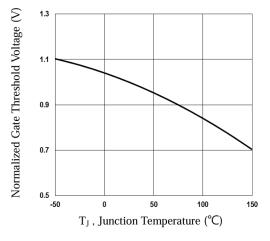


Fig.3 Normalized On-Resistance vs. T_J

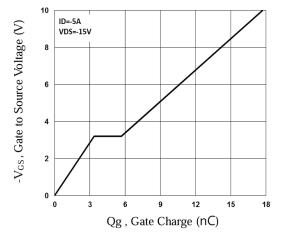


Fig.4 Normalized $V_{GS(th)}$ vs. T_J

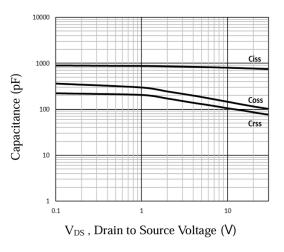
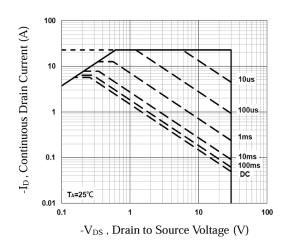


Fig.5 Gate Charge Characteristics

Fig.6 Capacitance Characteristics

Typical Performance Characteristics (Continued)





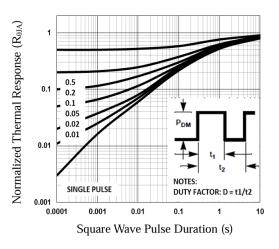
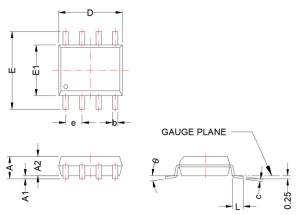


Fig.8 Normalized Transient Impedance

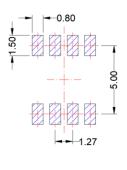


SOP-8L

Package Dimension



Recommended Land Pattern



Unit:mm

| | Dimensions | | | | |
|------------|-------------|------|-----------|-------|--|
| Symbol | Millimeters | | Inches | | |
| Symbol | Min | Max | Min | Max | |
| Α | | 1.75 | | 0.069 | |
| A 1 | 0.10 | 0.25 | 0.004 | 0.010 | |
| A2 | 1.25 | | 0.049 | | |
| b | 0.31 | 0.51 | 0.012 | 0.020 | |
| С | 0.10 | 0.25 | 0.004 | 0.010 | |
| D | 4.70 | 5.10 | 0.185 | 0.201 | |
| E | 5.80 | 6.20 | 0.228 | 0.244 | |
| E1 | 3.80 | 4.00 | 0.150 | 0.157 | |
| е | 1.27 BSC | | 0.050 BSC | | |
| L | 0.40 | 1.27 | 0.016 | 0.050 | |
| θ | 0° | 8° | 0° | 8° | |

NOTE:

Dimensions are exclusive of Burrs, Mold Flash and Tie Bar extrusions.



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