

# GSM3123NCF

## 30V P-Channel Enhancement Mode MOSFET

### Product Description

The P-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

The device is well suited for high efficiency fast switching applications.

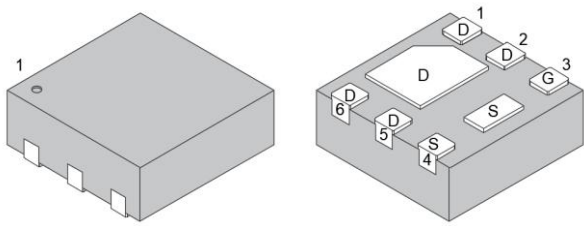
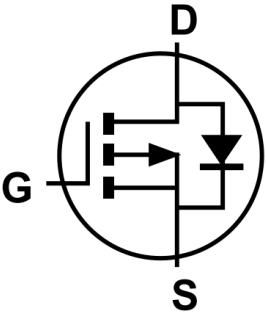
### Features

- $R_{DS(ON)} = 23m\Omega @ V_{GS} = -10V$
- $R_{DS(ON)} = 34m\Omega @ V_{GS} = -4.5V$
- Suit for -4.5V Gate Drive Applications
- DFN2x2-6L(C) Package
- RoHS Compliant and Halogen Free

### Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

### Packages & Pin Assignments

GSM3123NCF (DFN2x2-6L(C))		Equivalent Circuit
 Bottom View		
Pin	Description	
1	Drain	
2	Drain	
3	Gate	
4	Source	
5	Drain	
6	Drain	

## Ordering and Marking Information

Ordering Information			
Part Number	Package	Part Marking	Quantity / Reel
GSM3123NCF	DFN2x2-6L(C)	3123 □□□□	4,000 PCS
<b>GSM3123 1 2</b> - <b>Product Code:</b> GSM3123 - <b>Package Code:</b> 1 is <b>NC</b> for DFN2x2-6L(C) - <b>Green Level:</b> 2 is <b>F</b> for RoHS Compliant and Halogen Free			
Marking Information			
<div style="display: flex; align-items: center;"> <div style="border: 1px solid black; padding: 10px; margin-right: 20px;">           3123            □□□□            ○         </div> <div>           - <b>Product Code:</b> 3123             - <b>GS Code:</b> □□□□         </div> </div>			

## Absolute Maximum Ratings

T<sub>A</sub>=25°C, unless otherwise specified

Symbol	Parameter	Value	Unit
V <sub>DSS</sub>	Drain-Source Voltage	-30	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current <sup>1</sup>	T <sub>A</sub> =25°C	A
		T <sub>A</sub> =70°C	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	-36	A
P <sub>D</sub>	Total Power Dissipation <sup>3</sup>	T <sub>A</sub> =25°C	W
		T <sub>A</sub> =70°C	
T <sub>J</sub>	Operating Junction Temperature Range	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient <sup>1</sup>	41	°C/W

Note:

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2oz copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. The power dissipation is limited by 150°C junction temperature.

## Electrical Characteristics

T<sub>A</sub>=25°C, unless otherwise specified

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-30	-	-	V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1	-	-2.5	V
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V	-	-	±100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	-	-	-1	μA
R <sub>DS(ON)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-9.1A	-	17	23	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-6.9A	-	28	34	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-5A	-	-	10	S
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A	-	-	-1	V
<b>Dynamic characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz	-	1250	-	pF
C <sub>oss</sub>	Output Capacitance		-	160	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	90	-	
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A	-	11	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	3.4	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	4.2	-	
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-15V, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω, I <sub>D</sub> =-1A	-	5.8	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	18.8	-	
t <sub>d(off)</sub>	Turn-Off Delay Time		-	46.9	-	
t <sub>f</sub>	Turn-Off Fall Time		-	12.3	-	

## Typical Performance Characteristics

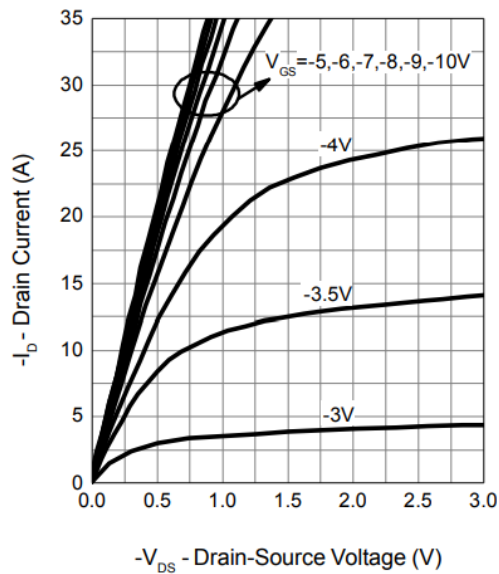


Figure 1. Typical Output Characteristics

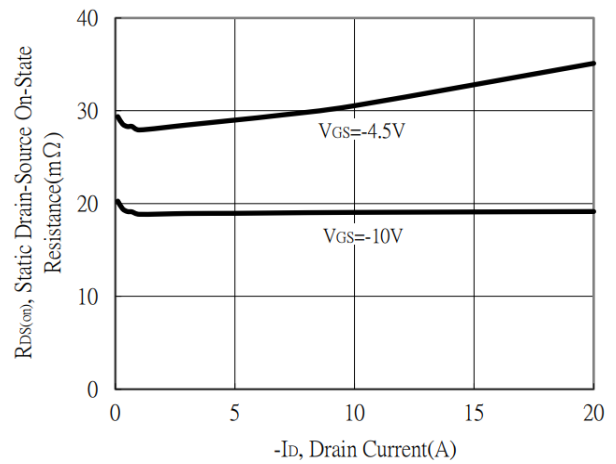


Figure 2. Typical Transfer Characteristics

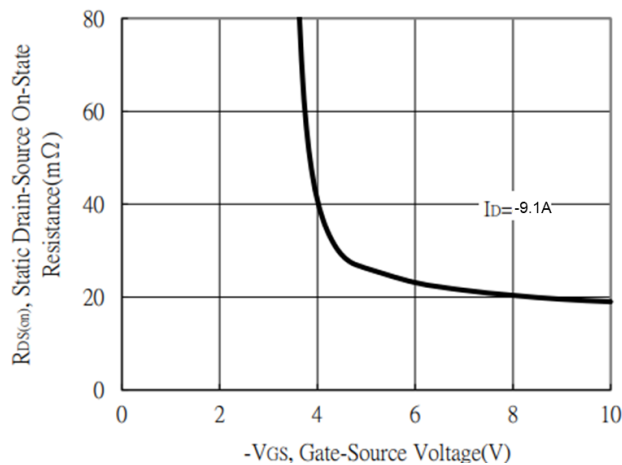


Figure 3. On-Resistance Variation with  $T_J$

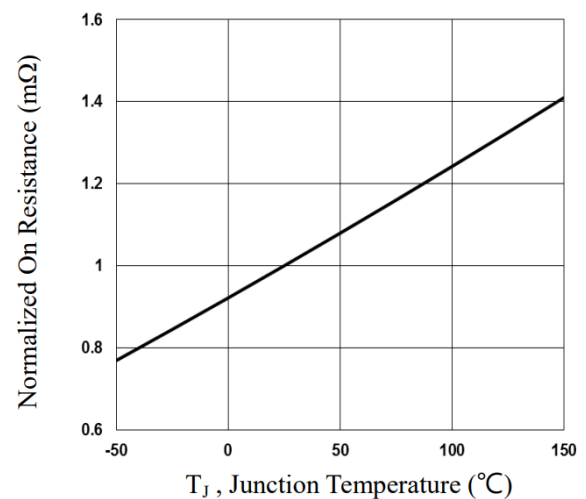


Figure 4. Gate Threshold Voltage Variation with  $T_J$

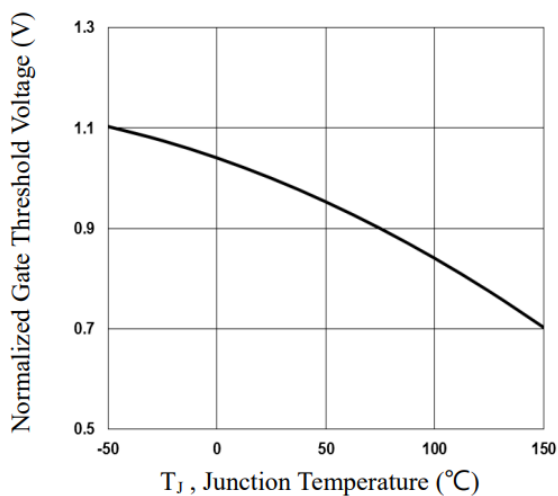


Figure 5. Drain-Source On-State resistance vs Drain Current

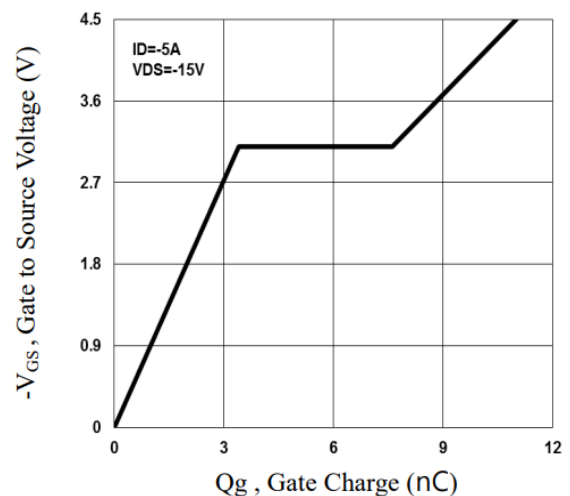


Figure 6. Gate Charge

## Typical Performance Characteristics

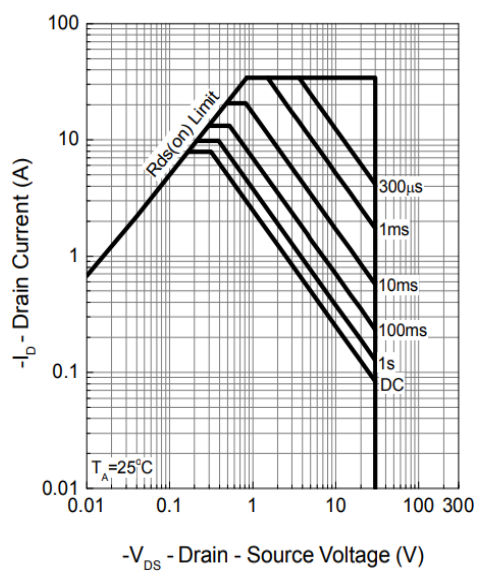
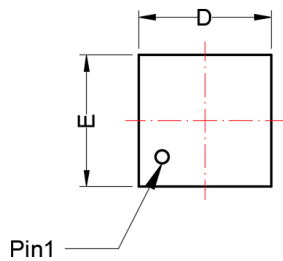


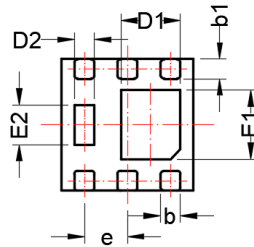
Figure 7. Safe Operation Area

# DFN2x2-6L(C)

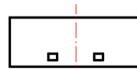
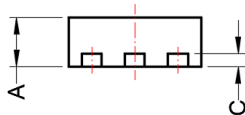
## Package Dimension



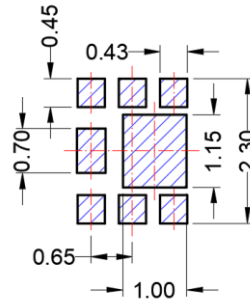
Pin1



BACKSIDE VIEW



## Recommended Land Pattern



Dimensions				
Symbol	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.70	0.80	0.028	0.031
b	0.25	0.35	0.010	0.014
b1	0.25	0.35	0.010	0.014
c	0.15	0.26	0.006	0.010
D	1.90	2.10	0.075	0.083
D1	0.80	1.00	0.031	0.039
D2	0.25	0.35	0.010	0.014
E	1.95	2.05	0.077	0.081
E1	0.90	1.10	0.035	0.043
E2	0.50	0.65	0.020	0.026
e	0.65 BSC		0.026 BSC	





### NOTE:



Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.

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