

# GSM2151EJZF

## 25V P-Channel MOSFET

### Product Description

The P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

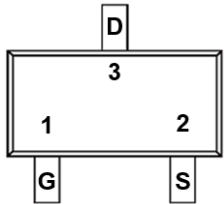
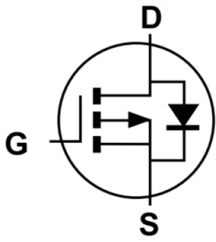
### Features

- $R_{DS(ON)} = 50m\Omega @ V_{GS} = -10V$
- $R_{DS(ON)} = 80m\Omega @ V_{GS} = -4.5V$
- SOT-23 Package
- RoHS Compliant and Halogen Free

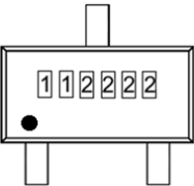
### Applications

- Load Switch
- Hand-held Instruments

### Packages & Pin Assignments

SOT-23			Equivalent Circuit		
					
Pin	Symbol	Description	Pin	Symbol	Description
1	G	Gate	3	D	Drain
2	S	Source			

## Ordering and Marking Information

Ordering Information			
Part Number	Package	Part Marking	Quantity / Reel
GSM2151EJZF	SOT-23	112222 or 11222	3,000 PCS
<b>GSM2151E</b> 1 2 - <b>Product Code:</b> GSM2151E - <b>Package Code:</b> 1 is JZ for SOT-23 - <b>Green Level:</b> 2 is F for RoHS Compliant and Halogen Free			
Marking Information			
 <ul style="list-style-type: none"> <li>- <b>Product Code:</b> 11 is 1E or 31</li> <li>- <b>GS Code:</b> 2222 or 222</li> <li>● Dot for Pin1</li> </ul>			

## Absolute Maximum Ratings (T<sub>J</sub>=25°C Unless otherwise noted)

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-Source Voltage	-25	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current	T <sub>A</sub> =25°C	-4.2
		T <sub>A</sub> =70°C	-3.4
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	-16	A
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> =25°C	1.38
		T <sub>A</sub> =70°C	0.88
R <sub>θJA</sub>	Max Thermal Resistance-Junction to Ambient <sup>2</sup>	90	°C/W
T <sub>J</sub>	Operating Junction Temperature Range	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

### NOTE:

1. Pulsed width is limited by the maximum junction temperature.
2. Surface mounted on 1in2 FR-4 board with 2oz. Copper.

## Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
B <sub>V</sub> DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-25	-	-	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	-	-	-1	μA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±30	μA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1	-	-2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A	-	32	50	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3A	-	50	80	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-4A	-	5	-	S
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz	-	830	-	pF
C <sub>oss</sub>	Output Capacitance		-	100	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	75	-	
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-15V, I <sub>D</sub> =-4A V <sub>GS</sub> =-10V	-	13.5	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	2.2	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	2.7	-	
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-15V, I <sub>D</sub> =-1A V <sub>GS</sub> =-10V, R <sub>g</sub> =3.3Ω	-	8	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	5	-	
t <sub>d(off)</sub>	Turn-Off Delay Time		-	22	-	
t <sub>f</sub>	Turn-Off Fall Time		-	5	-	
<b>Diode Characteristics</b>						
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A	-	-	-1.2	V

## Typical Performance Characteristics

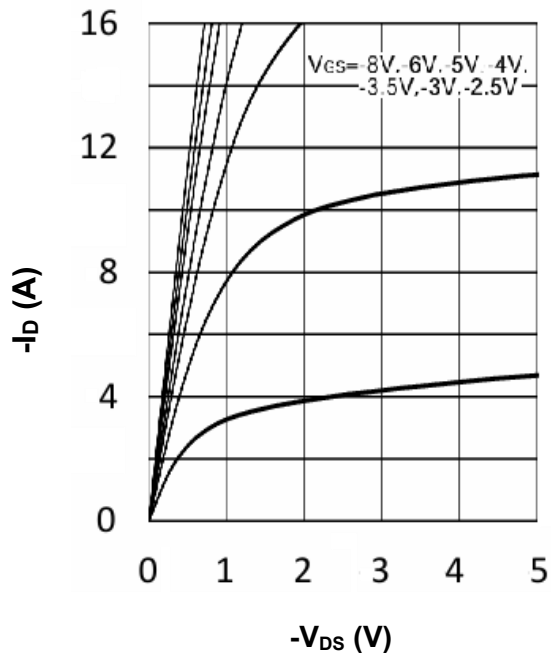


FIG.1 Output Characteristics

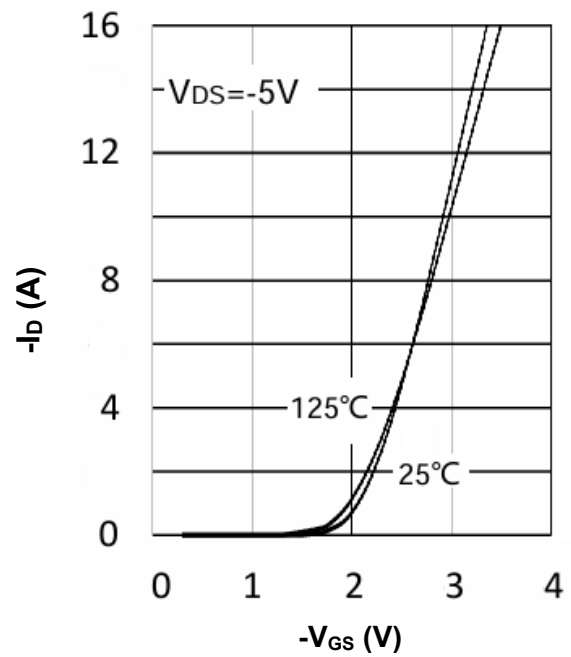


FIG.2 Transfer Characteristics

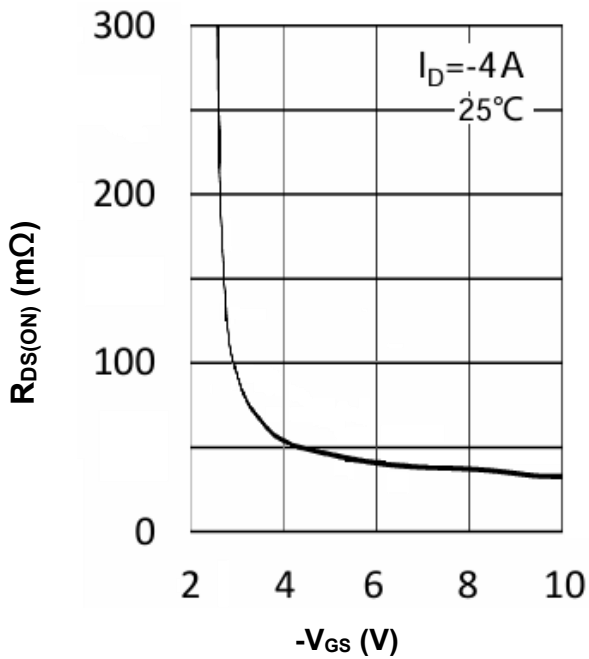


FIG.3 On-Resistance vs. Gate Voltage

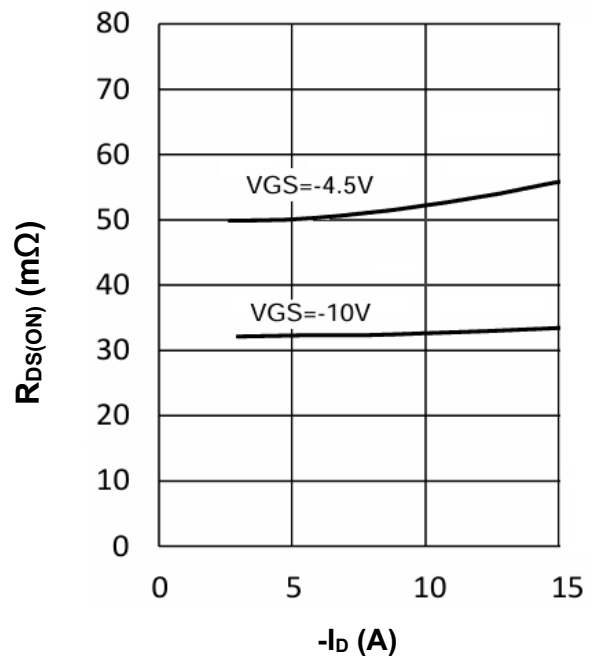


FIG.4 On-Resistance vs. Drain Current

## Typical Performance Characteristics

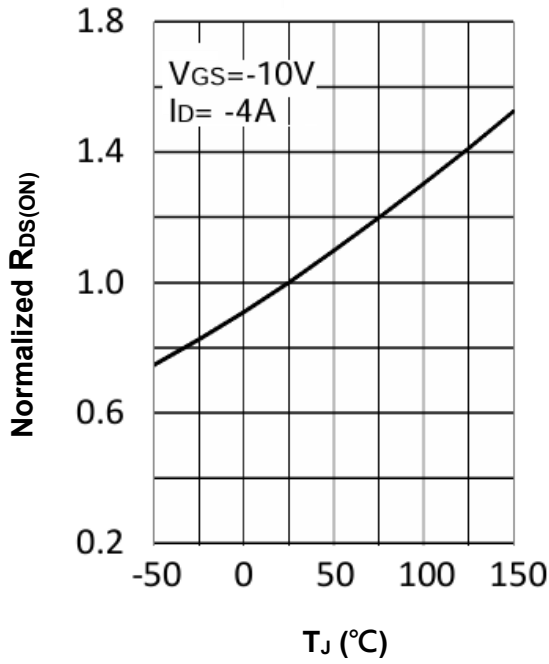


FIG.5 Normalized On-Resistance vs.  $T_J$

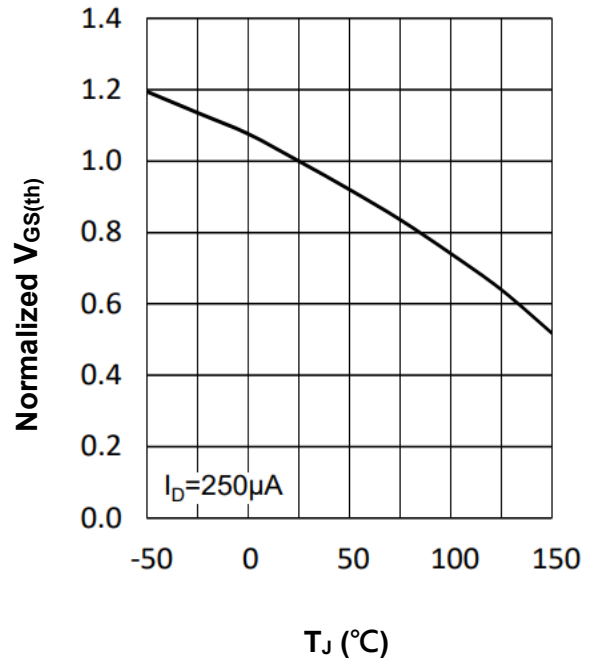


FIG.6 Normalized  $V_{GS(th)}$  vs.  $T_J$

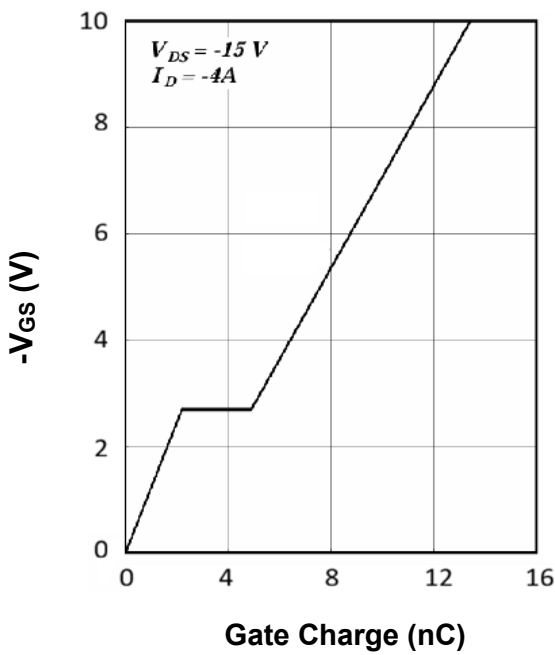


FIG.7 Gate Charge Characteristics

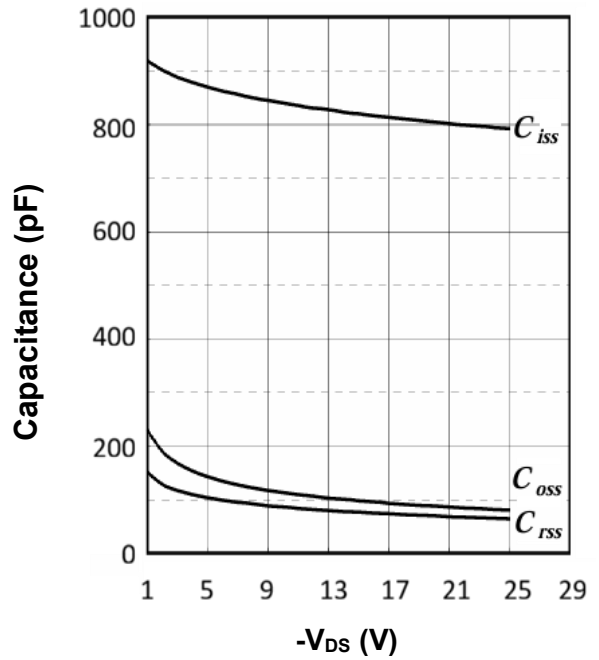


FIG.8 Capacitance Characteristics

## Typical Performance Characteristics

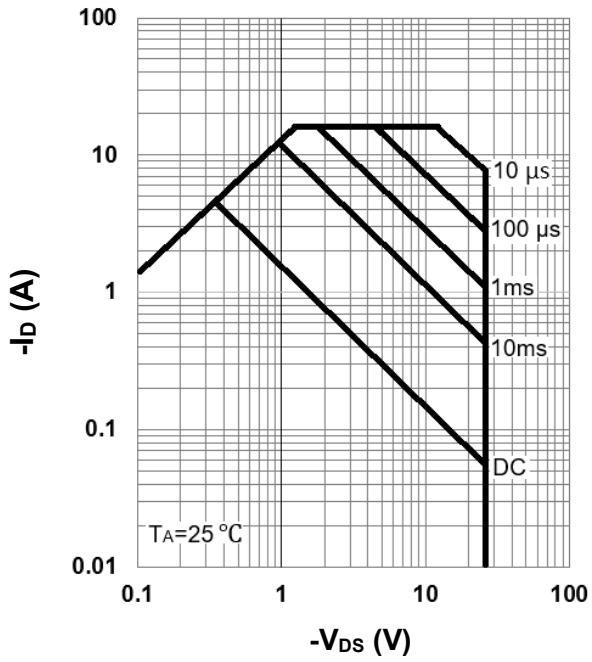


FIG.9 Maximum Safe Operation Area

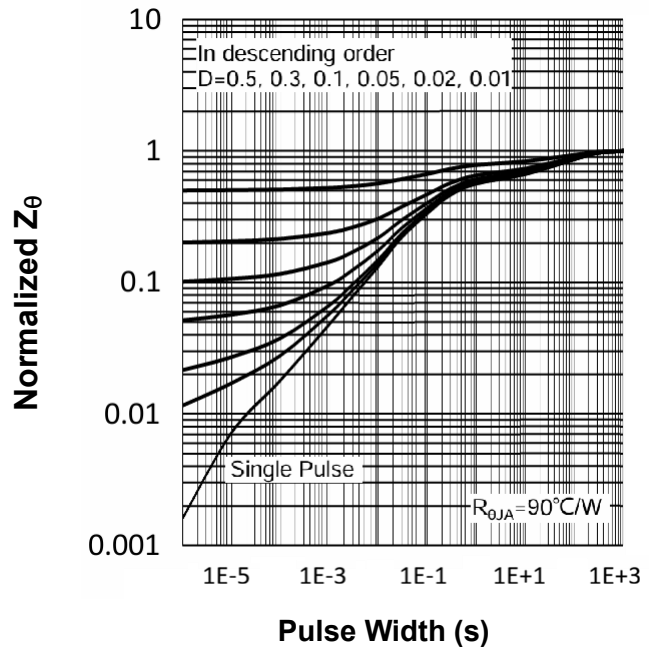
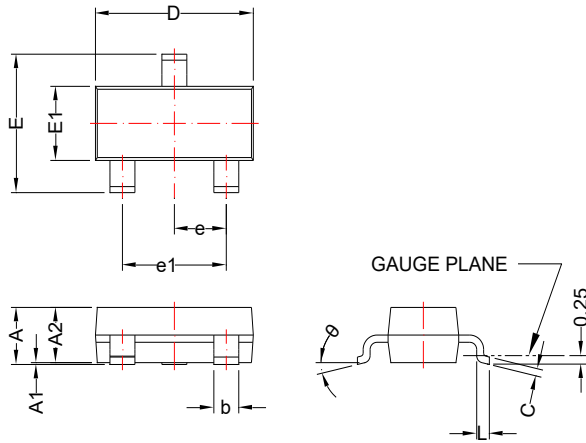


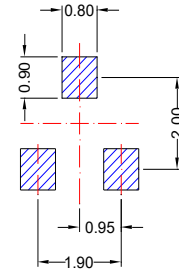
FIG.10 Normalized Transient Impedance

# SOT-23

## Package Dimension (2)



## Recommended Land Pattern



Unit:mm

Dimensions				
Symbol	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.75	1.17	0.030	0.046
A1	0.01	0.15	0.000	0.006
A2	0.70	1.02	0.028	0.040
b	0.30	0.50	0.012	0.020
c	0.08	0.20	0.003	0.008
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E1	1.20	1.40	0.047	0.055
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.3	0.6	0.012	0.024
θ	0°	8°	0°	8°





### NOTE:



1. Dimensions are exclusive of Burrs, Mold Flash and Tie Bar extrusions.

## NOTICE

- Globaltech Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Globaltech Semiconductor products described or contained herein. Globaltech Semiconductor products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Globaltech Semiconductor makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- Information furnished is believed to be accurate and reliable. However Globaltech Semiconductor assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Globaltech Semiconductor. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information without express written approval of Globaltech Semiconductor.

## CONTACT US

GS Headquarter	
	4F, NO.43-1, Lane 11, Sec. 6, Minquan E. Rd Neihu District, Taipei City 114761, Taiwan (R.O.C).
	886-2-2657-9980
	886-2-2657-3630
	<a href="mailto:sales_twn@gs-power.com">sales_twn@gs-power.com</a>

RD Division	
	824 Bolton Drive Milpitas. CA. 95035
	1-408-457-0587