

# GSM0954

## 100V N-Channel MOSFETs

### Product Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

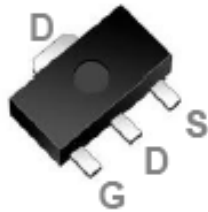
### Features

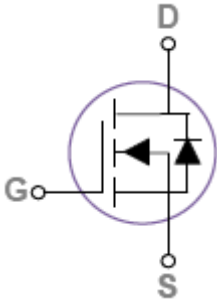
- 100V, 3A,  $R_{DS(ON)}=350m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- SOT-89 package design

### Applications

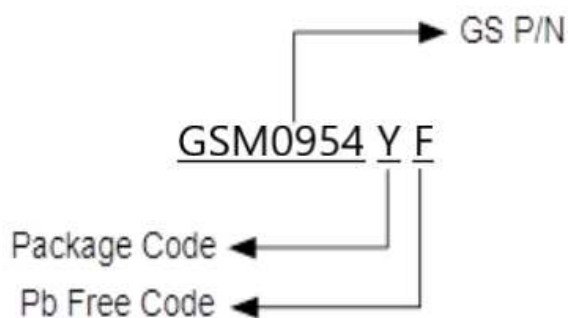
- Networking
- Load Switch
- LED applications

### Packages & Pin Assignments

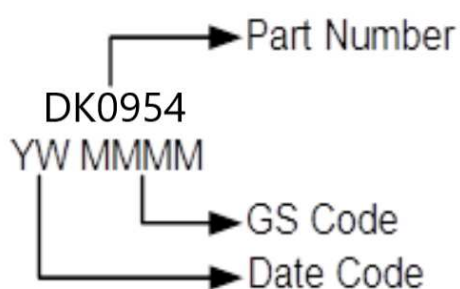
GSM0954YF (SOT-89)	
	
<b>Pin</b>	<b>Description</b>
1	Gate
2	Drain
3	Source



## Ordering Information



## Marking Information



Part Number	Package	Quantity
GSM0954YF	SOT-89	1000pcs

## Absolute Maximum Ratings

$T_C=25^\circ\text{C}$  Unless otherwise noted

Symbol	Parameter	Typical	Unit
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C=25^\circ\text{C}$	3
		$T_C=100^\circ\text{C}$	1.9
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	12	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	6	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	11	A
$P_D$	Power Dissipation ( $T_C=25^\circ\text{C}$ )	1.56	W
	Power Dissipation (Derate above $25^\circ\text{C}$ )	0.06	W/ $^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to +150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to +150	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	80	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	18	$^\circ\text{C}/\text{W}$

## Electrical Characteristics

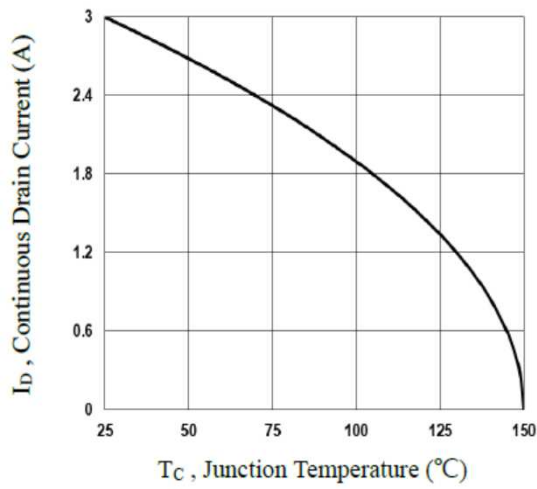
T<sub>J</sub>=25°C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	100			V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA		0.09		V/°C
V <sub>GS(th)</sub>	Gate Threshold Voltage		1.2	1.8	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA		-4.4		mV/°C
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±10V			±100	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			10	
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current			3	A
I <sub>SM</sub>	Pulsed Source Current				6	
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =2A		280	350	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =1A		290	360	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>S</sub> =2A		3		S
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A			1	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, di/dt=100A/μs		70		ns
Q <sub>rr</sub>	Reverse Recovery Charge			114		nC
<b>Dynamic</b>						
Q <sub>g</sub>	Total Gate Charge <sup>3,4</sup>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =1A		5.8	11	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3,4</sup>			0.7	3	
Q <sub>gd</sub>	Gate-Drain Charge <sup>3,4</sup>			2.5	5	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz		480	960	pF
C <sub>oss</sub>	Output Capacitance			25	50	
C <sub>rss</sub>	Reverse Transfer Capacitance			14	28	
t <sub>d(on)</sub>	Turn-On Time <sup>3,4</sup>	V <sub>DD</sub> =50V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω		5.2	10	ns
t <sub>r</sub>				6.8	12	
t <sub>d(off)</sub>	Turn-Off Time <sup>3,4</sup>			14.5	28	
t <sub>f</sub>				2.1	5	
R <sub>g</sub>	Gate resistance		V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz		2	

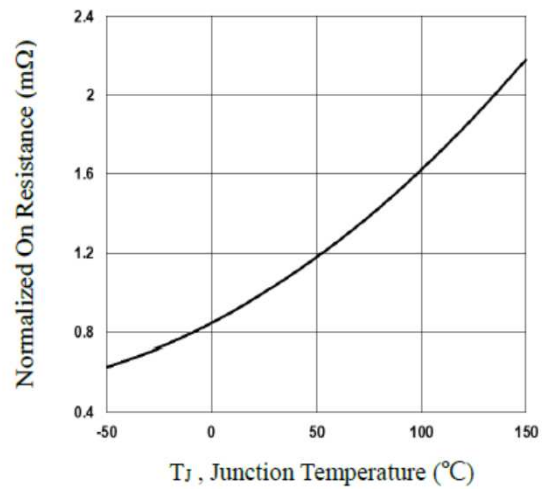
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=50V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=11A., R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

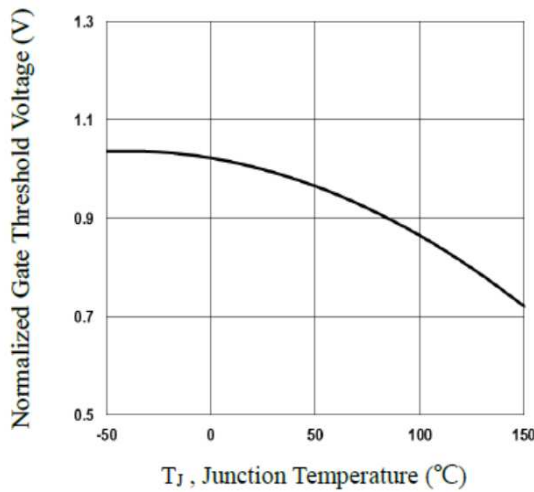
## Typical Performance Characteristics



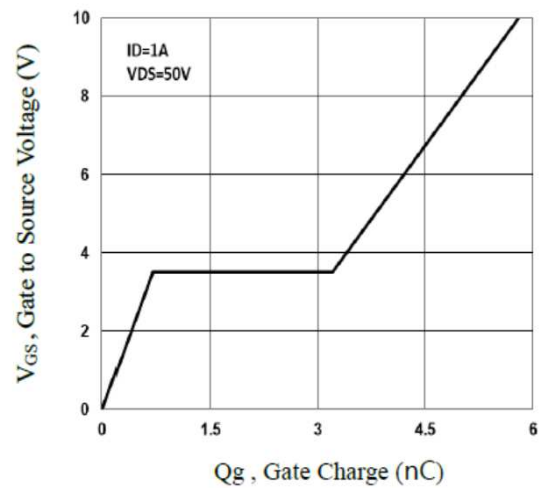
**Fig.1 Continuous Drain Current vs.  $T_c$**



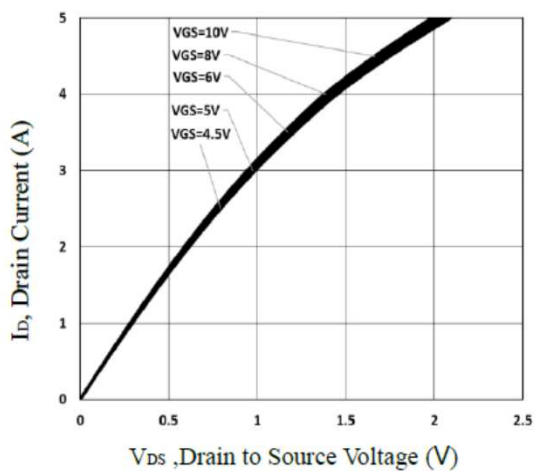
**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$**



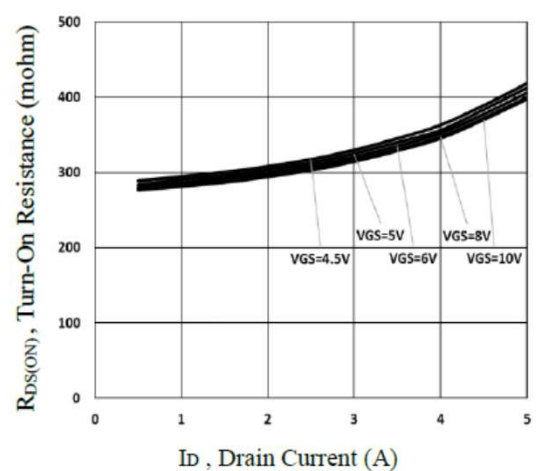
**Fig.3 Normalized  $V_{th}$  vs.  $T_j$**



**Fig.4 Gate Charge Waveform**

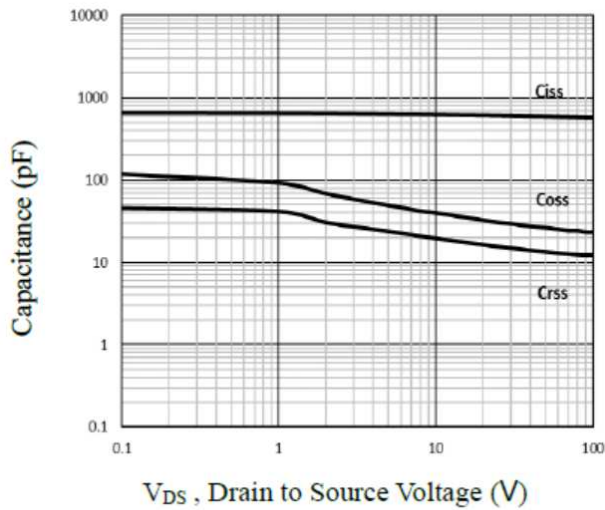


**Fig.5 Typical Output Characteristics**

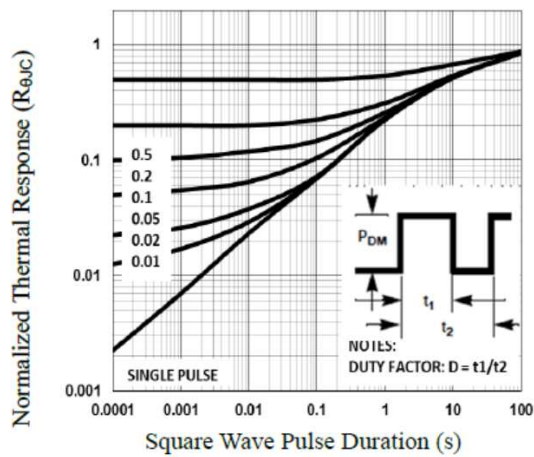


**Fig.6 Turn-On Resistance vs.  $I_D$**

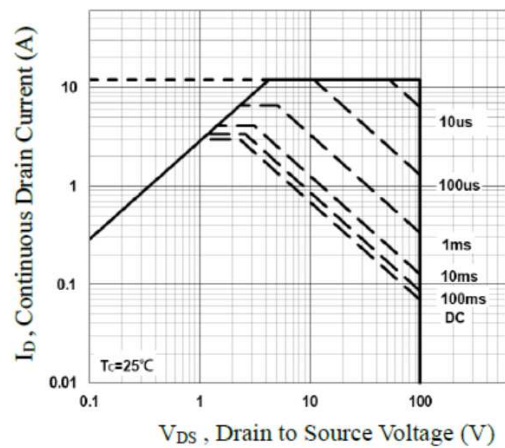
## Typical Performance Characteristics (Continue)



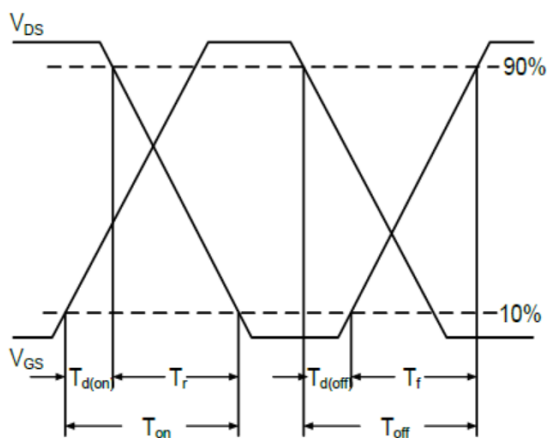
**Fig.7 Capacitance Characteristics**



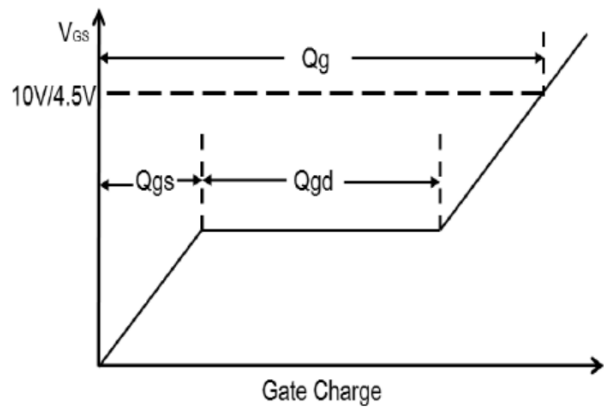
**Fig.8 Normalized Transient Impedance**



**Fig.9 Maximum Safe Operation Area**



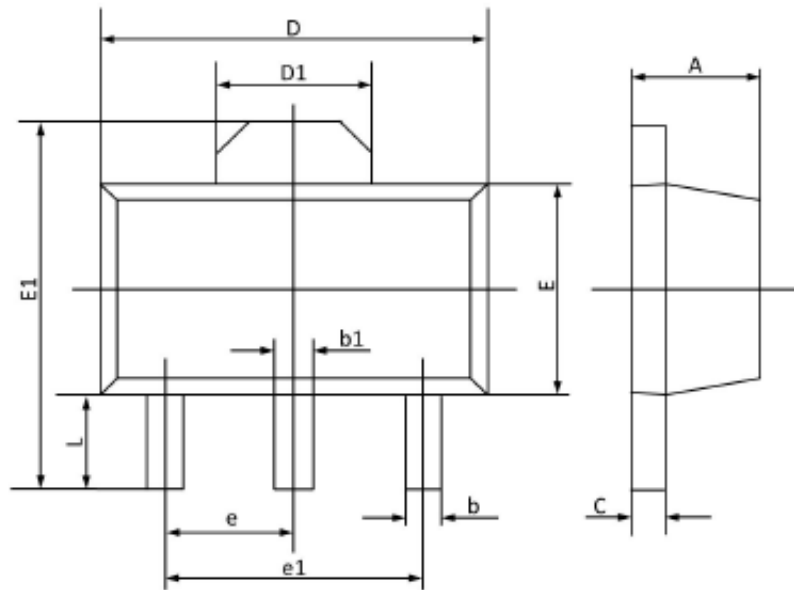
**Fig.10 Switching Time Waveform**



**Fig.11 Gate Charge Waveform**

Package Dimension

# SOT-89









Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
<b>A</b>	1.400	1.600	0.055	0.063
<b>b</b>	0.320	0.520	0.013	0.020
<b>b1</b>	0.400	0.580	0.016	0.023
<b>c</b>	0.350	0.440	0.014	0.017
<b>D</b>	4.400	4.600	0.173	0.181
<b>D1</b>	1.550 (REF)		0.061 (REF)	
<b>E</b>	2.300	2.600	0.091	0.102
<b>E1</b>	3.940	4.250	0.155	0.167
<b>e</b>	1.500 (TYP)		0.060 (TYP)	
<b>e1</b>	3.000 (TYP)		0.118 (TYP)	
<b>L</b>	0.900	1.200	0.035	0.047

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