

# GSM0906

## 100V N-Channel MOSFETs

### Product Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

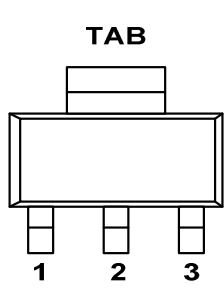
### Features

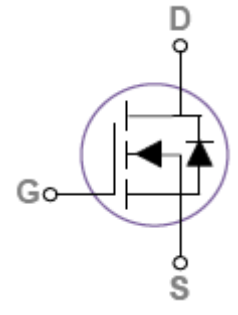
- 100V, 6.5A,  $R_{DS(ON)}=95m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS guaranteed
- Green Device Available
- SOT-223 package design

### Applications

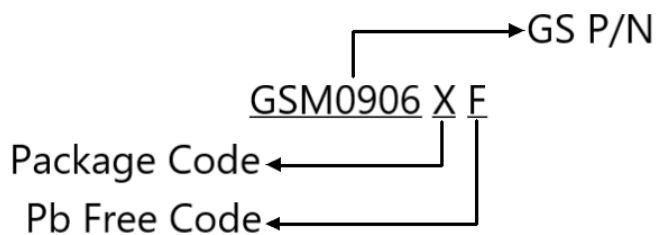
- Notebook
- Load Switch
- LED applications

### Packages & Pin Assignments

GSM0906XF (SOT-223)	
	
1	Gate
2	Drain
3	Source

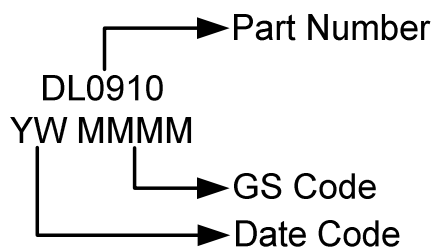


## Ordering Information



Part Number	Package	Quantity
GSM0906XF	SOT-223	2500pcs

## Marking Information



## Absolute Maximum Ratings

$T_C=25^{\circ}\text{C}$  Unless otherwise noted

Symbol	Parameter	Typical	Unit
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C=25^{\circ}\text{C}$	6.5
		$T_C=100^{\circ}\text{C}$	4.1
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	26	A
$P_D$	Power Dissipation ( $T_A=25^{\circ}\text{C}$ )	9	W
	Power Dissipation (Derate above $25^{\circ}\text{C}$ )	0.072	W/ $^{\circ}\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to +150	$^{\circ}\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to +150	$^{\circ}\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	14	$^{\circ}\text{C}/\text{W}$

## Electrical Characteristics

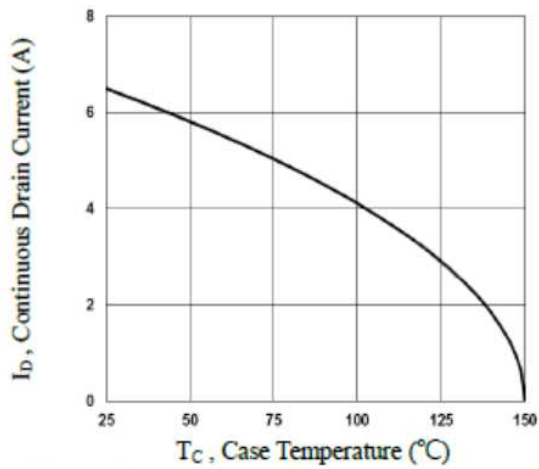
T<sub>J</sub>=25°C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	100			V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA		0.05		V/°C
V <sub>GS(th)</sub>	Gate Threshold Voltage		1.2	1.6	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA		-5		mV/°C
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			10	
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current			6.5	A
I <sub>SM</sub>	Pulsed Source Current				26	
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =5A		80	95	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A		85	110	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =3A		8.7		S
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A			1	V
<b>Dynamic</b>						
Q <sub>g</sub>	Total Gate Charge <sup>2,3</sup>	V <sub>DS</sub> =48V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A		22	44	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>2,3</sup>			3.9	8	
Q <sub>gd</sub>	Gate-Drain Charge <sup>2,3</sup>			5.2	10	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz		1480	2150	pF
C <sub>oss</sub>	Output Capacitance			480	700	
C <sub>rss</sub>	Reverse Transfer Capacitance			35	55	
t <sub>d(on)</sub>	Turn-On Time <sup>2,3</sup>	V <sub>DD</sub> =30V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω		2.9	6	ns
t <sub>r</sub>				9.5	18	
t <sub>d(off)</sub>	Turn-Off Time <sup>2,3</sup>			18.4	35	
t <sub>f</sub>				5.3	10	
R <sub>g</sub>	Gate Resistance		V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz		1.3	

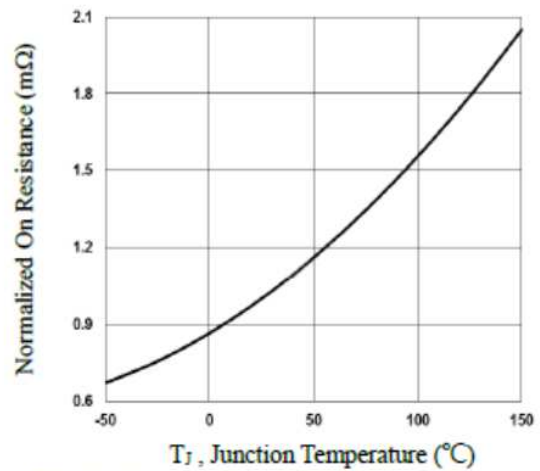
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
3. Essentially independent of operating temperature.

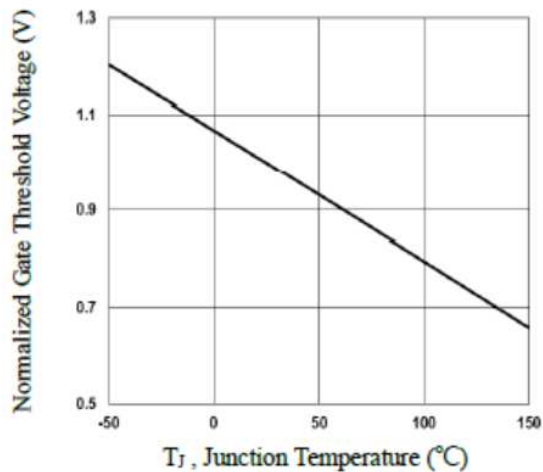
## Typical Performance Characteristics



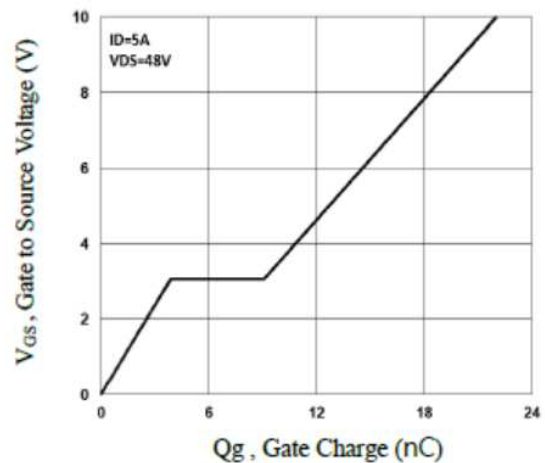
**Fig.1 Continuous Drain Current vs.  $T_C$**



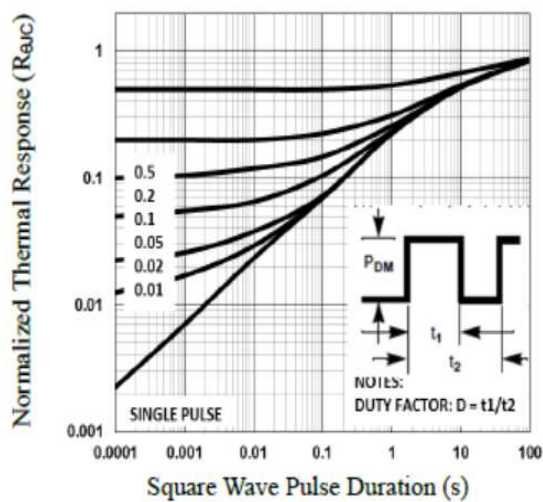
**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$**



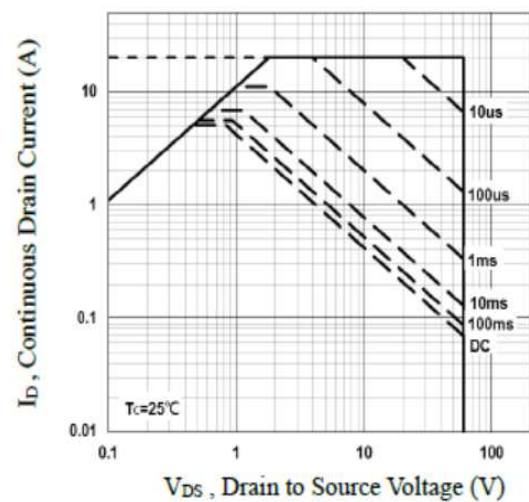
**Fig.3 Normalized  $V_{th}$  vs.  $T_J$**



**Fig.4 Gate Charge Waveform**



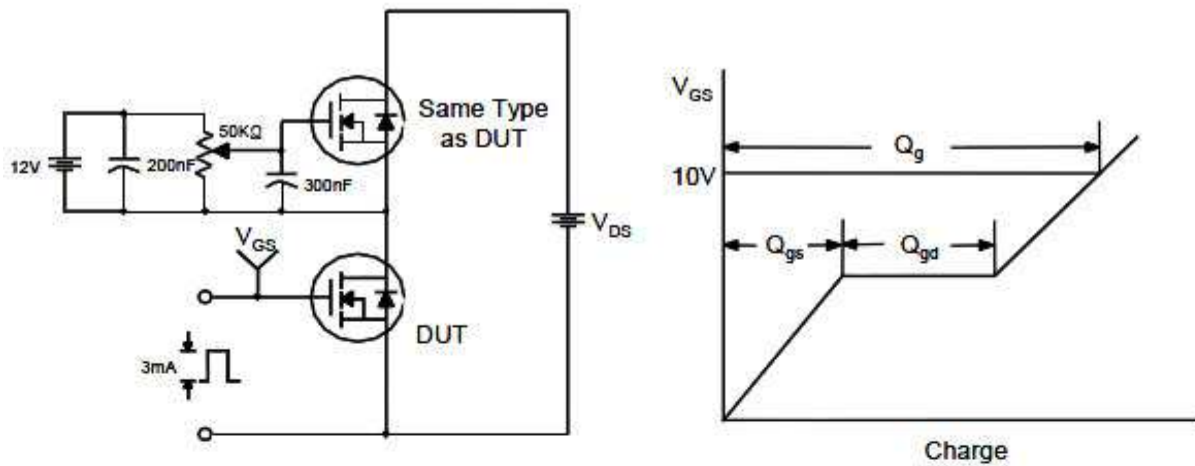
**Fig.5 Normalized Transient Impedance**



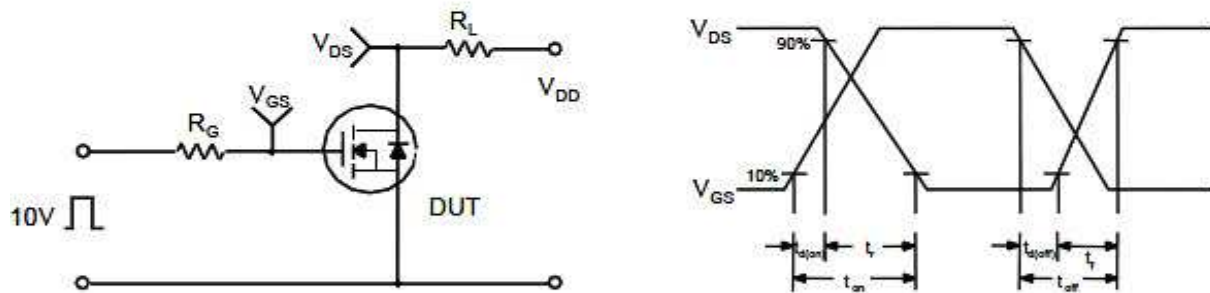
**Fig.6 Maximum Safe Operation Area**

## Typical Performance Characteristics (Continue)

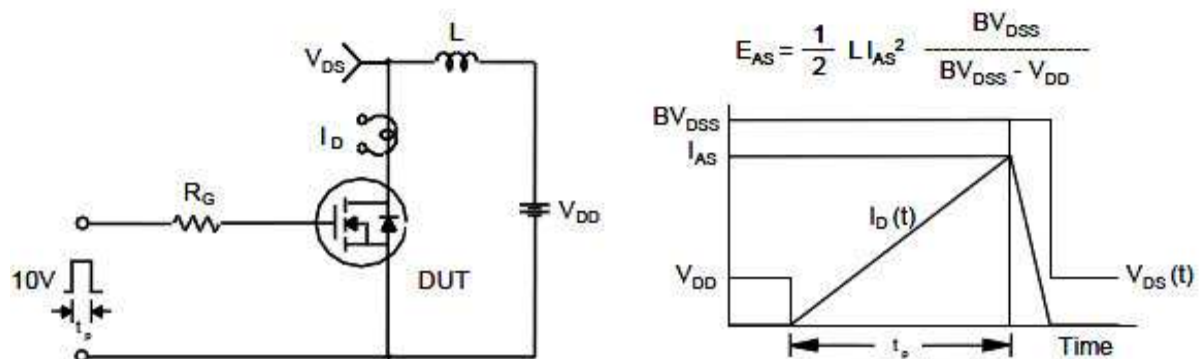
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

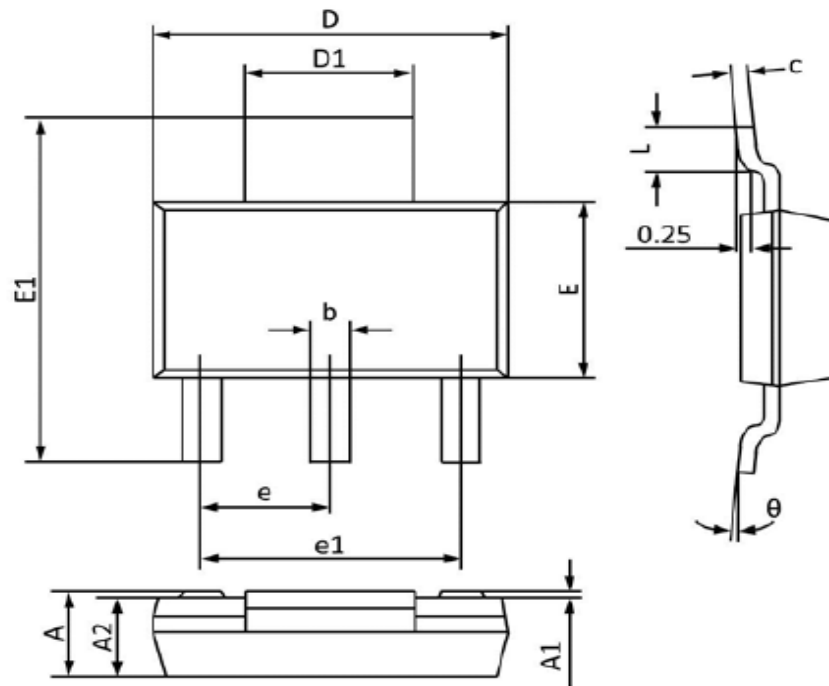


Unclamped Inductive Switching Test Circuit & Waveforms



## Package Dimension

### SOT-223







### Dimensions



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
<b>A</b>	1.520	1.800	0.060	0.071
<b>A1</b>	0.000	0.100	0.000	0.004
<b>A2</b>	1.500	1.700	0.059	0.067
<b>b</b>	0.660	0.820	0.026	0.032
<b>c</b>	0.250	0.350	0.010	0.014
<b>D</b>	6.200	6.400	0.244	0.252
<b>D1</b>	2.900	3.100	0.114	0.122
<b>E</b>	3.300	3.700	0.130	0.146
<b>E1</b>	6.830	7.070	0.269	0.278
<b>e</b>	2.300 (BSC)		0.091 (BSC)	
<b>e1</b>	4.500	4.700	0.177	0.185
<b>L</b>	0.900	1.150	0.035	0.045
<b>θ</b>	0°	10°	0°	10°

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