GS1117A Series

1A Low Dropout Voltage Regulator

Product Description

The GS1117A is a low dropout three-terminal regulator with 1A output current capability.

The GS1117A provides current limit and thermal shutdown protection functions. On-chip thermal shutdown provides protection against a combination of high current and ambient temperature that would create excessive junction temperature.

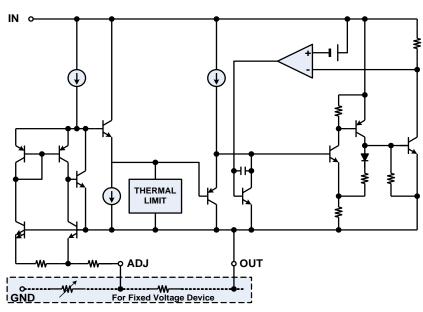
The GS1117A is available in 1.2V, 1.5V, 1.8V, 2.5V, 3.3V and 5.0V fixed output voltage versions and ADJ output voltage version. And it is available in three leads SOT-89, SOT-223 and TO-252(A2) surface mount packages.

Features

- Adjustable or Fixed Output
- Output Current of 1A
- Output Voltage Accuracy within ±.2.0%
- RoHS Compliant & Halogen Free

Applications

- Battery-Power Circuitry
- Post Regulator for Switching Power Supply
- Low Voltage Logic Suppliers
- ADSL Modem
- Power Management for Computer Mother Board and Graphic Card



Block Diagram



Packages & Pin Assignments

	TO-252(A2)		SOT-223	SOT-89	
Pin	GS1117AD	Pin	GS1117AX	Pin	GS1117AY
1	GND/ADJ	1	GND/ADJ	1	GND/ADJ
2	Vout	2	Vout	2	Vouт
3	VIN	3	V _{IN}	3	Vin

Ordering and Marking Information

Ordering Information				
TO-252(A2)	Output			
GS1117ADF	GS1117AXF	GS1117AYF	ADJ	
GS1117AD12F	GS1117AX12F	GS1117AY12F	1.2V	
GS1117AD15F	GS1117AX15F	GS1117AY15F	1.5V	
GS1117AD18F	GS1117AX18F	GS1117AY18F	1.8V	
GS1117AD25F	GS1117AX25F	GS1117AY25F	2.5V	
GS1117AD33F	GS1117AX33F	GS1117AY33F	3.3V	
GS1117AD50F	GS1117AX50F	GS1117AY50F	5.0V	

G S 1 1 1 7 A 122 F

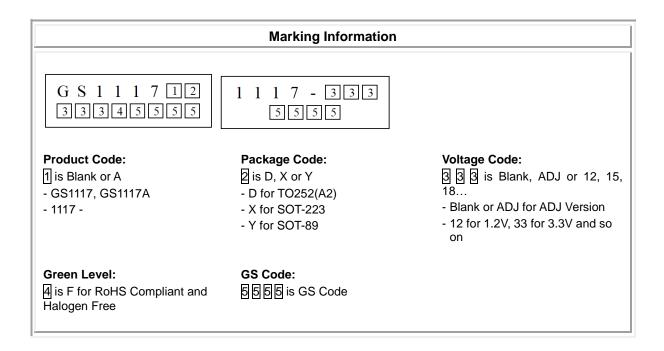
Product Name: GS1117A Package Code: is D, X or Y - D for TO252(A2) - X for SOT-223 - Y for SOT-89

F Suffix: Stands for RoHS Compliant and Halogen Free

Voltage Code:

2 2 is Blank or 12, 15, 18...
Blank for ADJ Version
12 for 1.2V, 33 for 3.3V and so on





Absolute Maximum Ratings (Ta=+25 °C, unless otherwise specified.)

Symbol	Parameter	Maximum		Unit
V _{IN}	Input Voltage	18		V
TJ	Junction Temperature	150		°C
T _{STG}	Storage temperature Range	-65 to	150	°C
T _{LEAD}	Lead Temperature (Soldering, 10 sec)	30	0	°C
	Thermal Resistance Junction to Ambient (Condition: No heat sink)	SOT-223	120	⁰C/W
θ _{JA}		SOT-89	250	
		TO-252(A2)	100	
		SOT-223	0.9	
PD	Power Dissipation	SOT-89	0.5	W
		TO-252(A2)	1.2	

Stresses above those listed "Absolute Maximum Ratings" above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied.

Exposure to Absolute Maximum Ratings conditions for extended periods may affect device reliability.

Recommended Operating Conditions

Parameter Name	Min.	Max.	Unit
Input Voltage Range	-	15	V
Junction Temperature	-40	125	°C



Parameter	Device	Conditions	Min.	Тур.	Max.	Unit
D (V _{IN} = 2.75V, I _{OUT} =10mA	1.238	1.250	1.262	
Reference Voltage	GS1117A-Adj	1.4V≤ V _{IN} -V _{OUT} ≤12V, 10mA≤ I _{OUT} ≤1A	1.225	1.250	1.270	V
	0011170 1 0	V _{IN} =3.2V, I _{OUT} =10mA	1.182	1.200	1.218	V
	GS1117A-1.2	3.0V≤ V _{IN} ≤12V, 0≤ I _{OUT} ≤1.0A	1.176	1.200	1.224	V
	0044774.4.5	V _{IN} =3.5V, I _{OUT} =10mA	1.477	1.500	1.523	V
	GS1117A-1.5	3.0V≤ V _{IN} ≤12V, 0≤ I _{OUT} ≤1.0A	1.470	1.500	1.530	V
	0011170.4.0	V _{IN} =3.8V, I _{OUT} =10mA	1.773	1.800	1.827	V
Output	GS1117A-1.8	3.2V≤ V _{IN} ≤12V, 0≤ I _{OUT} ≤1.0A	1.764	1.800	1.836	V
Voltage	0011170.05	V _{IN} =4.5V, I _{OUT} =10mA	2.463	2.500	2.537	V
	GS1117A-2.5	3.9V≤ V _{IN} ≤12V, 0≤ I _{OUT} ≤1.0A	2.450	2.500	2.550	V
	GS1117A-3.3	V _{IN} =5V, I _{OUT} =10mA	3.250	3.300	3.350	V
	GS1117A-3.3	4.75V≤ V _{IN} ≤12V, 0≤ I _{OUT} ≤1.0A	3.234	3.300	3.366	V
	GS1117A-5.0	V _{IN} =7.0V V, I _{OUT} =10mA	4.925	5.000	5.075	V
		6.5V≤ V _{IN} ≤12V, 0≤ I _{OUT} ≤1.0A	4.900	5.000	5.100	V
Line	GS1117A-Adj	$1.4V \le V_{IN}-V_{OUT} \le 10.75V,$ Iout =10mA			0.2	%
Regulation	GS1117A-XX	$(1.4V+V_{OUT}) \le V_{IN} \le 12V,$ $I_{OUT} = 10mA$		5	15	mV
Load	GS1117A-Adj	VIN-VOUT=3V, 10mA ≤ IOUT ≤ 1A			1.0	%
Regulation (Note 1 & 2)	GS1117A-XX	V _{IN} -V _{OUT} = 1.4V, 0≤ I _{OUT} ≤1.0A		6	15	mV
		\triangle Vout, Δ Vref =1%, I _{OUT} =100mA		1.0	1.2	V
Dropout Voltage	All	Δ Vout, Δ Vref=1%, I _{OUT} =500mA		1.05	1.25	V
voltage		\triangle Vout, Δ Vref=1%, I _{OUT} =1.0A		1.1	1.3	V
Current Limit	All	VIN - VOUT=2.0V, TJ=25°C	1	1.4		А
Ground Pin Current	GS1117A-XX	V _{IN} = V _{OUT} +1.5V		4	8	mA
Minimum Load Current (Note 3)	GS1117A-Adj			5	10	mA
Adjust Pin Current	GS1117A-Adj			50	120	μA
Temperature Stability	All			0.5		%

Electrical Characteristics (Ta=+25 °C, unless otherwise specified.)

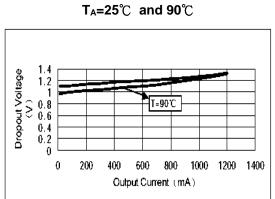
Note 1: The Parameters of Line Regulation and Load Regulation in above are tested under the room temperature. The Curve of Load Regulation vs. Temperature is shown in the section of typical performance characteristics.

Note 2: The parameter can meet the specs when lout varies from 0 to 1.0A, Vin-Vout varies from 1.4V to 12V at room temperature. It required the output current larger than 10mA to satisfy the criterion once the temperature varies between -40° C and 125° C.

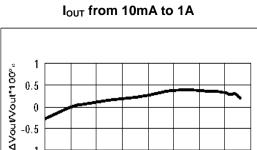
Note 3: Minimum Load Current is specified for adjustment version and it is the required minimum load current for regulating output voltage within the spec.

Typical Performance Characteristics

Dropout Voltage (VIN-VOUT)



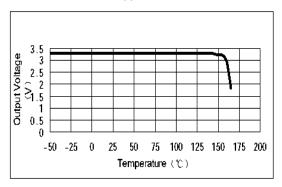
Load Regulation V.S Temperature



₹ -0.5 -1 -50 -25 0 25 50 75 100 125 150 Temperature (°C)

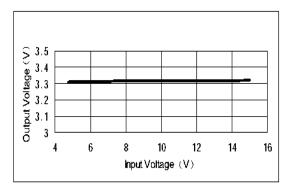


Vout=3.3V



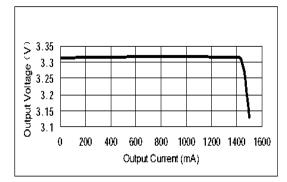
Line Regulation

VOUT=3.3V, IOUT=10mA

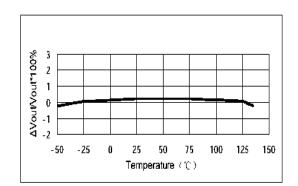


Load Regulation

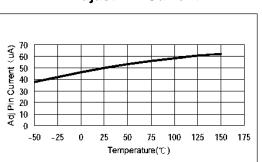
Vout=3.3V, Iout=10mA





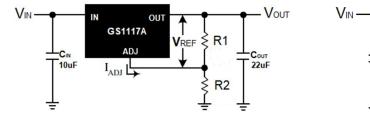






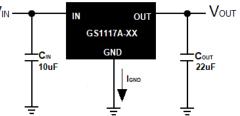
Adjust Pin Current

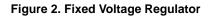
Typical Applications



Vout=VREF (1+R2/R1)+IADJR2







GS1117A is a series of low dropout voltage three terminal regulators. Its application circuit is very simple, the fixed version only needs two capacitors and the adjustable version only needs two resistors and two capacitors to work. It is composed of some modules including start-up circuit, bias circuit, bandgap, thermal shutdown, current limit, power transistors and its driver circuit and so on.

The thermal shut down and current limit modules can assure chip and its application system working safety when the junction temperature is larger than 125° or output current is larger than 1.4A typically.

The bandgap module provides stable reference voltage, whose temperature coefficient is compensated by careful design considerations. The accuracy of output voltage is guaranteed by trimming technique.

Application Hints

- 1. Recommend using 10µF tantalum capacitor for input capacitor C_{IN} for all of application circuits.
- 2. Recommend using 22µF tantalum capacitor for output capacitor COUT to assure circuit stability.
- 3. The adjust pin can be bypassed to ground with a bypass capacitor (C_{ADJ}) to improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. At any ripple frequency, the impedance of the C_{ADJ} must be less than R1 to prevent the ripple from being amplified:

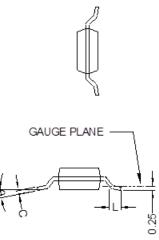
 $1 / (2\pi \times f_{RIPPLE} \times C_{ADJ}) < R1$

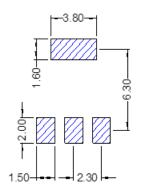
The R1 is the resistor between the output and the adjust pin. The value is normally in the range of 100 Ω to 200 Ω . For example, with R1 = 124 Ω and f_{RIPPLE} = 120 Hz, the C_{ADJ} must be > 11µF.



SOT-223

Package Dimension





Recommended Land Pattern

	Dimensions				
	Millimeters		Inches		
SYMBOL	MIN	MAX	MIN	MAX	
Α	1.50	1.80	0.059	0.071	
A1	0.02	0.12	0.001	0.005	
A2	1.45	1.75	0.057	0.069	
b	0.60	0.84	0.024	0.033	
b1	2.90	3.10	0.114	0.122	
С	0.23	0.35	0.009	0.014	
D	6.20	6.70	0.244	0.264	
Е	6.70	7.30	0.264	0.287	
E1	3.30	3.70	0.130	0.146	
е	2.30	BSC	0.091 BSC		
e1	4.60 BSC 0.181 BSC		BSC		
L	0.75	-	0.030	-	
θ	0 °	10°	0 °	10°	

NOTE: Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.



www.gs-power.com

Package Dimension

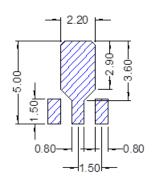
Ψ

b

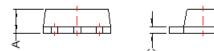
Type I Type II

BACKSIDE VIEW

þ



Recommended Land Pattern



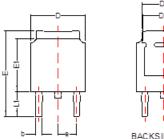
	Dimensions				
	Millimeters		Inches		
SYMBOL	MIN	MAX	MIN	MAX	
Α	1.40	1.60	0.055	0.063	
b	0.30	0.55	0.012	0.022	
b1	0.40	0.60	0.016	0.024	
С	0.35	0.44	0.014	0.017	
D	4.40	4.60	0.173	0.181	
D1	1.40	1.83	0.055	0.072	
D2	1.75	REF	0.069 REF		
E	3.94	4.25	0.155	0.167	
E1	2.30	2.60	0.091	0.102	
E2	2.84 REF 0.112 REF			2 REF	
е	1.50 BSC 0.059 BSC		9 BSC		
e1	3.00 BSC 0.118 BSC		B BSC		
L	0.89	1.20	0.035	0.047	

NOTE: Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.

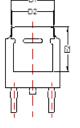


TO-252(A2)

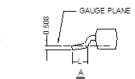
Package Dimension



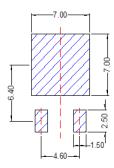
iii



BACKSIDE VIEW



Recommended Land Pattern



	Dimensions				
	Millin	neters	Inches		
SYMBOL	MIN	МАХ	MIN	MAX	
Α	2.18	2.40	0.086	0.094	
A1	0.00	0.15	0.000	0.006	
b	0.64	0.90	0.025	0.035	
b1	0.76	1.14	0.030	0.045	
С	0.40	0.89	0.016	0.035	
c1	0.40	0.61	0.016	0.024	
D	6.35	6.73	0.250	0.265	
D1	4.95	5.46	0.195	0.215	
D2	4.32	-	0.170	-	
E	9.40	10.41	0.370	0.410	
E1	5.97	6.22	0.235	0.245	
E2	4.95	-	0.195	-	
е	2.286 BSC		0.090) BSC	
L	1.40	1.77	0.055	0.070	
L1	2.67	3.07	0.105	0.121	
L2	-	1.20	· ·	0.047	
θ	0°	8°	0°	8°	

NOTE: Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.



- Globaltech Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Globaltech Semiconductor products described or contained herein. Globaltech Semiconductor products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.
- Applications shown on the herein document are examples of standard use and operation. Customers are
 responsible in comprehending the suitable use in particular applications. Globaltech Semiconductor makes no
 representation or warranty that such applications will be suitable for the specified use without further testing or
 modification.
- Information furnished is believed to be accurate and reliable. However Globaltech Semiconductor assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Globaltech Semiconductor. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information without express written approval of Globaltech Semiconductor.

CONTACT US

	GS Headquarter				
	4F.,No.43-1,Lane11,Sec.6,Minquan E.Rd Neihu District Taipei City 114, Taiwan (R.O.C)				
G	886-2-2657-9980				
Q•	886-2-2657-3630				
@	sales_twn@gs-power.com				

	RD Division
	824 Bolton Drive Milpitas. CA. 95035
G	1-408-457-0587

