# GSM6911PJZF

# **60V P-Channel MOSFETs**

## **Product Description**

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

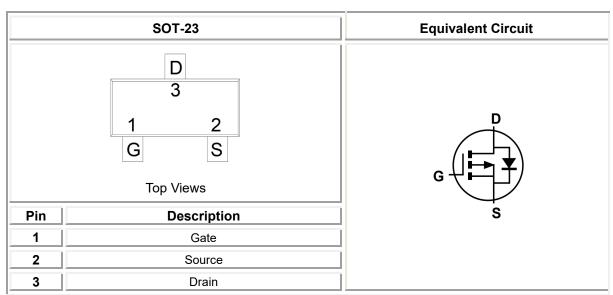
#### **Features**

- -60V, -3.1A,  $R_{DS(ON)}$ =190m $\Omega$ @ $V_{GS}$ =-10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available
- SOT-23 package design
- RoHS Compliant and Halogen Free

#### **Applications**

- Motor Drive
- **Power Tools**
- **LED Lighting**

## **Packages & Pin Assignments**





# **Ordering and Marking Information**

Ordering Information				
Part Number	Package	Package Part Marking Quantity		
GSM6911PJZF	SOT-23	M	3,000 PCS	
GSM6911P 1 2				
- <b>Product Code:</b> GSM6911P	- GSM6011P - Package Code: - Green Level:		2 is <b>F</b> for RoHS Compliant and	
	Marking In	formation		
M	- Product Code:	:		

# Absolute Maximum Ratings (TA=25°C Unless otherwise noted)

Symbol	Parameter		Value	Unit
V <sub>DS</sub>	Drain-Source Voltage		-60	V
V <sub>G</sub> s	Gate-Source Voltage		±20	V
	Ocation on Dusin Ocasant	T <sub>A</sub> =25°C	-3.1	A
I <sub>D</sub>	Continuous Drain Current	T <sub>A</sub> =100°C	-2	
Ірм	Pulsed Drain Current		-12.4	Α
EAS	Single Pulse Avalanche Energy		32	mJ
IAS	Single Pulse Avalanche Current		-8	Α
	Power Dissipation (T <sub>A</sub> =25°ℂ)		1.56	W
P <sub>D</sub>	Power Dissipation (Derate above 25℃)		0.012	W/°C
TJ	Operating Junction Temperature Range		-50 to +150	$^{\circ}\mathbb{C}$
Тѕтс	Storage Temperature Range		-50 to +150	$^{\circ}$ C
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient		80	°C/W

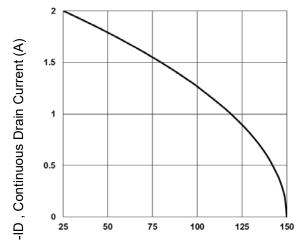


# **Electrical Characteristics** (T<sub>A</sub>=25°C Unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
	Static o	characteristics				
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-60			V
∆BV <sub>DSS</sub> /∆T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient Reference to 25℃, I <sub>D</sub> =-1mA			-0.05		V/°C
$V_{\text{GS(th)}}$	Gate Threshold Voltage  V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA		-1.2	-1.9	-2.5	V
$\triangle V_{GS(th)}$				5		mV/ ℃
Igss	Gate Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V			-1	
IDSS		V <sub>DS</sub> =-48V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			-10	uA
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V,			-3.1	A
Ism	Pulsed Source Current	Force Current			-12.4	
D-ac	Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-3A		160	190	mΩ
R <sub>DS(on)</sub>	Diain-Source On-Resistance	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.5A		200	240	
<b>g</b> FS	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3A		3.5		S
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A			-1	V
	Dynamic	c characteristics				
Qg	Total Gate Charge			8.2	12	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-3A		1.8	3.6	
Q <sub>gd</sub>	Gate-Drain Charge			1.5	3	
Ciss	Input Capacitance			425	615	pF
Coss	Output Capacitance	V <sub>DS</sub> =-30V,V <sub>GS</sub> =0V, f=1MHz		35	50	
Crss	Reverse Transfer Capacitance	2		20	30	
t <sub>d(on)</sub>	T 0 T			5.2	10	
t <sub>r</sub>	Turn-On Time	V <sub>DD</sub> =-30V, I <sub>D</sub> =-1A,		19	36	ns
t <sub>d(off)</sub>		$V_{GS}$ =-10V, $R_G$ =6 $\Omega$		35	67	
t <sub>f</sub>	Turn-Off Time			10.6	20	

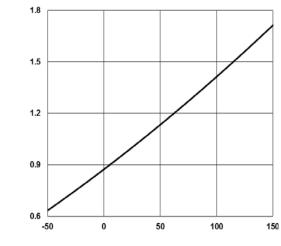


### **Typical Performance Characteristics**



T<sub>C</sub> , Case Temperature (°C)

Normalized On Resistance (m $\Omega$  )



T」, Junction Temperature (°C)

Fig.1 Continuous Drain Current vs. Tc



Fig.2 Normalized RDS<sub>(ON)</sub> vs. T<sub>J</sub>

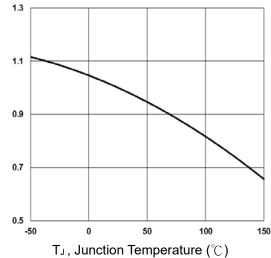


Fig.3 Normalized Vth vs. TJ



10 ID=-2A VDS=-30V 8 6 2 0 Qg, Gate Charge (nC)

Fig.4 Gate Charge Waveform

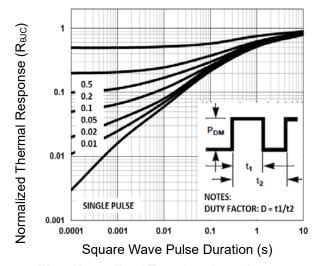
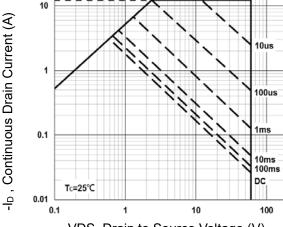


Fig.5 Normalized Transient Impedance



-VDS, Drain to Source Voltage (V)

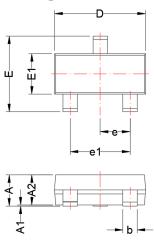
Fig.6 Maximum Safe Operation Area

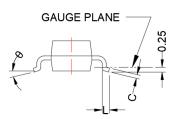


Normalized Gate Threshold Voltage (V)

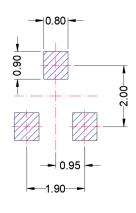
# **SOT-23**

# **Package Dimension**





#### **Recommended Land Pattern**



Unit:mm

	Dimensions				
0	Millimeters		Inches		
Symbol	MIN	MAX	MIN	MAX	
Α	0.75	1.17	0.030	0.046	
<b>A</b> 1	0.01	0.15	0.000	0.006	
A2	0.70	1.02	0.028	0.040	
b	0.30	0.50	0.012	0.020	
С	0.08	0.20	0.003	0.008	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E1	1.20	1.40	0.047	0.055	
е	0.95	0.95 BSC 0.037 BSC			
e1	1.90 BSC		0.075 BSC		
L	0.30	0.60	0.012	0.024	
θ	0°	8°	0°	8°	

#### NOTE:

Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.



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