

GSM3660ETF

30V N-Channel MOSFET

Product Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

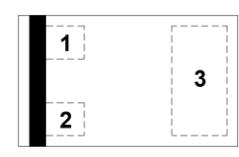
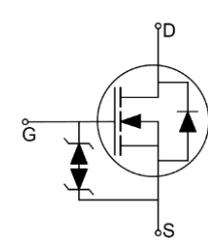
Features

- $R_{DS(ON)} = 0.48\Omega @ V_{GS}=10V$
- ESD Protected
- DFN1006-3L Package
- RoHS Compliant and Halogen Free

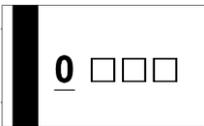
Applications

- Load Switch
- Portable Appliances

Packages & Pin Assignments

DFN1006-3L			Equivalent Circuit		
 Top View (Transparent)					
Pin	Symbol	Description	Pin	Symbol	Description
1	G	Gate	3	D	Drain
2	S	Source			

Ordering and Marking Information

Ordering Information			
Part Number	Package	Part Marking	Quantity / Reel
GSM3660ETF	DFN1006-3L	0□□□	10,000 PCS
GSM3660E ① ② - Product Code: GSM3660E - Package Code: ① is T for DFN1006-3L - Green Level: ② is F for RoHS Compliant and Halogen Free			
Marking Information			
 - Product Code: 0 - GS Code: □□□			

Absolute Maximum Ratings (T_A = 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	
V _{DSS}	Drain-Source Voltage	30	V	
V _{GSS}	Gate-Source Voltage	±12	V	
I _D	Continuous Drain Current	T _A =25°C	0.6	A
I _{DM}	Pulsed Drain Current ¹		1.8	A
P _D	Power Dissipation	T _A =25°C	0.41	W
R _{θJA}	Thermal Resistance-Junction to Ambient ²		300	°C/W
T _J	Operating Junction Temperature Range		-55 to +150	°C
T _{STG}	Storage Temperature Range		-55 to +150	°C

NOTE:

- Single pulse width is limited by max junction temperature.
- The device mounted on 1in² FR-4 board with 2oz. Copper

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
B _V DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30	-	-	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V, V _{GS} =0V	-	-	100	nA
I _{GSS}	Gate-Source Leakage Current	V _{DS} =0V, V _{GS} =±12V	-	-	±10	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.5	-	1.5	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =10V, I _D =0.5A		325	480	mΩ
		V _{GS} =4.5V, I _D =0.4A	-	400	650	
		V _{GS} =2.5V, I _D =0.3A		600	900	
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =0.5A		0.85	-	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	-	39	-	pF
C _{oss}	Output Capacitance		-	9	-	
C _{rss}	Reverse Transfer Capacitance		-	6	-	
Q _g	Total Gate Charge	V _{DS} =15V, I _D =0.5A V _{GS} =10V	-	1.5	-	nC
Q _{gs}	Gate-Source Charge		-	0.2	-	
Q _{gd}	Gate-Drain Charge		-	0.2	-	
t _{d(on)}	Turn-On Delay Time	V _{DD} =15V, I _D =0.5A V _{GS} =4.5V, R _g =6Ω	-	5.3	-	ns
t _r	Turn-On Rise Time		-	16	-	
t _{d(off)}	Turn-Off Delay Time		-	20	-	
t _f	Turn-Off Fall Time		-	18	-	
Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =0.2A	-	-	1.2	V

Typical Performance Characteristics

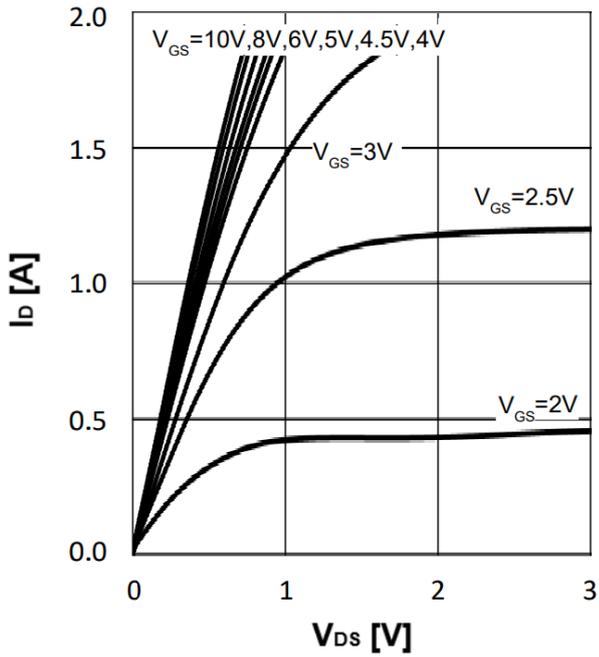


FIG.1 Output Characteristics

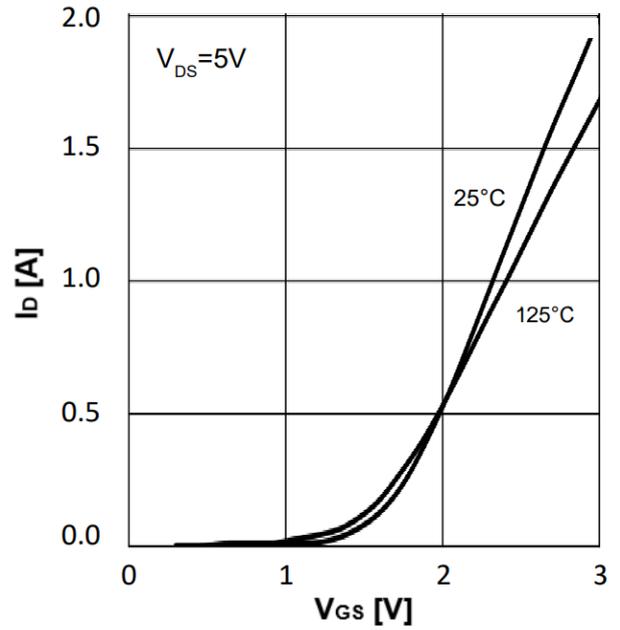


FIG.2 Transfer Characteristics

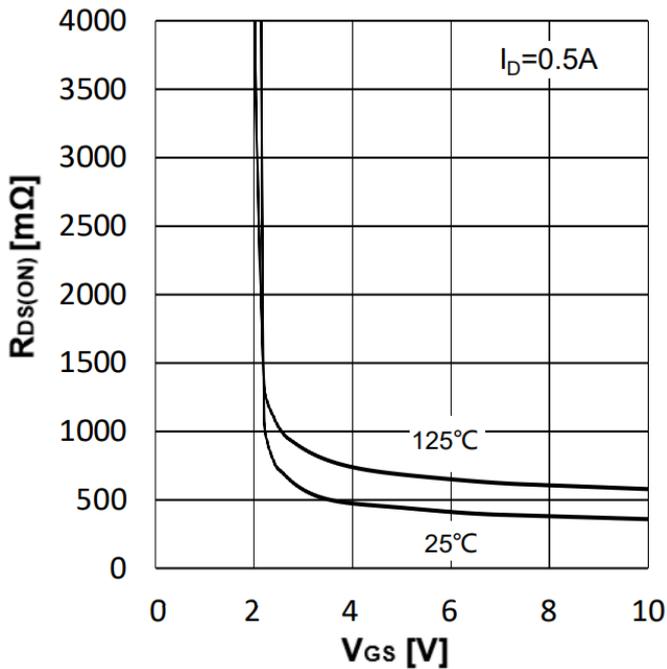


FIG.3 On-Resistance vs. Gate Voltage

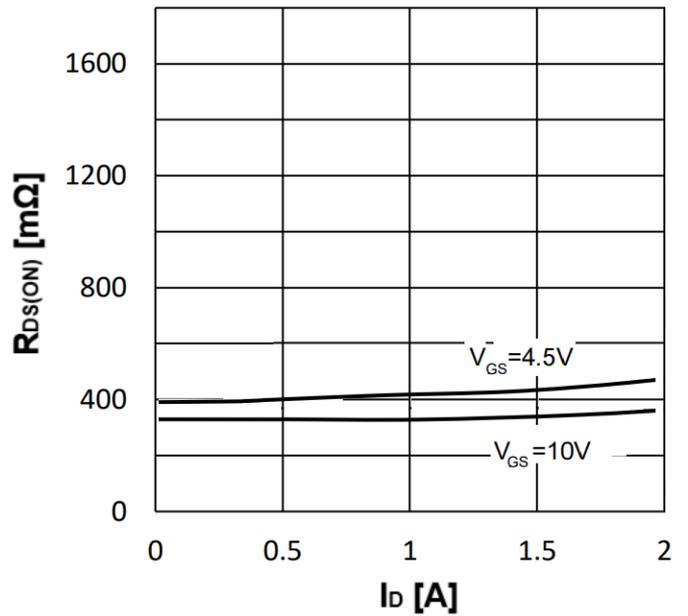


FIG.4 On-Resistance vs. Drain Current

Typical Performance Characteristics

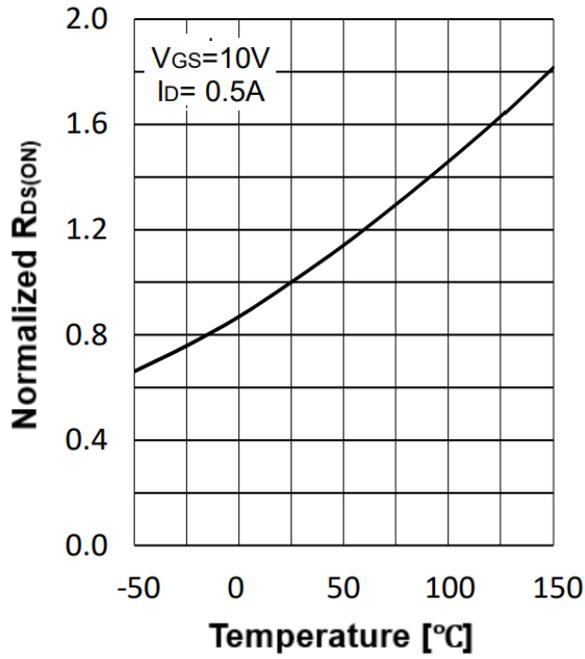


FIG.5 On-Resistance vs. T_J

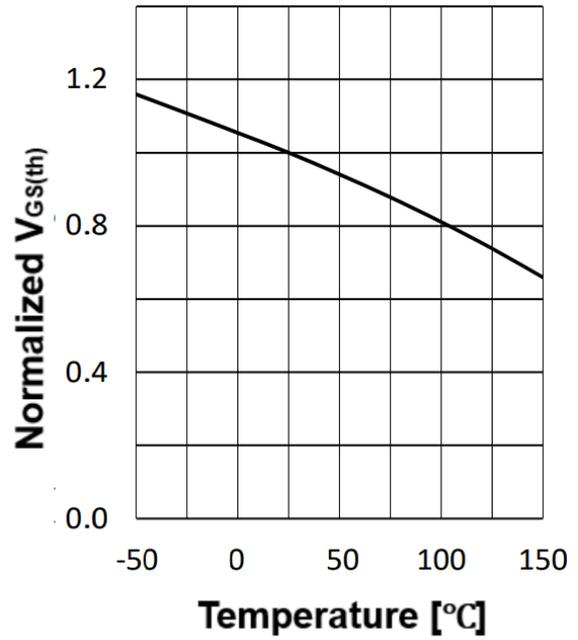


FIG.6 $V_{GS(th)}$ vs. T_J

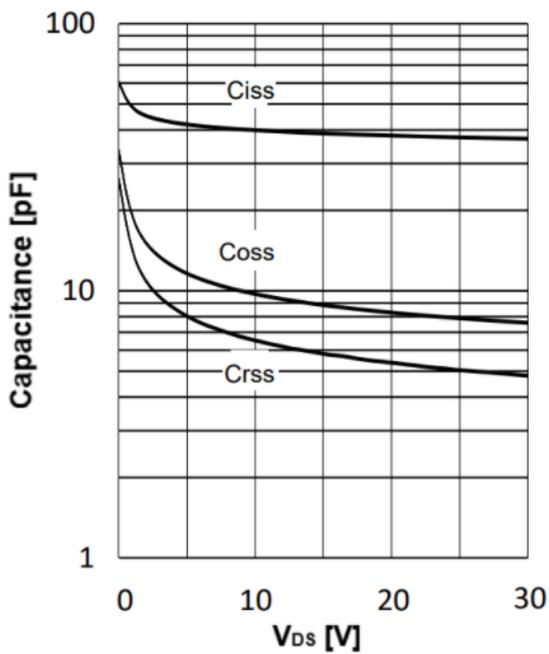


FIG.7 Capacitance Characteristics

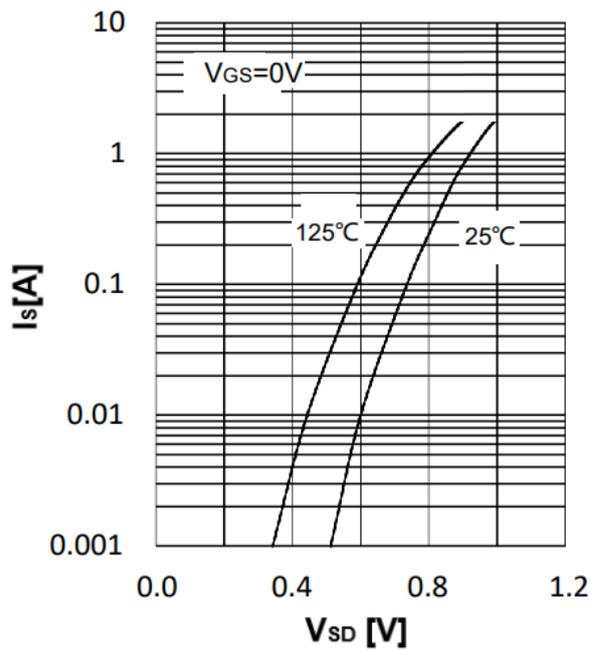
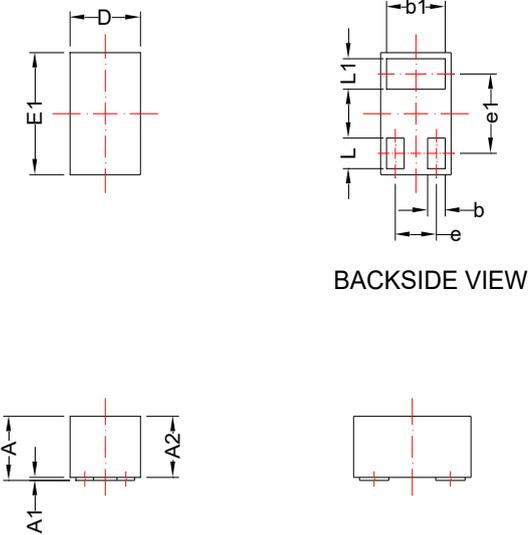


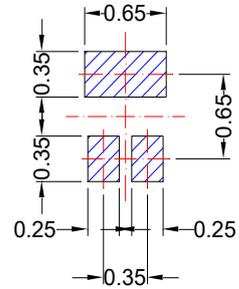
FIG.8 Body-Diode Characteristics

DFN1006-3L

Package Dimension



Recommended Land Pattern



Unit: mm

Dimensions

Symbol	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.45	0.60	0.018	0.024
A1	0.00	0.05	0.000	0.002
A2	0.40	0.60	0.016	0.024
b	0.10	0.20	0.004	0.008
b1	0.45	0.55	0.018	0.022
D	0.55	0.65	0.022	0.026
E1	0.95	1.05	0.037	0.041
e	0.35 BSC		0.014 BSC	
e1	0.65 BSC		0.026 BSC	
L	0.20	0.30	0.008	0.012
L1	0.20	0.30	0.008	0.012

NOTE:

Dimensions are exclusive of Burrs, Mold Flash and Tie Bar extrusions.

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