

GSM2302

20V N-Channel Enhancement Mode MOSFET

Product Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

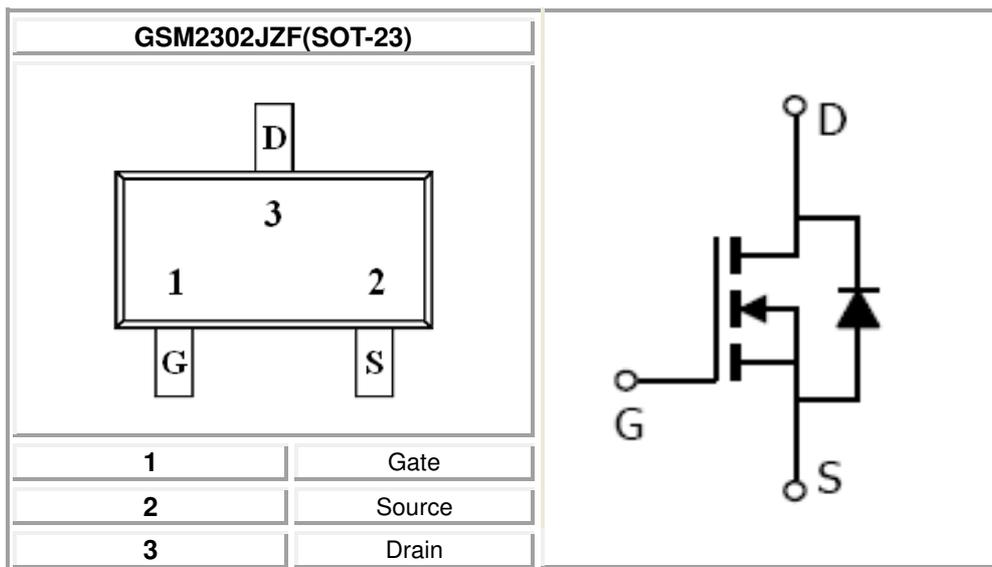
Features

- 20V/4A, $R_{DS(ON)}=65m\Omega@V_{GS}=4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for 1.8V Gate Drive Applications

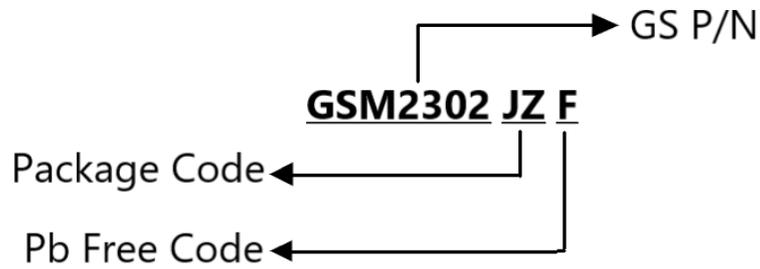
Applications

- Power Management in Notebook
- Hand-Held Instruments
- Load Switch

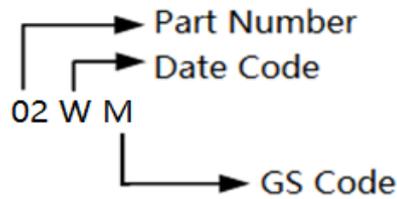
Packages & Pin Assignments



Ordering Information



Marking Information



Part Number	Package	Part Marking
GSM2302JZF	SOT-23	02WM

Absolute Maximum Ratings

(T_C=25°C unless otherwise noted)

Symbol	Parameter	Typical	Unit	
V _{DSS}	Drain-Source Voltage	20	V	
V _{GSS}	Gate –Source Voltage	±10	V	
I _D	Continuous Drain Current(T _J =150°C)	T _C =25°C	4	A
		T _C =100°C	2.5	
I _{DM}	Pulsed Drain Current ¹	16	A	
P _D	Power Dissipation	T _C =25°C	1.56	W
	Power Dissipation – Derate above 25 °C		0.012	W/°C
T _J	Operating Junction Temperature Range	-55 to 150	°C	
T _{STG}	Storage Temperature Range	-55 to 150	°C	
R _{θJA}	Thermal Resistance-Junction to Ambient	80	°C/W	

Electrical Characteristics

(T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ	Max.	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.3	0.5	1.0	V
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±10V			±100	nA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =20V, V _{GS} =0V			1	uA
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =4.5V, I _D =3A		50	65	mΩ
		V _{GS} =2.5V, I _D =2A		60	80	
		V _{GS} =1.8V, I _D =1A		85	120	
g _{FS}	Forward Transconductance	V _{DS} =10V, I _S =2A		4.4		S
V _{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =0V			1	V
I _S	Continuous Source Current	V _G =V _D =0V , Force Current			4	A
I _{SM}	Pulsed Source Current				8	A
Dynamic						
Q _g	Total Gate Charge ^{2,3}	V _{DS} =10V, V _{GS} =4.5V I _D =1A		3.6	7.2	nC
Q _{gs}	Gate-Source Charge ^{2,3}		0.38	0.76		
Q _{gd}	Gate-Drain Charge ^{2,3}		0.6	1.2		
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V f=1MHz		180	360	pF
C _{oss}	Output Capacitance		32	64		
C _{rss}	Reverse Transfer Capacitance		26	52		
t _{d(on)}	Turn-On Time ^{2,3}	V _{DD} =10V, I _D =1.0A, V _{GS} =4.5V, R _G =25Ω		1.8	5	ns
t _r			5.6	12		
t _{d(off)}	Turn-Off Time ^{2,3}		11.3	24		
t _f			3.2	7		

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
3. Essentially independent of operating temperature.

Typical Performance Characteristics

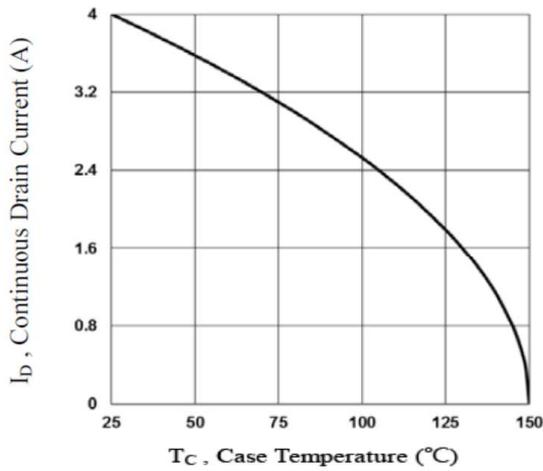


Fig.1 Continuous Drain Current vs. T_C

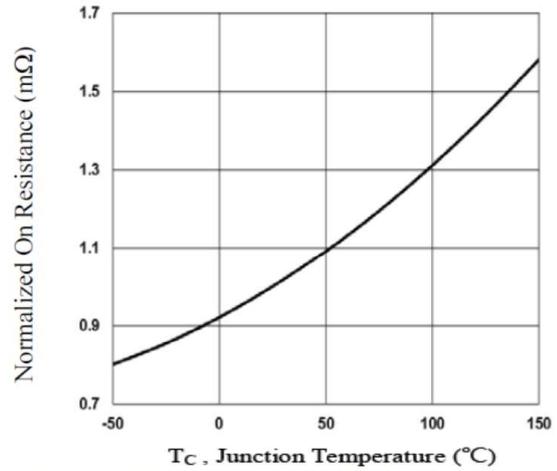


Fig.2 Normalized $R_{DS(on)}$ vs. T_J

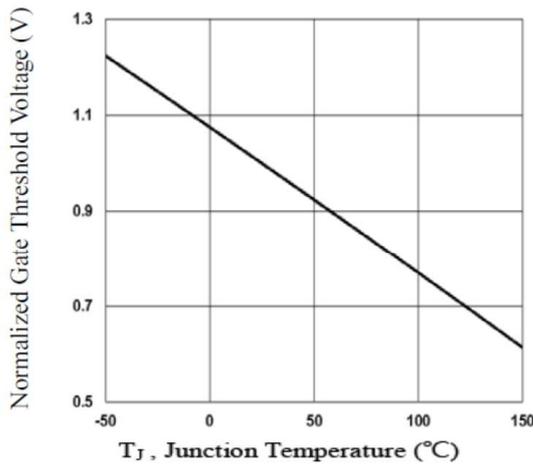


Fig.3 Normalized V_{th} vs. T_J

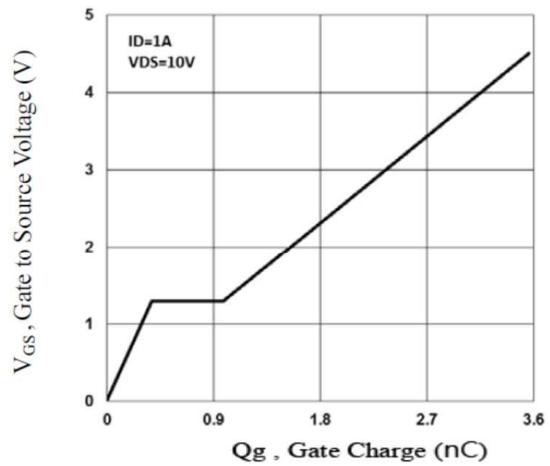


Fig.4 Gate Charge Waveform

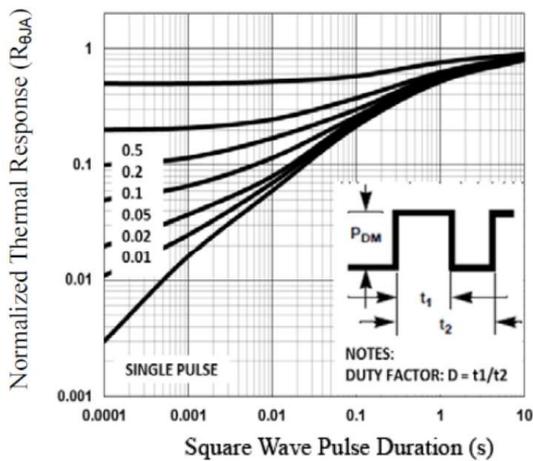


Fig.5 Normalized Transient Impedance

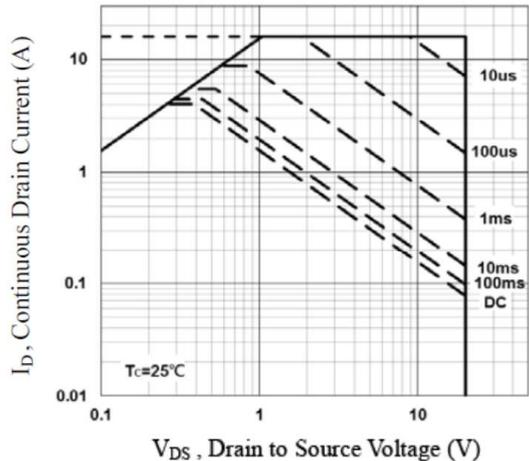


Fig.6 Maximum Safe Operation Area

Typical Characteristics

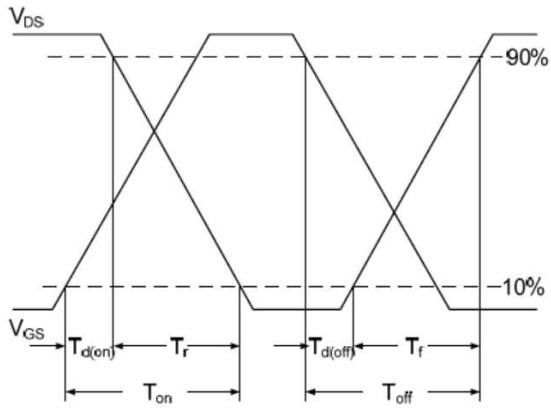


Fig.7 Switching Time Waveform

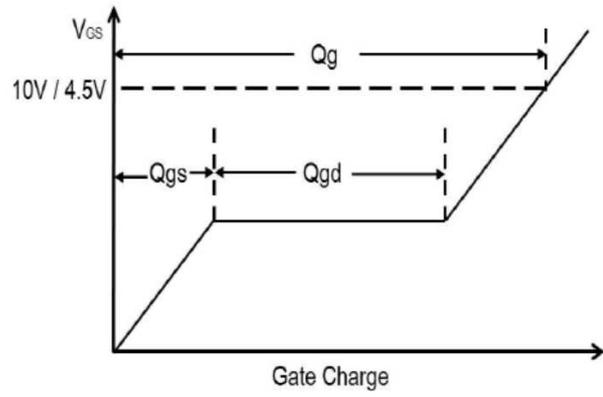
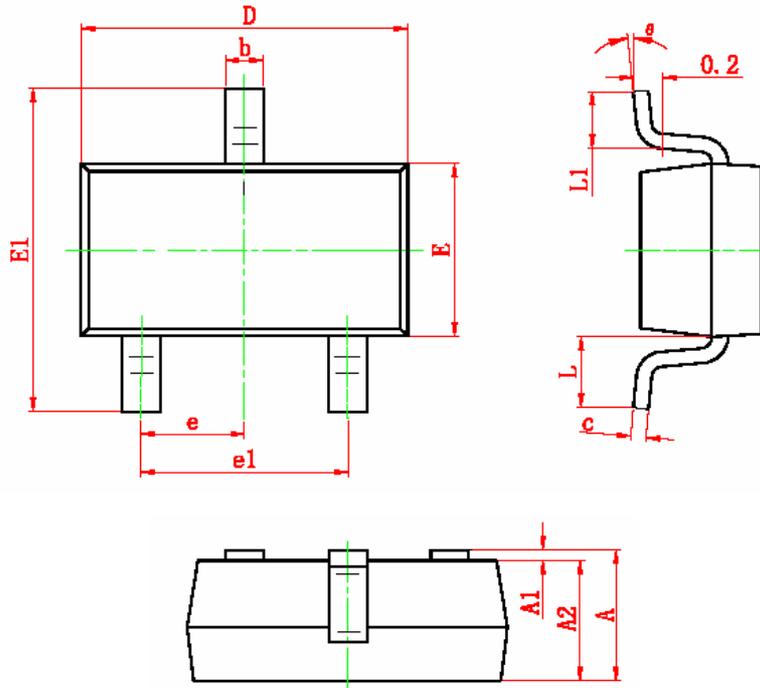


Fig.8 Gate Charge Waveform

Package Dimension

SOT-23



Dimensions				
SYMBOL	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.900	1.200	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.100	0.035	0.039
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

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