

# GSM1362SF

## 100V N-Channel MOSFET

### Product Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

The device is well suited for high efficiency fast switching applications.

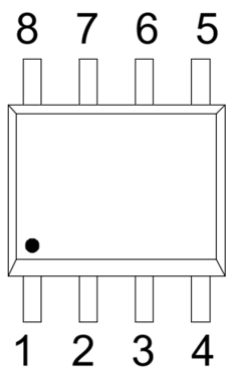
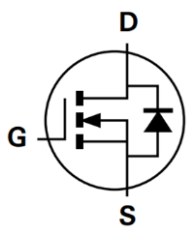
### Features

- $R_{DS(ON)} = 6.4m\Omega @ V_{GS}=10V$
- $R_{DS(ON)} = 9.6m\Omega @ V_{GS}=4.5V$
- SOP-8L Package
- RoHS Compliant and Halogen Free

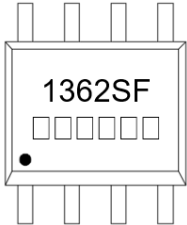
### Applications

- MB / VGA / Vcore
- POL Applications
- SMPS

### Packages & Pin Assignments

SOP-8L			Equivalent Circuit		
					
Pin	Symbol	Description	Pin	Symbol	Description
1	S	Source	8	D	Drain
2	S	Source	7	D	Drain
3	S	Source	6	D	Drain
4	G	Gate	5	D	Drain

## Ordering and Marking Information

Ordering Information			
Part Number	Package	Part Marking	Quantity / Reel
GSM1362SF	SOP-8L	1362SF □□□□□□	4,000 PCS
<b>GSM1362</b> <span style="border: 1px solid black; padding: 0 2px;">1</span> <span style="border: 1px solid black; padding: 0 2px;">2</span>			
<div> <div>- <b>Product Code:</b> GSM1362</div> <div>- <b>Package Code:</b> <span style="border: 1px solid black; padding: 0 2px;">1</span> is <b>S</b> for SOP-8L</div> <div>- <b>Green Level:</b> <span style="border: 1px solid black; padding: 0 2px;">2</span> is <b>F</b> for RoHS Compliant and Halogen Free</div> </div>			
Marking Information			
<div>  <div> <div>- <b>Product Code:</b> 1362SF</div> <div>- <b>GS Code:</b> □□□□□□</div> <div>•The dot indicates pin1</div> </div> </div>			

## Absolute Maximum Ratings (T<sub>A</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit
V <sub>DSS</sub>	Drain-Source Voltage	100	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current	T <sub>A</sub> =25°C	A
		T <sub>A</sub> =70°C	
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	56	A
I <sub>AS</sub>	Single Pulse Avalanche Current, L = 0.1mH <sup>1</sup>	25	A
E <sub>AS</sub>	Single Pulse Avalanche Energy, L = 0.1mH <sup>1</sup>	62.5	mJ
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> =25°C	W
		T <sub>A</sub> =70°C	
R <sub>θJA</sub>	Thermal Resistance-Junction to Ambient <sup>2</sup>	75	°C/W
T <sub>J</sub>	Operating Junction Temperature Range	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

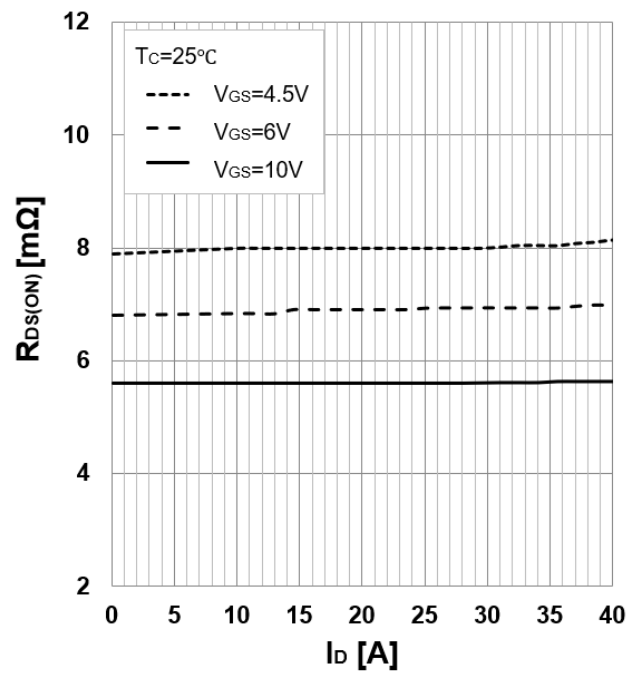
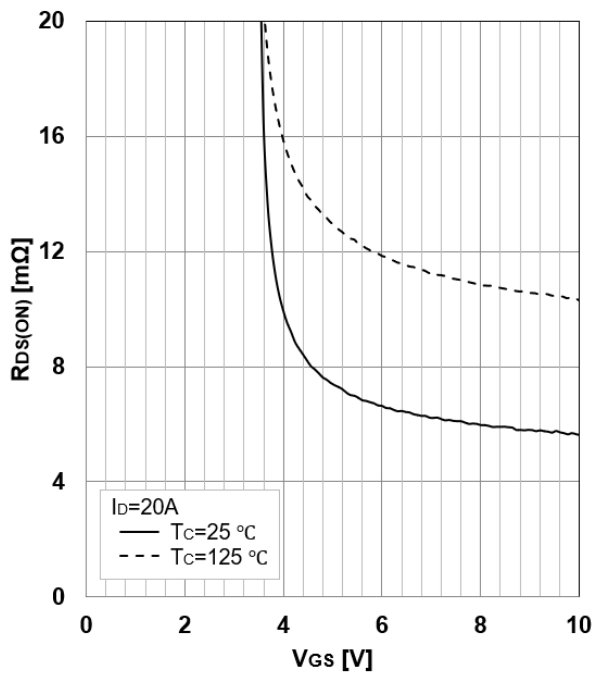
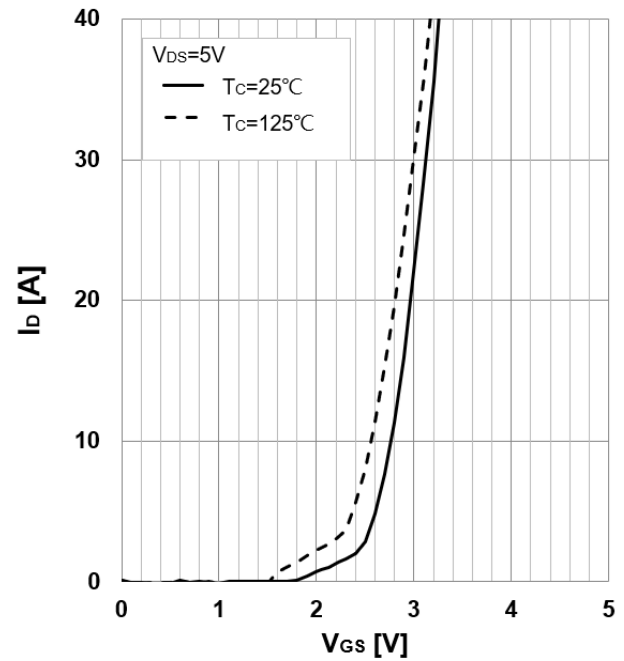
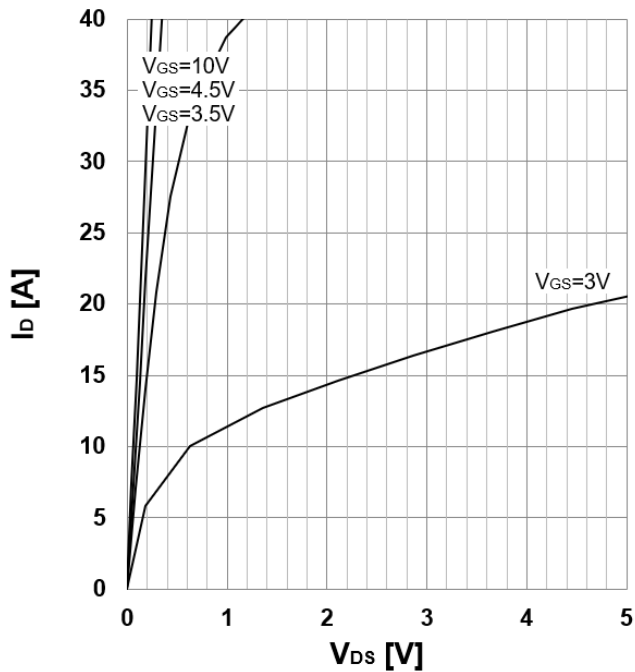
### NOTE:

- Single pulse width is limited by max junction temperature.
- The device was mounted on 1in<sup>2</sup> FR-4 board with 2oz.copper.

## Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100	-	-	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	-	-	1	μA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.2	-	2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =15A	-	5.6	6.4	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	8	9.6	
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =30A	-	30	-	S
Dynamic Characteristics						
R <sub>g</sub>	Gate Resistance	f=1MHz		1		Ω
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz	-	2340	-	pF
C <sub>OSS</sub>	Output Capacitance		-	455	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	22	-	
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =50V, I <sub>D</sub> =40A V <sub>GS</sub> =10V	-	54	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	8	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	20	-	
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =50V, I <sub>D</sub> =40A V <sub>GS</sub> =10V, R <sub>g</sub> =3Ω	-	20	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	10	-	
t <sub>d(off)</sub>	Turn-Off Delay Time		-	31	-	
t <sub>f</sub>	Turn-Off Fall Time		-	7	-	
Diode Characteristics						
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A	-	-	1	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>R</sub> =50V, I <sub>F</sub> =40A , dI/dt=100A/μs	-	58	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	115	-	nC

## Typical Performance Characteristics



## Typical Performance Characteristics

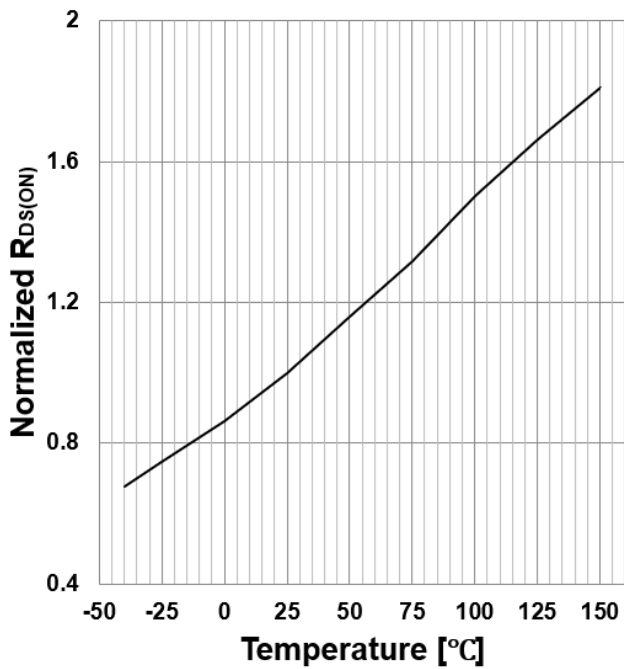


FIG.5 Normalized On-Resistance vs.  $T_J$

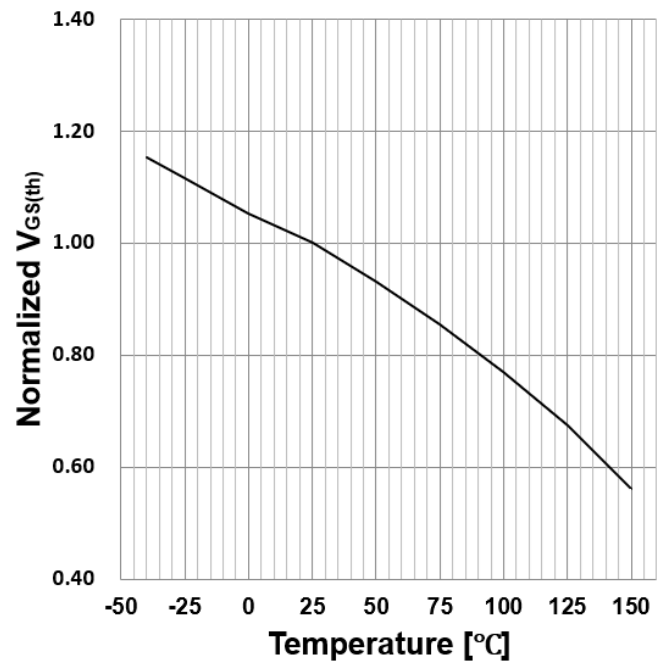


FIG.6 Normalized  $V_{GS(th)}$  vs.  $T_J$

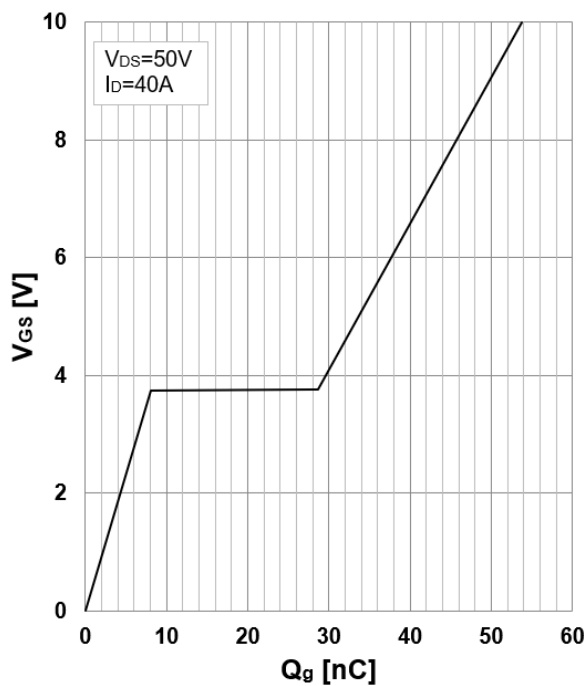


FIG.7 Gate Charge Characteristics

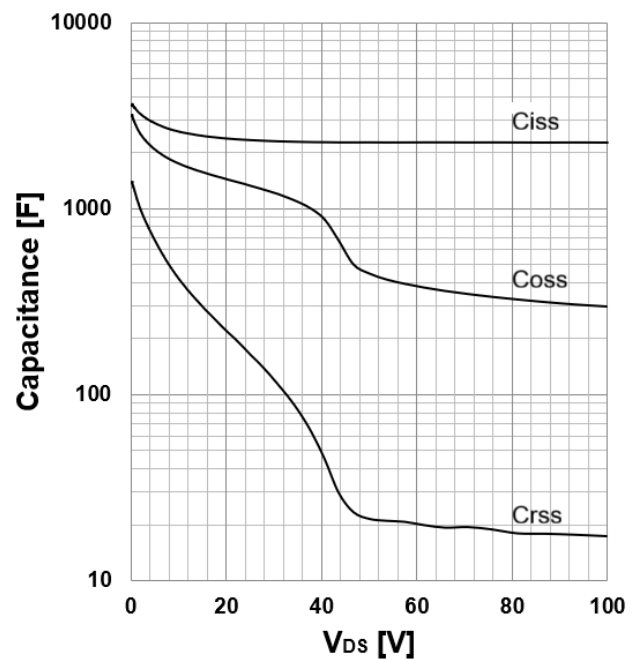
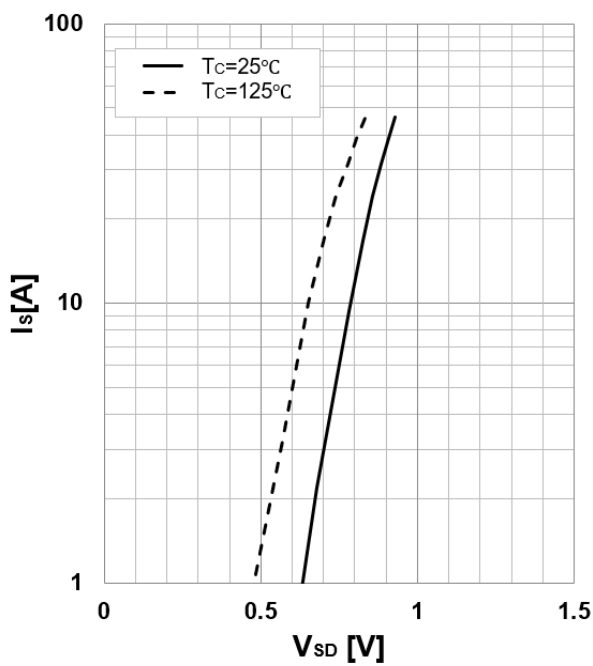
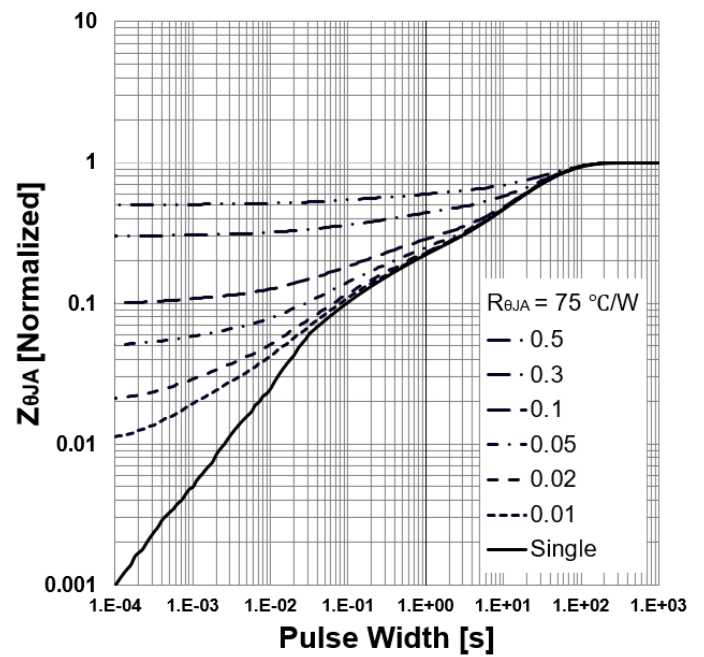
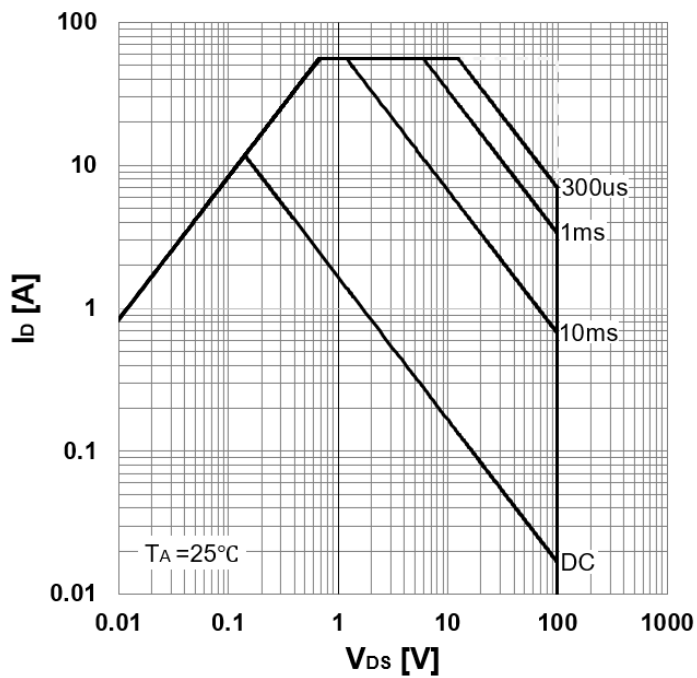


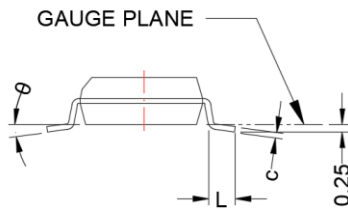
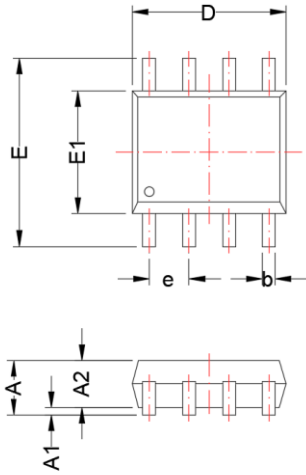
FIG.8 Capacitance Characteristics

## Typical Performance Characteristics

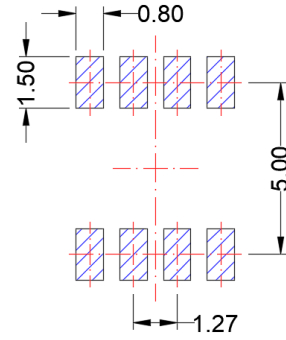


# SOP-8L

## Package Dimension



## Recommended Land Pattern



Unit:mm

Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	---	1.75	---	0.069
A1	0.10	0.25	0.004	0.010
A2	1.25	---	0.049	---
b	0.31	0.51	0.012	0.020
c	0.10	0.25	0.004	0.010
D	4.70	5.10	0.185	0.201
E	5.80	6.20	0.228	0.244
E1	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
L	0.40	1.27	0.016	0.050
θ	0°	8°	0°	8°





### NOTE:



Dimensions are exclusive of Burrs, Mold Flash and Tie Bar extrusions.

## NOTICE

- Globaltech Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Globaltech Semiconductor products described or contained herein. Globaltech Semiconductor products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Globaltech Semiconductor makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- Information furnished is believed to be accurate and reliable. However Globaltech Semiconductor assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Globaltech Semiconductor. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information without express written approval of Globaltech Semiconductor.

## CONTACT US

GS Headquarter	
	4F, NO.43-1, Lane 11, Sec. 6, Minquan E. Rd Neihs District, Taipei City 114761, Taiwan (R.O.C).
	886-2-2657-9980
	886-2-2657-3630
	<a href="mailto:sales_twn@gs-power.com">sales_twn@gs-power.com</a>

RD Division	
	824 Bolton Drive Milpitas. CA. 95035
	1-408-457-0587