

GSM0988Z

100V N-Channel MOSFETs

Product Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

Features

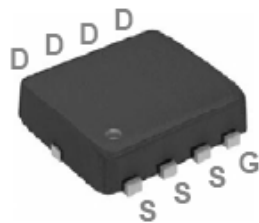
- 100V, 16A, $R_{DS(ON)}=38m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS guaranteed
- Green Device Available
- DFN3X3-8L package design

Applications

- Motor Drive
- Power Tools
- LED Lighting
- Quick Charger

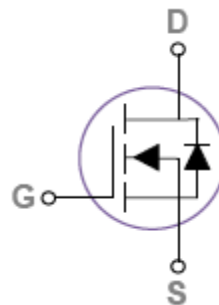
Packages & Pin Assignments

GSM0988ZFF (DFN3X3-8L)

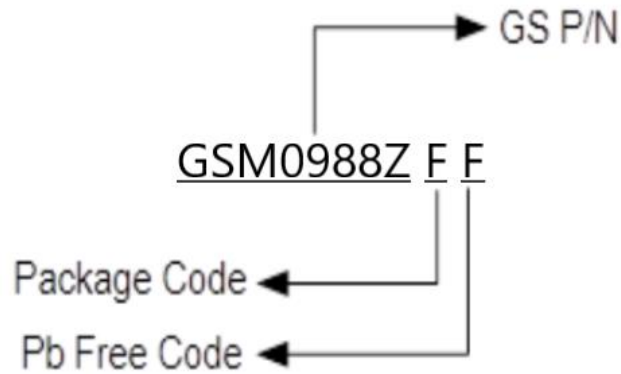


Top View

Pin	Description
1	Source
2	Source
3	Source
4	Gate
5	Drain
6	Drain
7	Drain
8	Drain

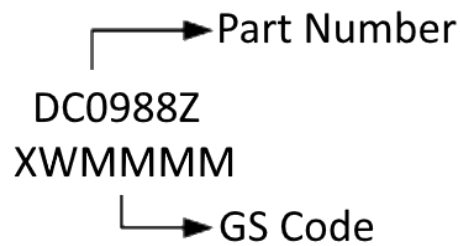


Ordering Information



Part Number	Package	Quantity
GSM0988ZFF	DFN3X3-8L	5000 PCS

Marking Information



Absolute Maximum Ratings

$T_C=25^{\circ}\text{C}$ Unless otherwise noted

Symbol	Parameter	Typical	Unit
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_C=25^{\circ}\text{C}$	16
		$T_C=100^{\circ}\text{C}$	10
I_{DM}	Pulsed Drain Current (Note 1)	64	A
EAS	Single Pulse Avalanche Energy (Note 2)	34	mJ
IAS	Single Pulse Avalanche Current (Note 2)	26	A
P_D	Power Dissipation ($T_C=25^{\circ}\text{C}$)	32.5	W
	Power Dissipation (Derate above 25°C)	0.26	W/ $^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to +150	$^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to +150	$^{\circ}\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	3.85	$^{\circ}\text{C}/\text{W}$

Electrical Characteristics

$T_J=25^{\circ}\text{C}$ Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.6	2.5	V
I_{GSS}	Gate-Source Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=100V, V_{GS}=0V$			1	uA
		$V_{DS}=80V, V_{GS}=0V, T_J=125^{\circ}\text{C}$			10	
I_S	Continuous Source Current	$V_G=V_D=0V,$ Force Current			16	A
I_{SM}	Pulsed Source Current				32	
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V, I_D=10A$		32	38	m Ω
		$V_{GS}=4.5V, I_D=8A$		49	63	
g_{FS}	Forward Transconductance	$V_{DS}=10V, I_D=3A$		5		S
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=1A$			1	V
t_{rr}	Reverse Recovery Time (Note 3,4)	$V_{GS}=0V, I_S=10A,$ $di/dt=100A/\mu s$		30		ns
Q_{rr}	Reverse Recovery Charge (Note 3,4)			24		nC
Dynamic						
Q_g	Total Gate Charge (Note 3,4)	$V_{DS}=50V, V_{GS}=10V,$ $I_D=10A$		8	12	nC
Q_{gs}	Gate-Source Charge (Note 3,4)			2.1	3.5	
Q_{gd}	Gate-Drain Charge (Note 3,4)			2.3	4	
C_{iss}	Input Capacitance	$V_{DS}=20V, V_{GS}=0V,$ $f=1\text{MHz}$		553	740	pF
C_{oss}	Output Capacitance			181	460	
C_{rss}	Reverse Transfer Capacitance			30	20	
$t_{d(on)}$	Turn-On Time (Note 3,4)	$V_{DD}=50V, I_D=1A,$ $V_{GS}=10V, R_G=3.3\Omega$		7.4	15	ns
t_r				12	24	
$t_{d(off)}$	Turn-Off Time (Note 3,4)			23	46	
t_f				16	32	
R_g	Gate Resistance		$V_{DS}=0V, V_{GS}=0V,$ $f=1\text{MHz}$		0.8	

Note 1: Repetitive Rating: Pulsed width limited by maximum junction temperature.

Note 2: $V_{DD}=50V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=26A, R_G=25\Omega,$ Starting $T_J=25^{\circ}\text{C}$.

Note 3: The data tested by pulsed, pulse width $\leq 300\mu s,$ duty cycle $\leq 2\%$.

Note 4: Essentially independent of operating temperature.

Typical Performance Characteristics

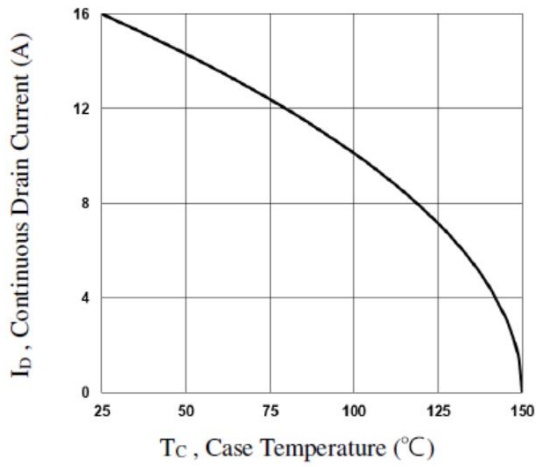


Fig.1 Continuous Drain Current vs. T_c

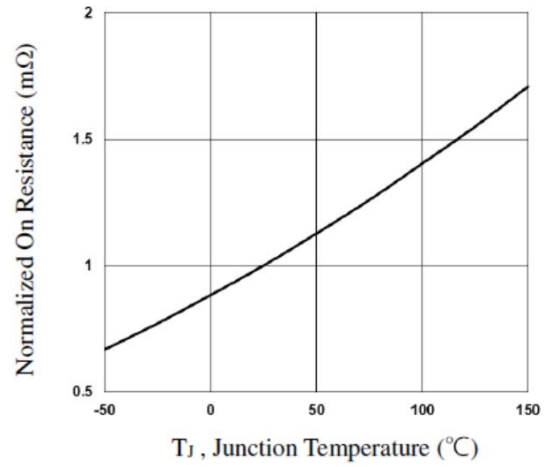


Fig.2 Normalized RDSON vs. T_j

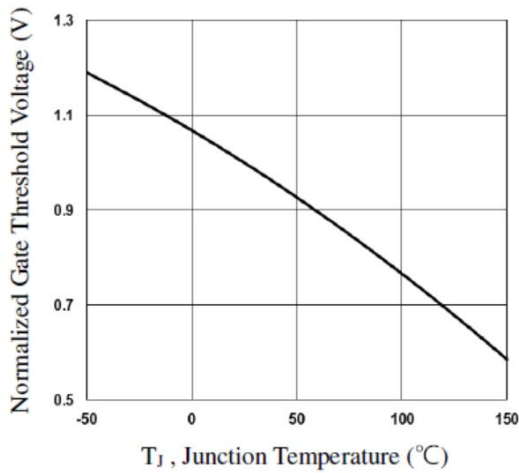


Fig.3 Normalized V_{th} vs. T_j

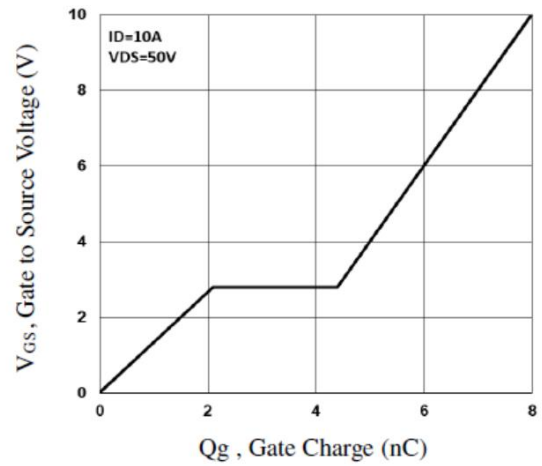


Fig.4 Gate Charge Waveform

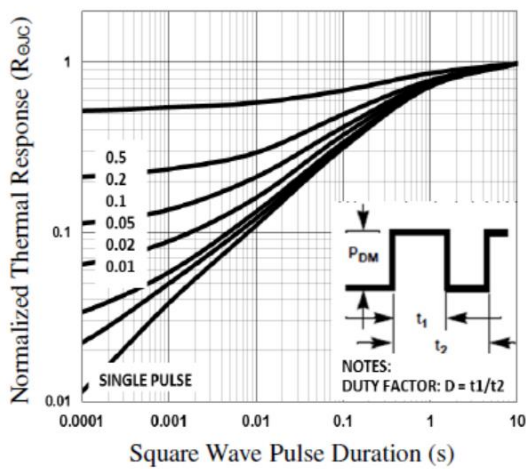


Fig.5 Normalized Transient Response

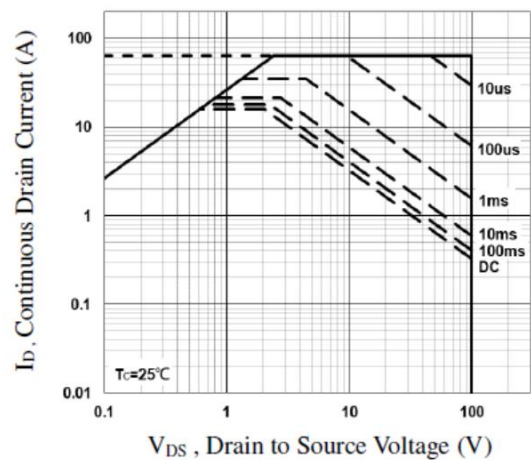


Fig.6 Maximum Safe Operation Area

Typical Performance Characteristics (Continue)

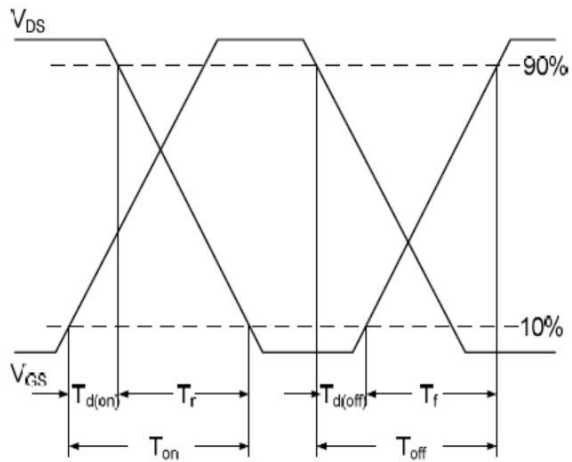


Fig.7 Switching Time Waveform

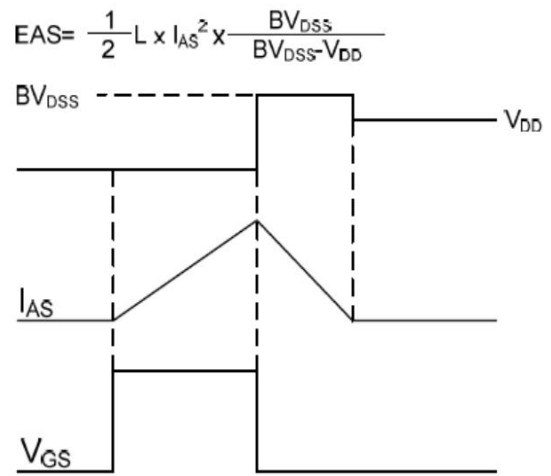
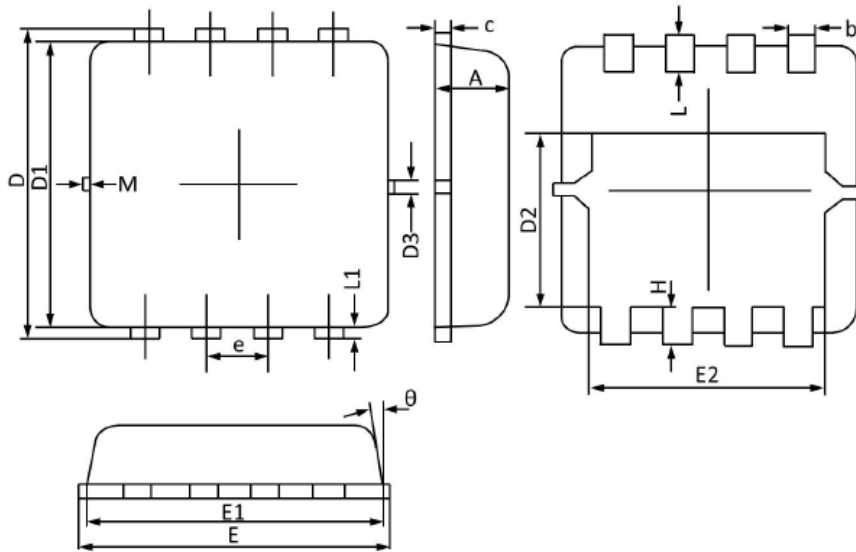


Fig.8 EAS Waveform

Package Dimension

DFN3X3-8L







Dimensions



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
b	0.250	0.350	0.010	0.013
c	0.100	0.250	0.004	0.009
D	3.250	3.450	0.128	0.135
D1	3.000	3.200	0.119	0.125
D2	1.780	1.980	0.070	0.077
D3	0.130 (REF)		0.005 (REF)	
E	3.200	3.400	0.126	0.133
E1	3.000	3.200	0.119	0.125
E2	2.390	2.590	0.094	0.102
e	0.650 (BSC)		0.026 (BSC)	
H	0.300	0.500	0.011	0.019
L	0.300	0.500	0.011	0.019
L1	0.130 (REF)		0.005 (REF)	
θ	0°	12°	0°	12°
M	0.150 (REF)		0.006 (REF)	

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