

# GSM0905D

## 100V P-Channel MOSFETs

### Product Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

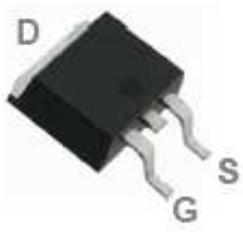
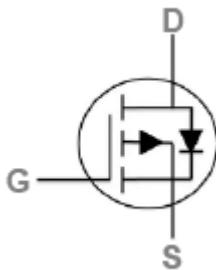
### Features

- -100V, -18A,  $R_{DS(ON)}=95m\Omega @ V_{GS}=-10V$
- $V_{GS}$  Guaranteed  $\pm 25V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- TO-252-2L package design

### Applications

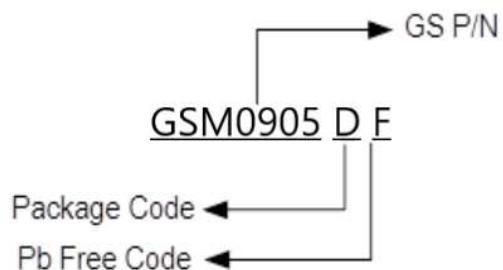
- Networking
- Load Switch
- LED applications

### Packages & Pin Assignments

| GSM0905DF (TO-252-2L)   |          |
|---|----------|
|    | Top View |
|  |          |
| Description   |          |
| Gate  |          |
| Source  |          |
| Drain   |          |

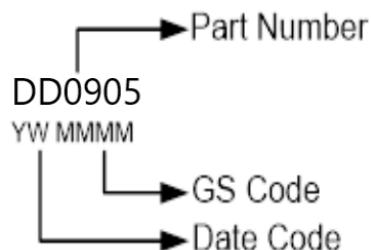
GSM0905D

## Ordering Information



| Part Number | Package   | Quantity Reel |
|-------------|-----------|---------------|
| GSM0905DF   | TO-252-2L | 2500 PCS      |

## Marking Information



## Absolute Maximum Ratings

T<sub>C</sub>=25°C Unless otherwise noted

| Symbol           | Parameter                                | Typical   | Unit         |
|------------------|--|---|--------------|
| V <sub>DS</sub>  | Drain-Source Voltage                     | -100  | V            |
| V <sub>GS</sub>  | Gate -Source Voltage                     | ±25   | V            |
| I <sub>D</sub>   | Continuous Drain Current                 | $T_C=25^\circ\text{C}$<br>$T_C=100^\circ\text{C}$ | -18<br>-11.4 |
| I <sub>DM</sub>  | Pulsed Drain Current <sup>1</sup>        | -72   | A            |
| P <sub>D</sub>   | Power Dissipation (T <sub>C</sub> =25°C) | 73.5  | W            |
|                  | Power Dissipation (Derate above 25°C)    | 0.59  | W/°C         |
| T <sub>J</sub>   | Operating Junction Temperature Range     | -55 to +150                                       | °C           |
| T <sub>STG</sub> | Storage Temperature Range                | -55 to +150                                       | °C           |
| R <sub>θJA</sub> | Thermal Resistance-Junction to Ambient   | 62  | °C/W         |
| R <sub>θJC</sub> | Thermal Resistance-Junction to Case      | 1.7   | °C/W         |

GSM0905D

## Electrical Characteristics

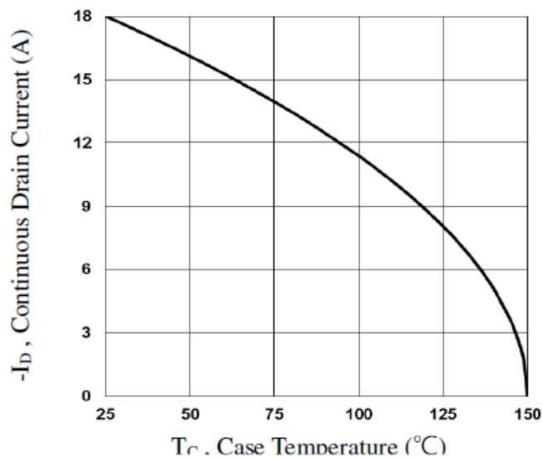
T<sub>J</sub>=25°C Unless otherwise noted

| Symbol               | Parameter                         | Conditions   | Min  | Typ  | Max  | Unit |
|----------------------|-----------------------------------|--|------|------|------|------|
| <b>Static</b>        |                                   |  |      |      |      |      |
| V <sub>(BR)DSS</sub> | Drain-Source Breakdown Voltage    | V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA  | -100 |      |      | V    |
| V <sub>GS(th)</sub>  | Gate Threshold Voltage            | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA                                | -1.2 | -1.6 | -2.2 | V    |
| I <sub>GSS</sub>     | Gate-Source Leakage Current       | V <sub>DS</sub> =0V, V <sub>GS</sub> =±25V   |      |      | ±100 | nA   |
| I <sub>DSS</sub>     | Drain-Source Leakage Current      | V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V  |      |      | -1   |      |
|                      |                                   | V <sub>DS</sub> =-80V, V <sub>GS</sub> =0V,<br>T <sub>J</sub> =125°C                     |      |      | -10  | uA   |
| I <sub>S</sub>       | Continuous Source Current         | V <sub>G</sub> =V <sub>D</sub> =0V,<br>Force Current                                     |      |      | -18  | A    |
| I <sub>SM</sub>      | Pulsed Source Current             |  |      |      | -36  |      |
| R <sub>DS(on)</sub>  | Drain-Source On-Resistance        | V <sub>GS</sub> = -10V, I <sub>D</sub> =-6A  |      | 75   | 95   | mΩ   |
|                      |                                   | V <sub>GS</sub> = -4.5V, I <sub>D</sub> =-3A   |      | 80   | 110  | mΩ   |
| V <sub>SD</sub>      | Diode Forward Voltage             | V <sub>GS</sub> =0V, I <sub>S</sub> =-1A   |      |      | -1.2 | V    |
| <b>Dynamic</b>       |                                   |  |      |      |      |      |
| Q <sub>g</sub>       | Total Gate Charge <sup>2,3</sup>  | V <sub>DS</sub> =-50V, V <sub>GS</sub> =-10V,<br>I <sub>D</sub> =-6A                     |      | 40.4 | 70   |      |
| Q <sub>gs</sub>      | Gate-Source Charge <sup>2,3</sup> |  |      | 7.7  | 15   | nC   |
| Q <sub>gd</sub>      | Gate-Drain Charge <sup>2,3</sup>  |  |      | 6.6  | 13   |      |
| C <sub>iss</sub>     | Input Capacitance                 | V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V,<br>f=1MHz                                    |      | 2250 | 3900 |      |
| C <sub>oss</sub>     | Output Capacitance                |  |      | 130  | 250  | pF   |
| C <sub>rss</sub>     | Reverse Transfer Capacitance      |  |      | 90   | 180  |      |
| t <sub>d(on)</sub>   | Turn-On Time <sup>2,3</sup>       | V <sub>DD</sub> =-30V, I <sub>D</sub> =-1A,<br>V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω |      | 27   | 54   |      |
| t <sub>r</sub>       |                                   |  |      | 12   | 24   | ns   |
| t <sub>d(off)</sub>  | Turn-Off Time <sup>2,3</sup>      |  |      | 150  | 300  |      |
| t <sub>f</sub>       |                                   |  |      | 45   | 90   |      |
| R <sub>g</sub>       | Gate Resistance                   | V <sub>DS</sub> =0V, V <sub>GS</sub> =0V,<br>f=1MHz                                      |      | 10   |      | Ω    |

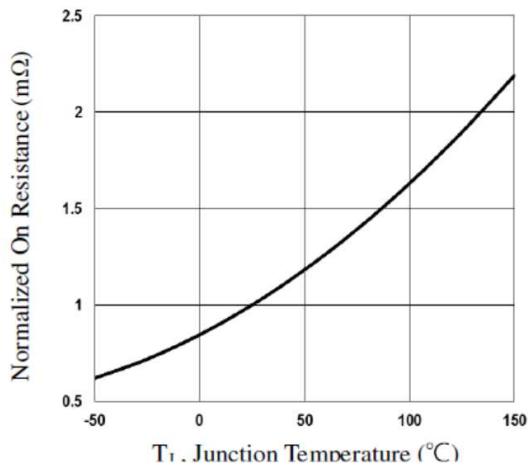
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
3. Essentially independent of operating temperature.

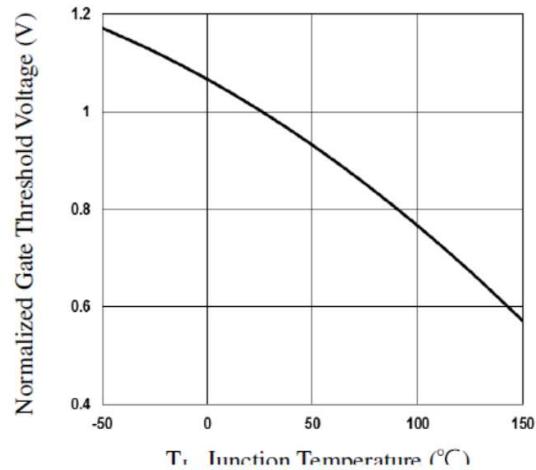
## Typical Performance Characteristics



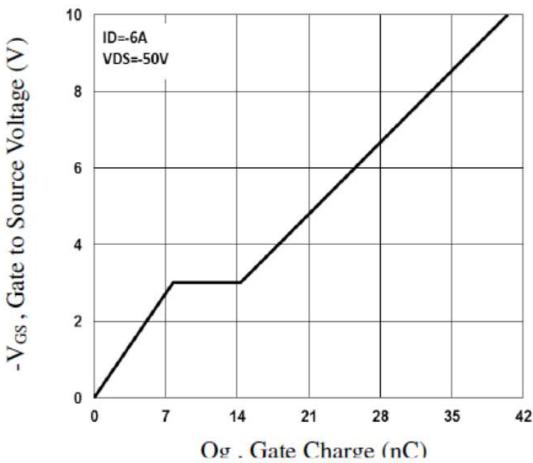
**Fig.1 Continuous Drain Current vs.  $T_c$**



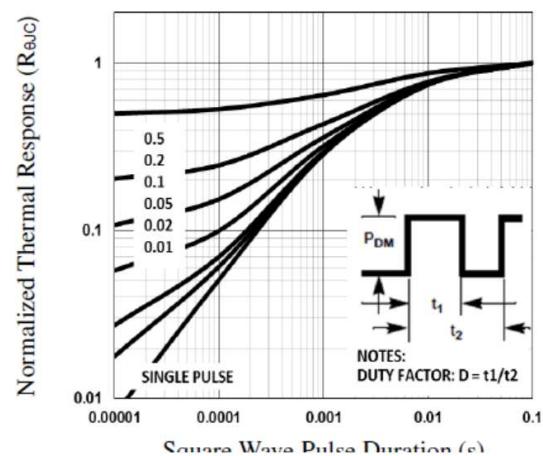
**Fig.2 Normalized RDSON vs.  $T_j$**



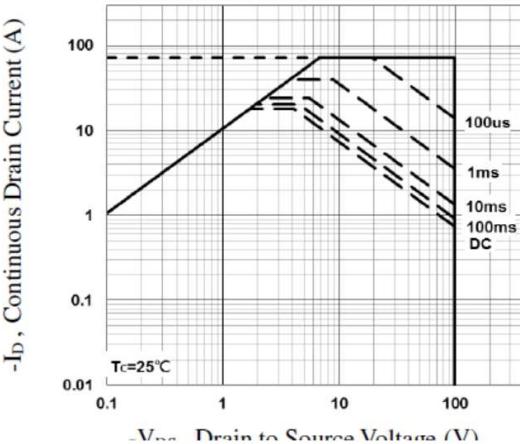
**Fig.3 Normalized  $V_{th}$  vs.  $T_j$**



**Fig.4 Gate Charge Waveform**



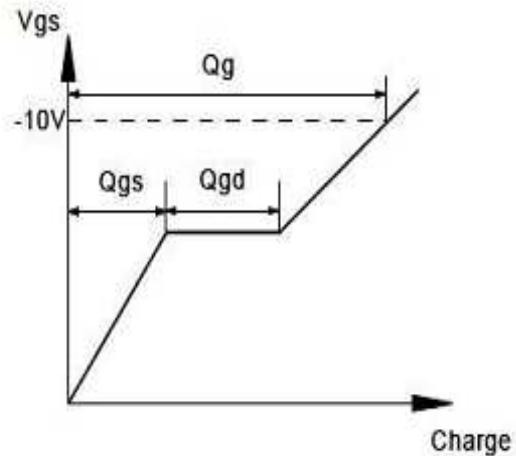
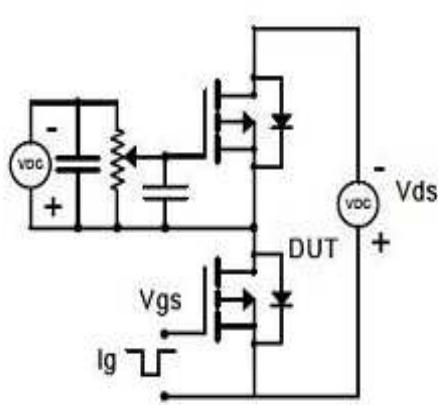
**Fig.5 Normalized Transient Impedance**



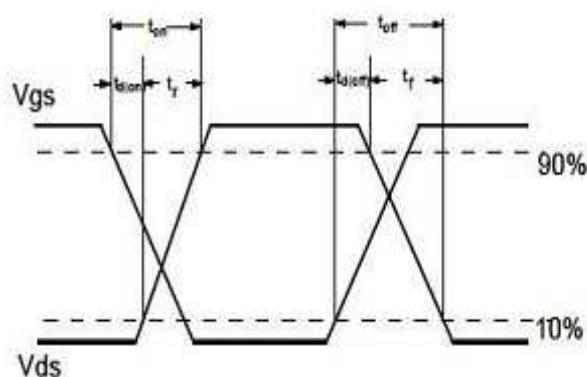
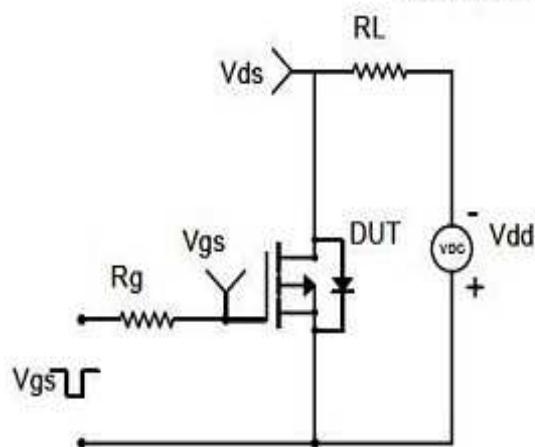
**Fig.6 Maximum Safe Operation Area**

## Typical Performance Characteristics (Continue)

Gate Charge Test Circuit & Waveform

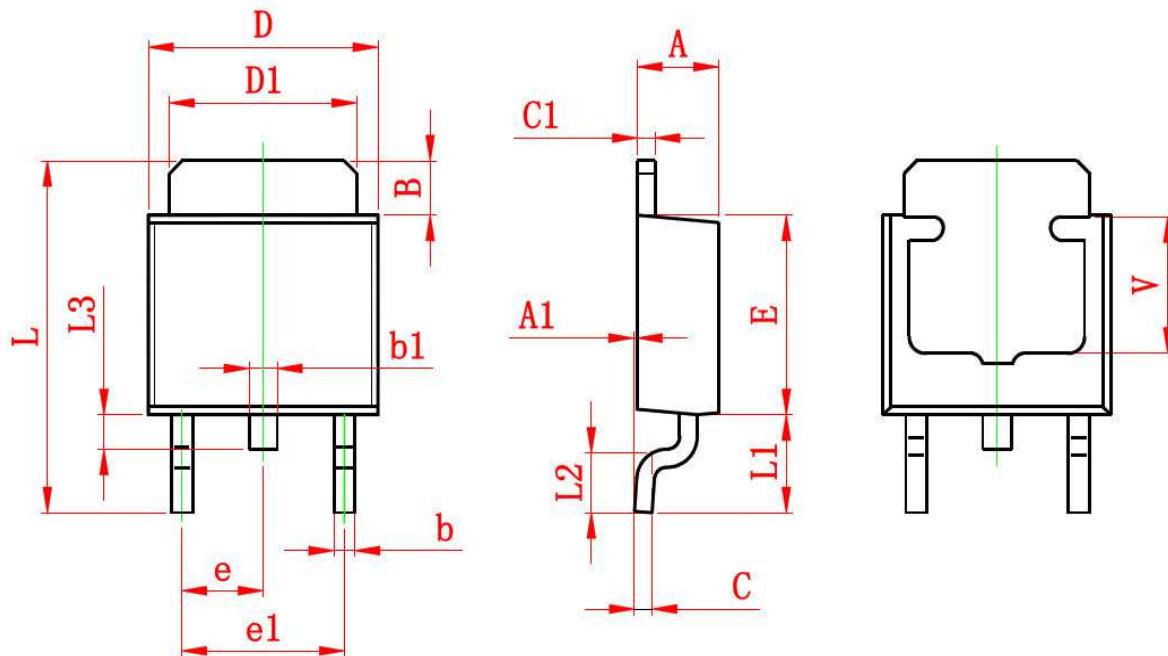


Resistive Switching Test Circuit & Waveforms



## Package Dimension

### TO-252-2L



### Dimensions

| SYMBOL | Millimeters |       | Inches    |       |
|--------|-------------|-------|-----------|-------|
|        | MIN         | MAX   | MIN       | MAX   |
| A      | 2.200       | 2.400 | 0.087     | 0.094 |
| A1     | 0.000       | 0.127 | 0.000     | 0.005 |
| B      | 1.350       | 1.650 | 0.053     | 0.065 |
| b      | 0.500       | 0.700 | 0.020     | 0.028 |
| b1     | 0.700       | 0.900 | 0.028     | 0.035 |
| c      | 0.430       | 0.580 | 0.017     | 0.023 |
| c1     | 0.430       | 0.580 | 0.017     | 0.023 |
| D      | 6.350       | 6.650 | 0.250     | 0.262 |
| D1     | 5.200       | 5.400 | 0.205     | 0.213 |
| E      | 5.400       | 5.700 | 0.213     | 0.224 |
| e      | 2.300 TYP   |       | 0.091 TYP |       |
| e1     | 4.500       | 4.700 | 0.177     | 0.185 |
| L      | 9.500       | 9.900 | 0.374     | 0.390 |
| L1     | 2.550       | 2.900 | 0.100     | 0.114 |
| L2     | 1.400       | 1.780 | 0.055     | 0.070 |
| L3     | 0.600       | 0.900 | 0.024     | 0.035 |
| V      | 3.800 REF   |       | 0.150 REF |       |

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